Three Heads Are Better Than One: Mastering NSA's Ghidra Reverse Engineering Tool

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github.com/0xAlexei/INFILTRATE2019

Disclaimer

This material is based on the publicly released Ghidra, there is no classified information in this presentation

Alexei Bulazel @0xAlexei

Senior Security Researcher at River Loop Security

- Research presentations and publications:
 - Presentations at REcon (MTL & BRX), SummerCon, DEFCON, Black Hat, etc.
 - Academic publications at USENIX WOOT and ROOTS
 - Cyber policy in Lawfare, etc.
- Collaborated with Jeremy on research at RPI, MIT Lincoln Laboratory, and Boston Cybernetics Institute
- Proud RPISEC alumnus



Jeremy Blackthorne @0xJeremy

- Instructor at the Boston Cybernetics Institute
- PhD candidate at RPI focused on environmental keying
- Former researcher at MIT Lincoln Laboratory
- United States Marine Corps 2002 2006
- RPISEC alumnus











Outline

- 1. Intro
- 2. Interactive Exercises
 - a. Manual Static Analysis
 - **b.** Scripting Ghidra
- 3. P-Code & SLEIGH
- 4. Discussion
- 5. Conclusion

Participating

- 1. Install OpenJDK 11, add its bin directory to your PATH
 - jdk.java.net/11
- 2. Download Ghidra
 - ghidra-sre.org
 - github.com/NationalSecurityAgency/ghidra/releases
- 3. Download our demo scripts and binaries
 - github.com/0xAlexei/INFILTRATE2019

Ghidra

- Java-based interactive reverse engineering tool developed by US National Security Agency - similar in functionality to IDA Pro, Binary Ninja, etc...
 - O Static analysis only currently, debugger support promised to be coming soon
 - O Runs on Mac, Linux, and Windows
- All credit for creating Ghidra goes to the developers at NSA
- Released open source at RSA in March 2019
 - o 1.2M+ lines of code
- NSA has not discussed the history of the tool, but comments in source files go as far back as February 1999

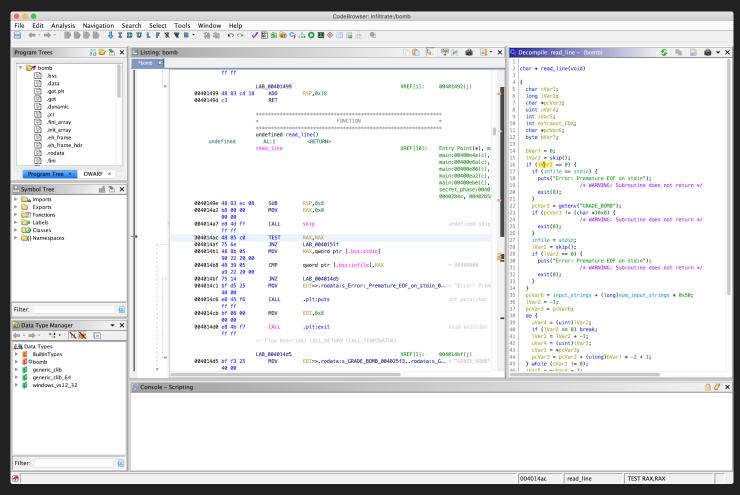


src/Generic-src/ghidra/util/exception/NotYetImplementedException.java: * @version 1999/02/05
src/SoftwareModeling-src/ghidra/program/model/address/AddressOutOfBoundsException.java: * @version 1999-03-31
src/SoftwareModeling-src/ghidra/program/model/scalar/ScalarOverflowException.java: * @version 1999-03-31
src/SoftwareModeling-src/ghidra/program/model/scalar/ScalarFormat.java: * @version 1999/02/04

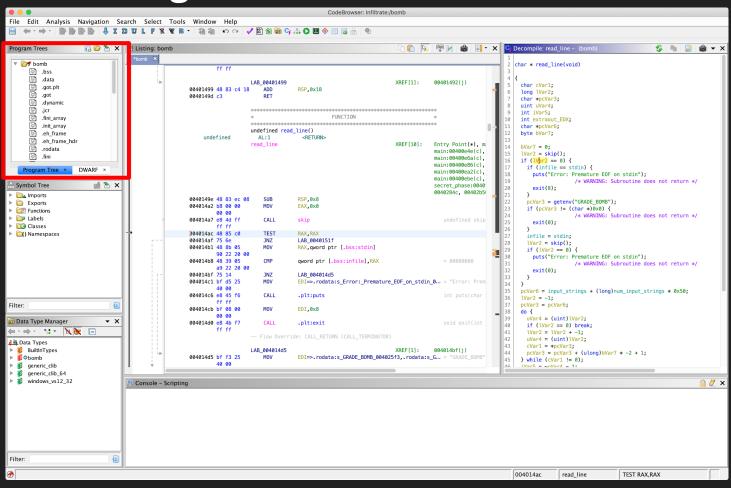
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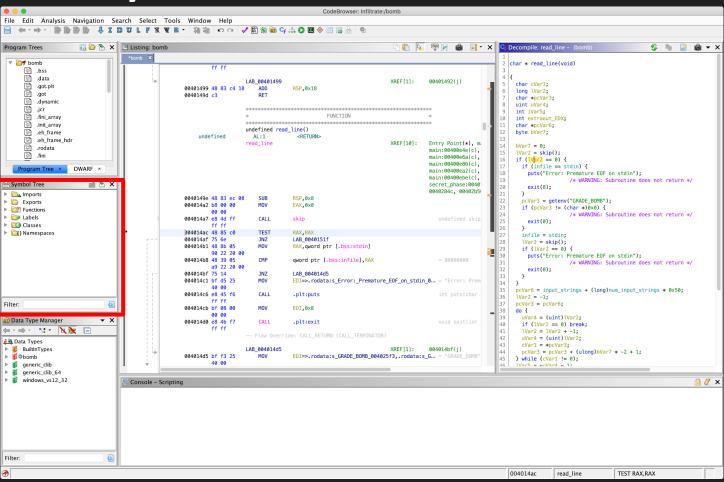
Default UI - CodeBrowser



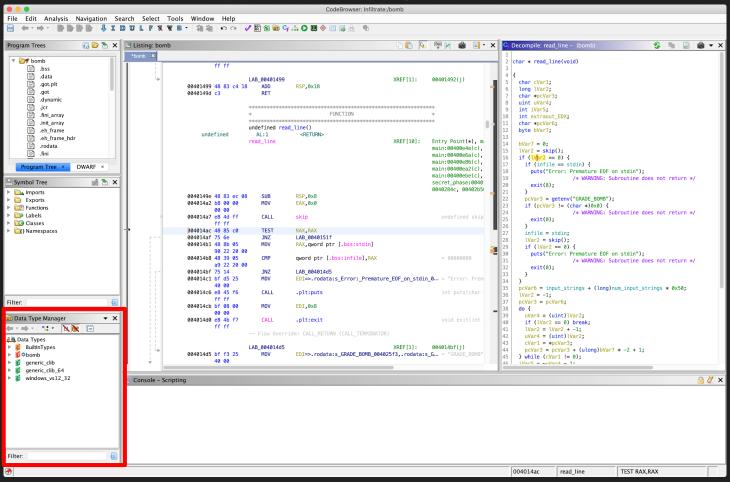
Default UI - Program Trees



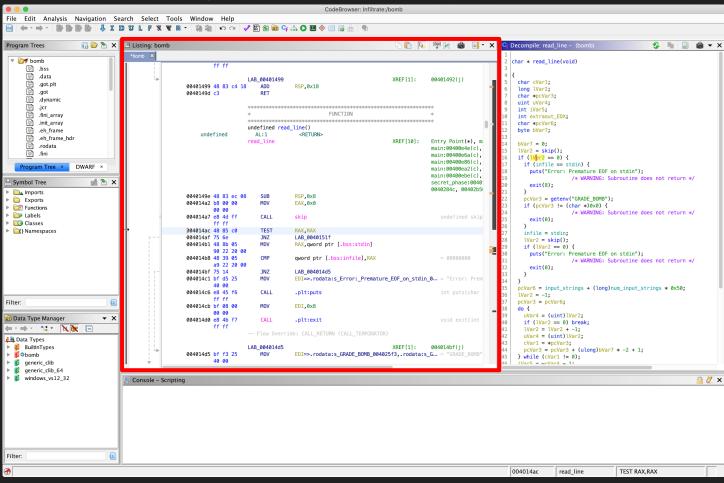
Default UI - Symbol Tree



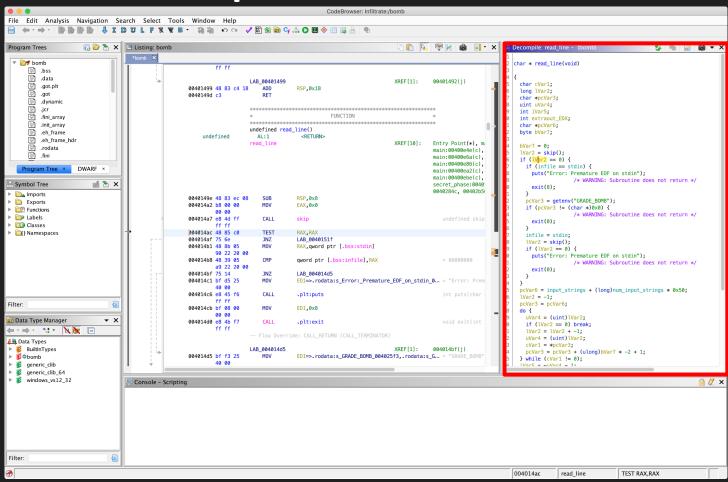
Default UI - Data Type Manager



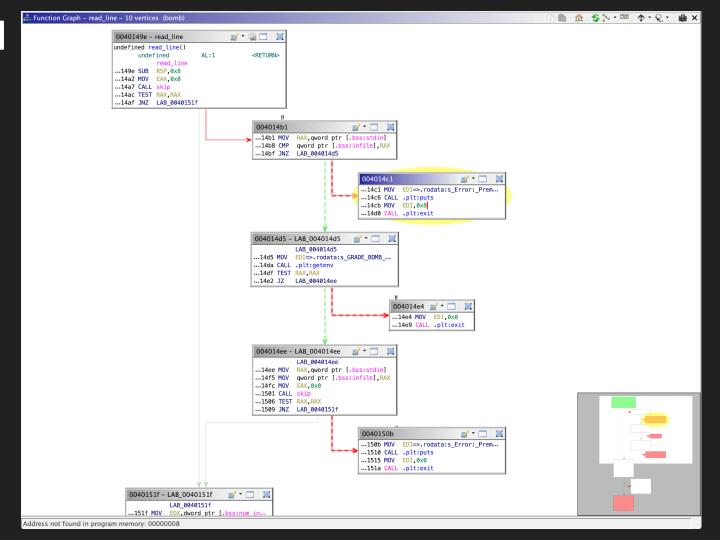
Default UI - Listing (Disassembly)



Default UI - Decompiler



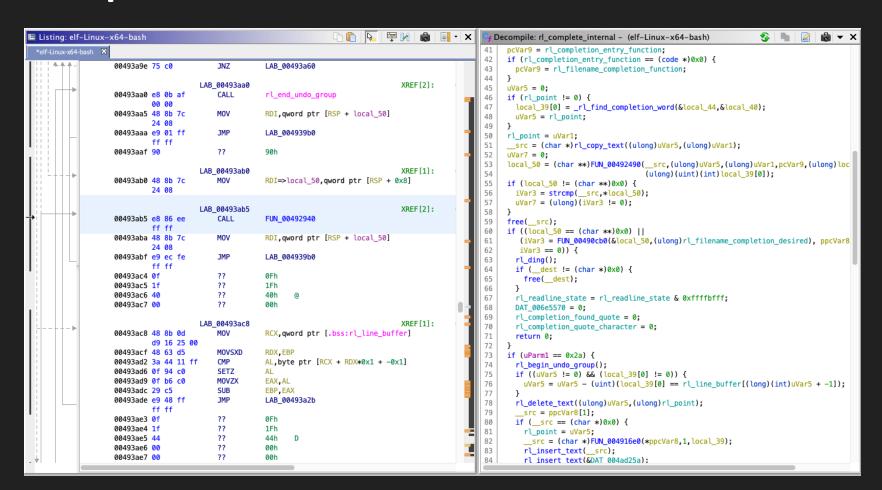
Control Flow Graph



Disassembly with P-Code

```
🖺 🖫 🖟 👸 📋 - 🗙
E Listing: bomb
  *bomb ×
                     004014cb bf 08 00
                                              MOV
                                                          EDI,0x8
                              00 00
                                                                    (register, 0x38, 8) = COPY (const, 0x8, 8)
                    004014d0 e8 4b f7
                                              CALL
                                                          .plt:exit
                              ff ff
                                                                    (register, 0x20, 8) = INT_SUB (register, 0x20, 8), (const, 0x8, 8)
                                                                    STORE (const, 0x131, 8), (register, 0x20, 8), (const, 0x4014d5, 8)
                                                                    CALL (ram, 0x400c20, 8)
                                                                    RETURN (const, 0x0, 8)
                                         -- Flow Override: CALL RETURN (CALL TERMINATOR)
                                          LAB 004014d5
                                                                                           XREF[1]:
                                                                                                        004014bf(j)
                    004014d5 bf f3 25
                                                          EDI=>.rodata:s GRADE BOMB 004025f3..rodata:s G... = "GRADE BOMB"
                                              MOV
                              40 00
                                                                    (register, 0x38, 8) = COPY (const. 0x4025f3, 8)
                    004014da e8 01 f6
                                              CALL
                                                          .plt:getenv
                                                                                                           char * getenv(char * name)
                              ff ff
                                                                    (register, 0x20, 8) = INT_SUB (register, 0x20, 8), (const, 0x8, 8)
                                                                    STORE (const, 0x131, 8), (register, 0x20, 8), (const, 0x4014df, 8)
                                                                    CALL (ram, 0x400ae0, 8)
                    004014df 48 85 c0
                                              TEST
                                                          RAX.RAX
                                                                    (register, 0x200, 1) = COPY (const, 0x0, 1)
                                                                    (register, 0x20b, 1) = COPY (const, 0x0, 1)
                                                                    (unique, 0 \times bb10, 8) = INT AND (register, 0 \times 0, 8), (register, 0 \times 0, 8)
                                                                    (register, 0 \times 207, 1) = INT SLESS (unique, 0 \times bb10, 8), (const, 0 \times 0, 8)
                                                                    (register, 0 \times 206, 1) = INT EQUAL (unique, 0 \times bb10, 8), (const, 0 \times 0, 8)
                    004014e2 74 0a
                                                          LAB 004014ee
                                                                    CBRANCH (ram, 0x4014ee, 8), (register, 0x206, 1)
                    004014e4 bf 00 00
                                              MOV
                                                          EDI.0x0
                              00 00
                                                                    (register, 0x38, 8) = COPY (const, 0x0, 8)
                    004014e9 e8 32 f7
                                              CALL
                                                          .plt:exit
                              ff ff
                                                                    (register, 0x20, 8) = INT_SUB (register, 0x20, 8), (const, 0x8, 8)
                                                                    STORE (const, 0x131, 8), (register, 0x20, 8), (const, 0x4014ee, 8)
                                                                    CALL (ram, 0x400c20, 8)
                                                                    RETURN (const, 0x0, 8)
                                         -- Flow Override: CALL RETURN (CALL TERMINATOR)
                                         LAB 004014ee
                                                                                           XREF[1]:
                                                                                                        004014e2(i)
                    004014ee 48 8b 05
                                                          RAX, qword ptr [.bss:stdin]
                              53 22 20 00
                                                                    (register, 0x0, 8) = COPY (ram, 0x603748, 8)
                    004014f5 48 89 05
                                              MOV
                                                          qword ptr [.bss:infile],RAX
                              6c 22 20 00
                                                                    (ram, 0x603768, 8) = COPY (register, 0x0, 8)
                     004014fc b8 00 00
                                              MOV
                                                          EAX,0x0
```

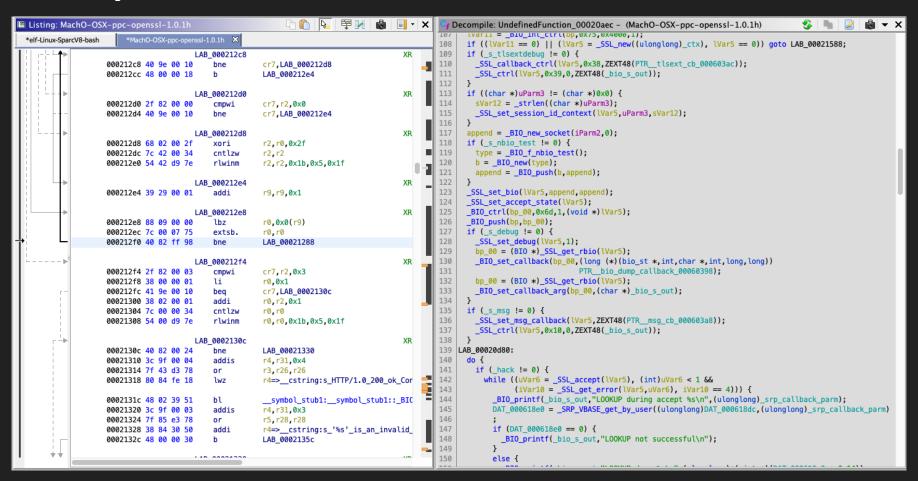
Decompilation Across Architectures - x64



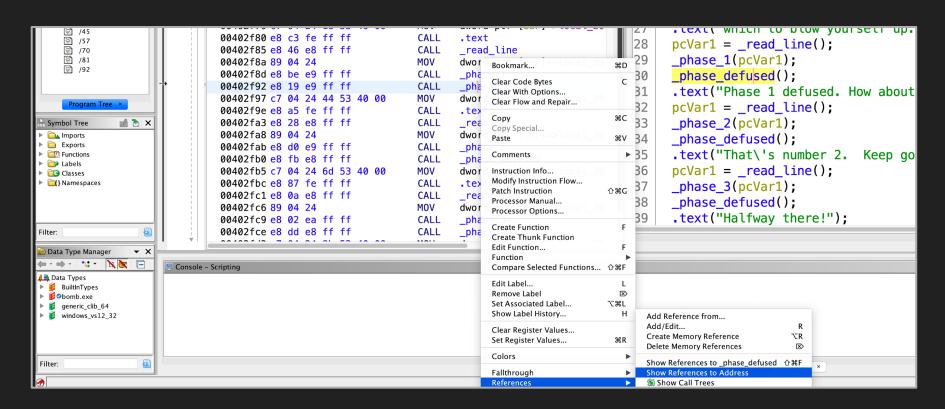
Decompilation Across Architectures - SPARC

```
🐞 🔋 - X
Listing: elf-Linux-SparcV8-bash
                                                                                                                 Decompile: type builtin - (elf-Linux-SparcV8-bash)
  *elf-Linux-SparcV8-bash X
                                                                                                               15 type builtin(undefined4 *puParm1.undefined4 uParm2.undefined4 uParm3.undefined *puParr
                                                                                                               16
                                                                                                                               undefined4 uParm5.char *pcParm6)
                                           undefined type builtin()
                                                                                                               17
                          undefined
                                                            <RETURN>
                                             00:1
                                                                                                               18
                                                                                            XREF[1]:
                                           type_builtin
                                                                                                               19
                                                                                                                     char cVar1;
                     0008e320 9d e3 bf a0
                                                           sp.-0x60.sp
                                               save
                                                                                                               20
                                                                                                                     char *pcVar2;
                     0008e324 80 a6 20 00
                                                           i0,0x0
                                               CMD
                                                                                                                     int *piVar3:
                     0008e328 02 40 00 49
                                               bpe.pn
                                                           %icc.LAB 0008e44c
                                                                                                               22
                                                                                                                     int iVar4;
                     0008e32c 82 10 20 00
                                                           0 \times 0.q1
                                               mov
                                                                                                               23
                                                                                                                     int iVar5;
                     0008e330 c2 06 20 04
                                                           [i0+0x4],q1
                                               lduw
                                                                                                               24
                                                                                                                     undefined4 uVar6:
                     0008e334 c2 00 40 00
                                               lduw
                                                           [a1+a0].a1
                                                                                                               25
                                                                                                                     undefined4 *puVar7;
                     0008e338 c4 48 40 00
                                               ldsb
                                                           [q1+q0],q2
                                                                                                               26
                                                                                                                     uint uVar8;
                                                           q2,0x2d
                     0008e33c 80 a0 a0 2d
                                               cmp
                                                                                                               27
                                                                                                                     char *_s1;
                                                           %icc.LAB 0008e400
                     0008e340 12 40 00 30
                                               bpne, pn
                                                                                                                     int **ppiVar9;
                     0008e344 25 00 03 5d
                                               sethi
                                                           %hi(0xd7400), l2
                                                                                                               29
                                                                                                                     uint uVar10;
                     0008e348 2d 00 03 5d
                                               sethi
                                                           %hi(0xd7400),16
                                                                                                               30
                                                           %hi(0xd7400).15
                     0008e34c 2b 00 03 5d
                                               sethi
                                                                                                                     if (puParm1 == (undefined4 *)0x0) {
                                                                                                               31
                     0008e350 3b 00 03 5d
                                               sethi
                                                           %hi(0xd7400), i5
                                                                                                               32
                                                                                                                       return 0;
                                                           %hi(0xd7400),i3
                     0008e354 37 00 03 5d
                                               sethi
                                                                                                               33
                     0008e358 27 00 03 21
                                                           %hi(0xc8400).l3
                                               sethi
                                                                                                                     pcVar2 = *(char **)puParm1[1]:
                     0008e35c a4 14 a3 28
                                                           12,0x328,12
                                                                                                               35
                                                                                                                     if (*pcVar2 == '-') {
                     0008e360 ac 15 a3 18
                                                           16,0x318,16
                                                                                                               36
                                                                                                                       pcParm6 = "-path";
                     0008e364 aa 15 63 10
                                                           15,0x310,15
                                                                                                               37
                                                                                                                       puParm4 = &DAT 000d7730:
                     0008e368 ba 17 63 20
                                                           i5.0x320.i5
                                                                                                               38
                                                                                                                       uParm5 = 0x61;
                     0008e36c b6 16 e3 30
                                                           i3,0x330,i3
                                                                                                               39
                                                                                                                       cVar1 = pcVar2[1];
                     0008e370 a6 14 e0 20
                                                           13,0x20,13
                                                                                                               40
                                                                                                                       puVar7 = puParm1:
                     0008e374 a0 10 00 18
                                                           i0.l0
                                               mov
                                                                                                               41
                                                                                                                       do {
                                                           0x74,14
                     0008e378 a8 10 20 74
                                               mov
                                                                                                               42
                                                                                                                         s1 = pcVar2 + 1;
                     0008e37c b8 10 20 61
                                                           0x61, i4
                                               mov
                                                                                                               43
                                                                                                                         if (cVar1 == 't') {
                     0008e380 ae 10 20 70
                                               mov
                                                           0×70.17
                                                                                                                           iVar4 = strcmp( s1,"type");
                     0008e384 c4 48 60 01
                                               ldsb
                                                           [q1+0x1],q2
                                                                                                               45
                                                                                                                           if (iVar4 == 0) {
                                                                                                               46
                                                                                                                             * s1 = 't':
                                          LAB 0008e388
                                                                                            XRFF[1]:
                                                                                                                   LAB 0008e470:
                     0008e388 a2 00 60 01
                                               add
                                                           q1,0x1,l1
                                                                                                               48
                                                                                                                             *(undefined *)(*(int *)puVar7[1] + 2) = 0;
                     0008e38c 80 a0 a0 74
                                                           q2,0x74
                                               cmp
                                                                                                               49
                                                                                                                             goto LAB 0008e3d8:
                     0008e390 02 40 00 31
                                                           %icc.LAB 0008e454
                                               bpe.pn
                                                                                                               50
                                                           [q1+0x1], g1
                     0008e394 c2 08 60 01
                                               _ldub
                                                                                                               51
                                                                                                                           puVar7 = (undefined4 *)*puVar7;
                     0008e398 80 a0 a0 2d
                                                           q2.0x2d
                                               cmp
                                                                                                               52
                     0008e39c 02 40 00 39
                                                           %icc.LAB 0008e480
                                               bpe.pn
                                                                                                               53
                                                                                                                         else {
                     00000220 00 20 20 70
                                                           ~2 AV7A
```

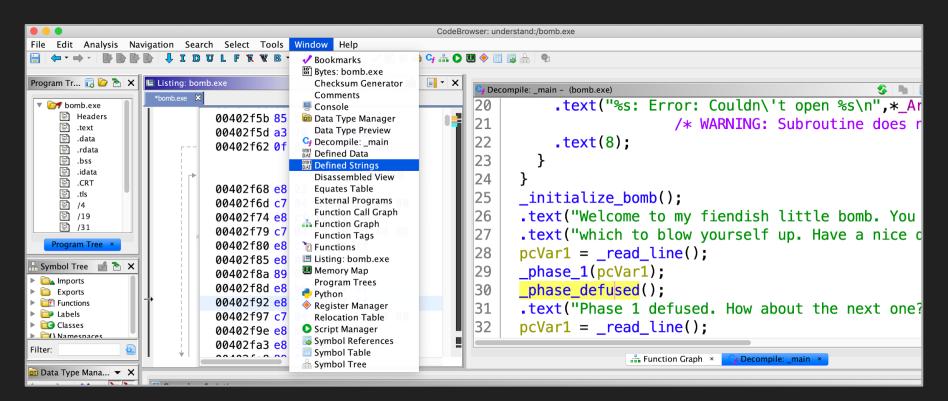
Decompilation Across Architectures - PowerPC



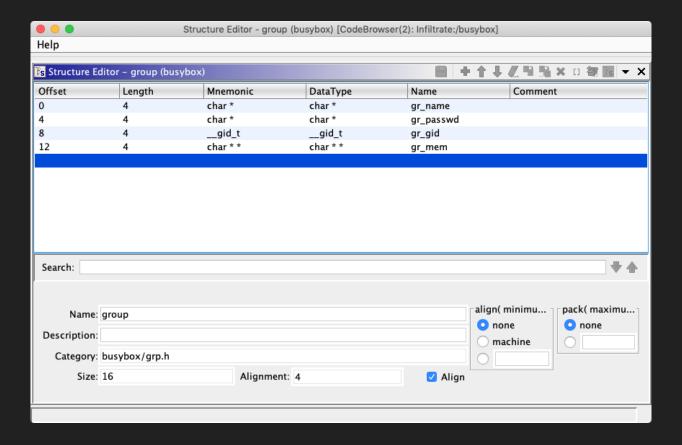
Cross-References



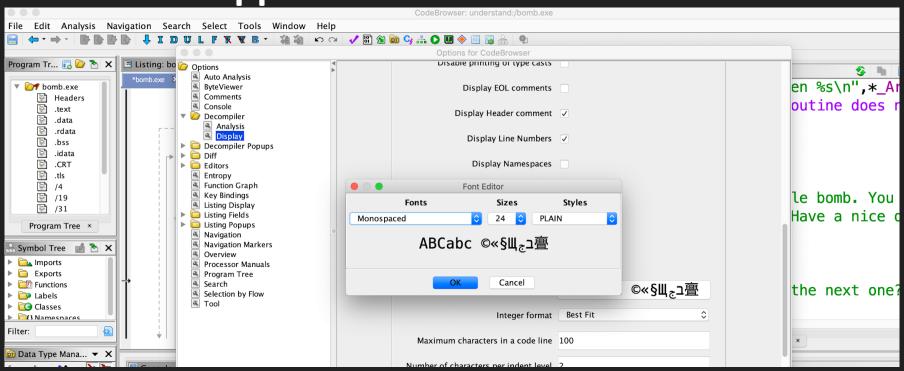
Strings



Type System / Structure Recovery



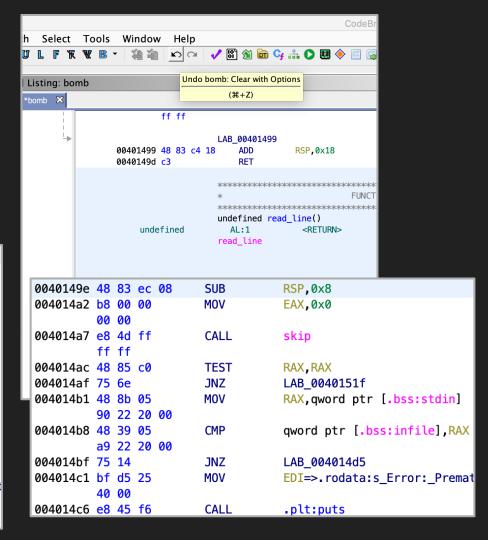
Customize Appearance



Edit >Tool Options → Filter: Fonts

The Undo Button!

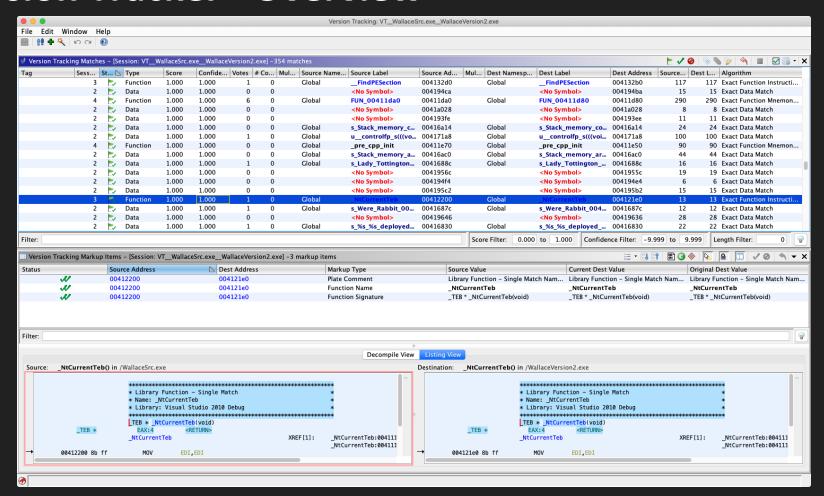
0040149	e 48 8	3 e	c 0	8	SUB		RSP,0x8
004014a	2 b8 0	0 0	0		MOV		EAX,0x0
	00 0	0					
004014a		-	f		CALI	_	skip
	ff f						
004014a			0		TEST		RAX, RAX
004014a	f 75 6	e			JNZ		LAB_0040151f
004014b					MOV		RAX,qword ptr [.bss:stdin]
	90 2			0			
004014b			_		CMP		qword ptr [.bss:infile],RAX
	a9 2		0 0	0			
004014b					1N/7	??	1 AR 404414d5 H
0040 00	40149E					??	83h
0040 00						??	ECh
0040 00	4014a1	08				??	08h
	4014a2			00		MOV	EAX,0×0
			00				
00	4014a7	e8	4d	ff		CALL	skip
		ff	ff				•
00	4014ac	48	85	с0		TEST	RAX, RAX
00	4014af	75	6e			JNZ	LAB_0040151f
00	4014b1	48	8 b	05		MOV	RAX,qword ptr [.bss:stdin]
			22		00		
00	4014b8					CMP	qword ptr [.bss:infile],RA
			22	20	00		
	4014bf					JNZ	LAB_004014d5
00	4014c1			25		MOV	<pre>EDI=>.rodata:s_Error:_Prem</pre>
	4014.6		00			CALL	-14
00	4014c6	e8	45	16		CALL	.plt:puts



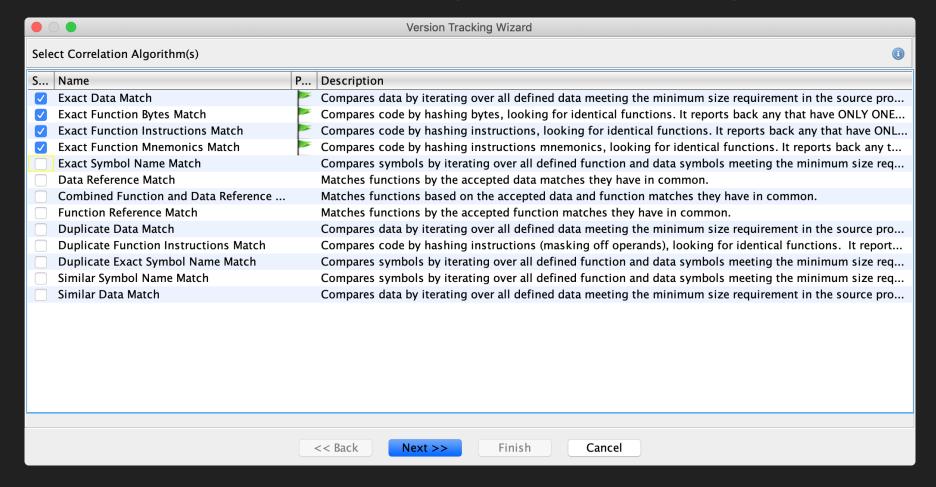
Version Tracker

- Feature for porting RE symbols, annotations, etc. between incrementally updated versions of the same program
- In our experience, not well suited for quick 1-day discovery in patch analysis
 - Use Diaphora or BinDiff for this purpose

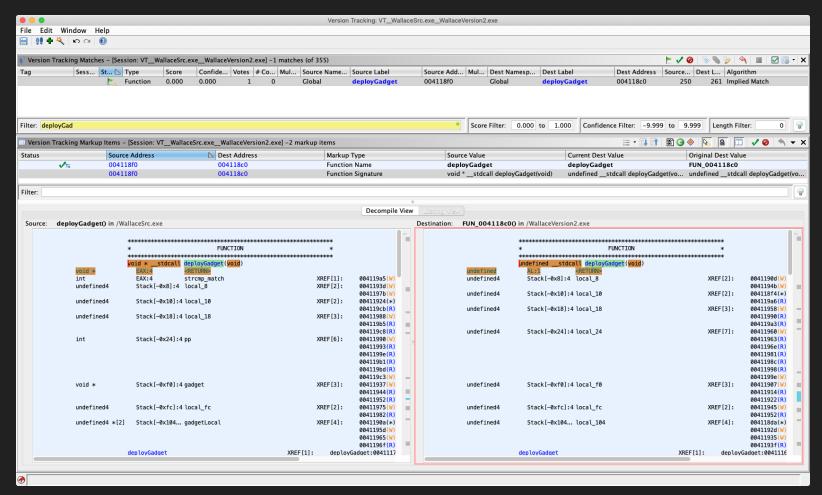
Version Tracker - Overview



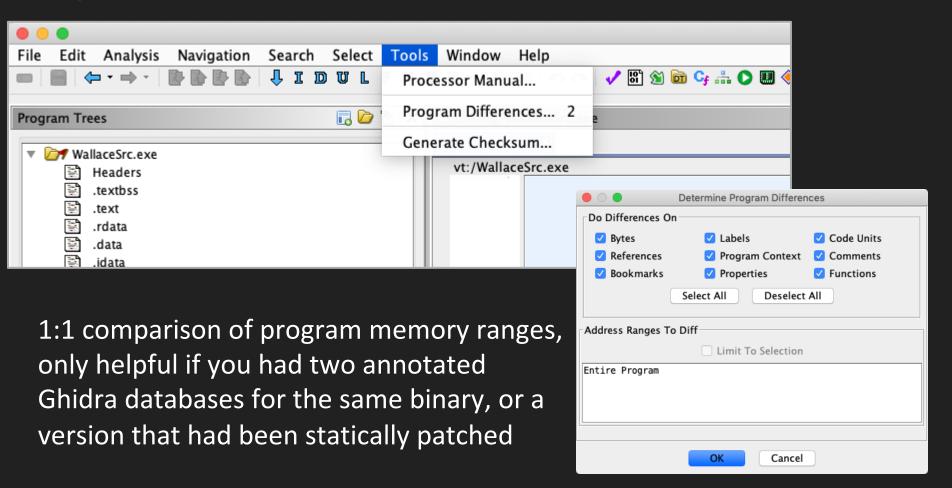
Version Tracker - Selecting Correlation Algorithms



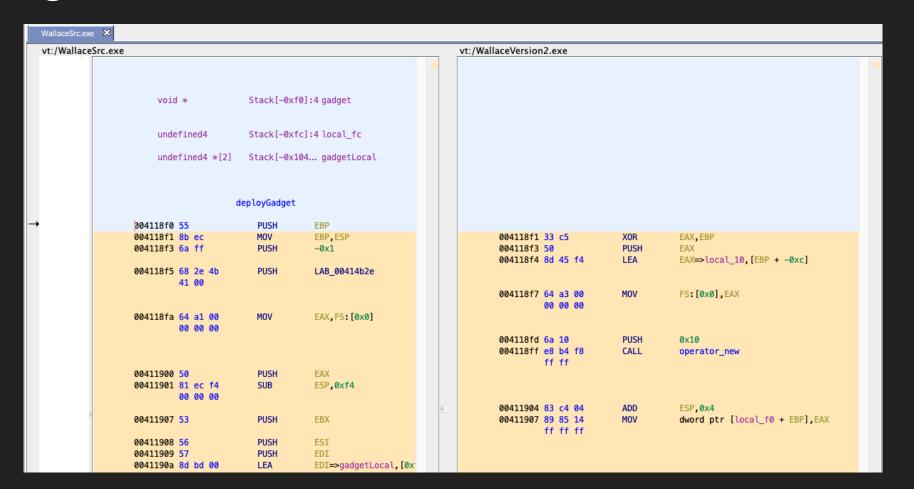
Version Tracker - Function Name Ported



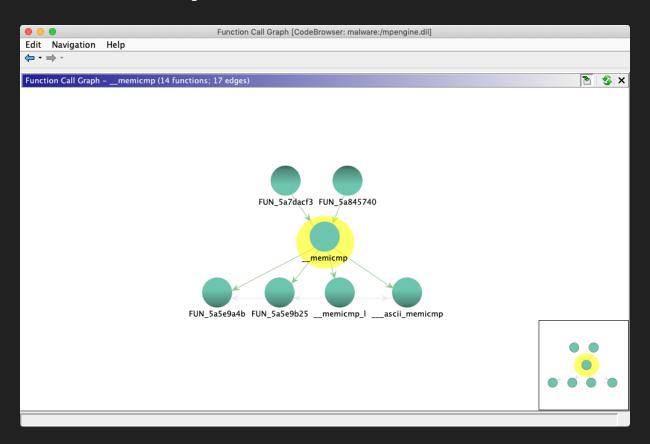
Program Differences



Program Differences



Function Call Graph



Decompiler Slicing

- Decompiler calculates data flow during auto-analysis
- Users can right-click on variables to view def-use chain and forward / backward slices
- Menu bar "Select" options allow users to trace flows to / from given points

```
= (int)uParm1:
  uVar4
          Edit Function Signature
                                           00000
  returi
          Rename Variable
if (rl
          Retype Variable
                                     ЖL
  rl_di
          Auto Create Structure
                                     16
          Highlight Def-use
          Highlight Forward Slice
else {
          Highlight Backward Slice
  if ((
                                           )) {
          Highlight Forward Inst Slice
                                           != 0))
    if
          Highlight Backward Inst Slice
                                           long)(i
          Commit Params/Return
                                           a)examp
          Commit Locals
          Comments
                                           ng)bVari
          Find...
                                     #F
          Find Uses of int
                                   ΰ₩F
                                           ng)bVari
                                           ampleVar
          Properties
```

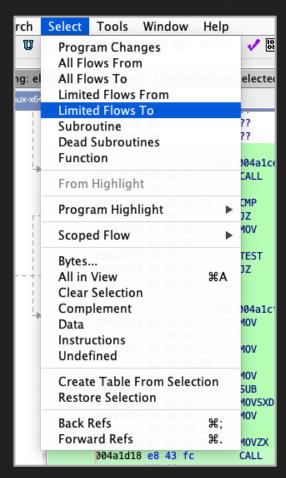
Decompiler Slicing

```
undefined8 rl rubout char(ulong uParm1)
 byte bVar1;
 size t sVar2:
 ushort **ppuVar3;
 undefined8 uVar4:
 int iVar5;
 uint exampleVar;
 exampleVar = rl point:
 iVar5 = (int)uParm1;
 if (iVar5 < 0) {
   uVar4 = rl delete(uParm1 & 0xffffffff00000000 | (ulong)(uint)-iVar5);
   return uVar4:
 if (rl_point == 0) {
  rl ding():
  uVar4 = 0xfffffffff:
 else {
   if ((iVar5 < 2) && (rl explicit arg == 0)) {
     sVar2 = ctype get mb cur max();
     if ((sVar2 == 1) || (rl_byte_oriented != 0)) {
       rl point = rl point - 1;
       bVar1 = *(byte *)(rl_line_buffer + (long)(int)rl_point);
       rl delete text((ulong)rl point,(ulong)exampleVar);
       exampleVar = rl_point;
       if ((rl_point == rl_end) &&
          ((ppuVar3 = __ctype_b_loc(),
           (*(byte *)((long)*ppuVar3 + (ulong)bVar1 * 2 + 1) & 0x40) != 0 &&
         exampleVar = rl character len((ulong)bVar1,(ulong)exampleVar);
         rl_erase_at_end_of_line((ulong)exampleVar);
         return 0:
       return 0;
     rl point = rl find prev mbchar(rl line buffer.(ulong)rl point.1):
     rl delete text((ulong)rl point,(ulong)exampleVar);
     uVar4 = 0:
   else {
     rl backward char();
     rl_kill_text((ulong)exampleVar,(ulong)rl_point);
     uVar4 = 0;
 return uVar4:
```

```
vart = *(byte */(rt_time_burrer + (tong/(int/rt_point))
     rl delete text((ulong)rl point.(ulong)exampleVar);
     exampleVar = rl point:
            Edit Function Signature
            Override Signature
                                             bVar1 * 2 + 1) & 0x40) != 0 && ( r
            Rename Variable
                                             bVar1, (ulong)exampleVar);
                                      %L
            Retype Variable
                                             leVar):
            Auto Create Structure
                                       10
            Highlight Def-use
            Highlight Forward Slice
            Highlight Backward Slice
                                            puffer,(ulong)rl point,1);
            Highlight Forward Inst Slice
                                             mpleVar):
    rl de
            Highlight Backward Inst Slice
    uVar4
            Commit Params/Return
                                        Ρ
  else {
             Commit Locals
   rl bac
   rl_kil
                                             point):
            Comments
   uVar4
             Find...
                                       ЖF
            Find Uses of uint
                                     Ω₩F
return uVa
            Properties
```

Highlight Forward
Inst Slice

Decompiler Slicing



```
undefined8 rl rubout char(ulong uParm1)
 byte bVar1;
 size t sVar2;
 ushort **ppuVar3;
 undefined8 uVar4:
 int iVar5;
 uint exampleVar:
 exampleVar = rl point:
 iVar5 = (int)uParm1:
 if (iVar5 < 0) {
   uVar4 = rl_delete(uParm1 & 0xffffffff00000000 | (ulong)(uint)-iVar5);
   return uVar4:
 if (rl point == 0) {
   rl ding():
   uVar4 = 0xffffffff;
 else {
   if ((iVar5 < 2) && (rl_explicit_arg == 0)) {
     sVar2 = __ctype_get_mb_cur_max();
     if ((sVar2 == 1) || (rl_byte_oriented != 0)) {
       rl point = rl point - 1:
       bVar1 = *(byte *)(rl_line_buffer + (long)(int)rl_point);
       rl_delete_text((ulong)rl_point,(ulong)exampleVar);
       exampleVar = rl point:
       if ((rl point == rl end) &&
          ((ppuVar3 = __ctype_b_loc(),
          (*(byte *)((long)*ppuVar3 + (ulong)bVar1 * 2 + 1) & 0x40) != 0 && ( rl last c pos != 0))
          )) {
         exampleVar = rl character len((ulong)bVar1,(ulong)exampleVar);
         rl erase at end of line((ulong)exampleVar);
         return 0;
       return 0;
     rl_point = _rl_find_prev_mbchar(rl_line_buffer,(ulong)rl_point,1);
     rl delete text((ulong)rl point,(ulong)exampleVar);
     uVar4 = 0:
   else {
     rl backward_char();
     rl_kill_text((ulong)exampleVar,(ulong)rl_point);
     uVar4 = 0:
 return uVar4;
```

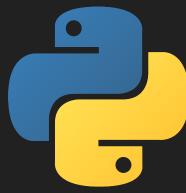
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Scripting With Ghidra

- Available in Java (natively) and Python (via Jython)
- Can be run with interactive GUI or in headless mode
- Ghidra comes with 230+ scripts pre-installed
 - **Educational examples**
 - Code patching
 - Import / export
 - Analysis enhancements
 - Windows, Mac, Linux, VXWorks
 - PE, ELF, Mach-O, COFF
 - x86, MIPS, ARM/THUMB, 8051, etc...





Ghidra APIs

FlatProgramAPI

- Simple "flattened" API for Ghidra scripting
- Programmatic access to common tasks
 - query / modify / iterate / create /
 delete functions / data /
 instructions / comments
- Mostly doesn't require the use of Java objects
- Stable

Ghidra Program API

- More complex rich API for deeper scripting
- Object-oriented (Program, Memory, Function, Instruction, etc...)
- Utility functions help with common scripting tasks
- UI scripting / interactivity
- Prone to change between versions

API Highlights

Rich Scripting Interface

- Programmatic access to binary file formats
- P-code interaction
- Decompiler API
- C header parsing
- Interface for graphing (implementation not included)
- Cyclomatic complexity

Common Utilities Included

- UI windows
- Assembly
- Data serialization
- String manipulation
- Hashing
- Search / byte matching
- XML utilities

Eclipse Integration

- Ghidra has built-in Eclipse integration, via its "GhidraDev" Eclipse plugin
- **NOTE:** For these exercises, we'll be using Ghidra's built-in basic editor don't waste time trying to get Eclipse set up during this workshop

Quick Access



Scripting Demos

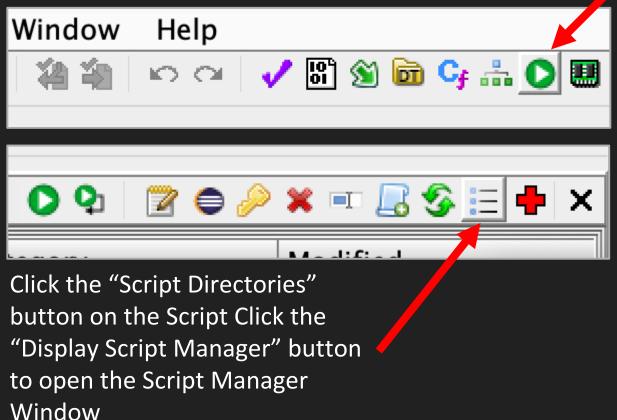
- 1. Hello World
 - a. Java
 - b. Python
- 2. Crypto Constant Search
- 3. Cyclomatic Complexity
- 4. Xor with Python

Importing Demo Scripts



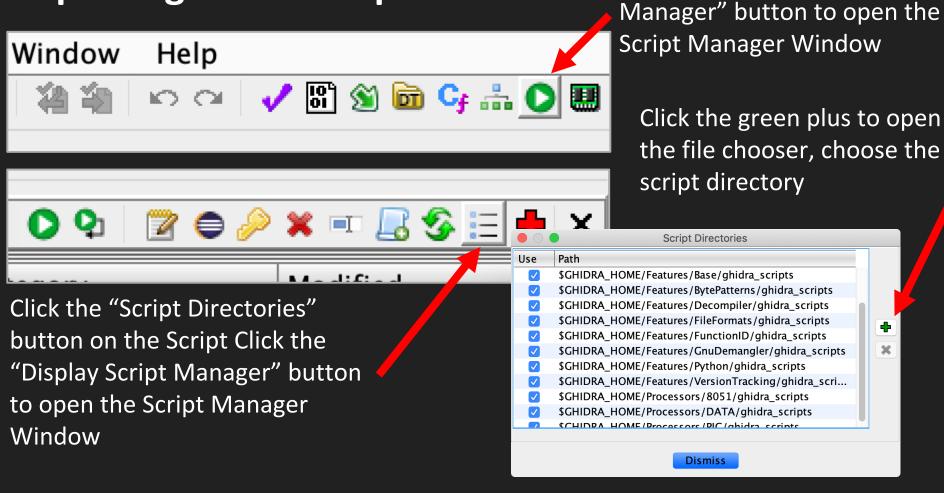
Click the "Display Script Manager" button to open the Script Manager Window

Importing Demo Scripts



Click the "Display Script Manager" button to open the Script Manager Window

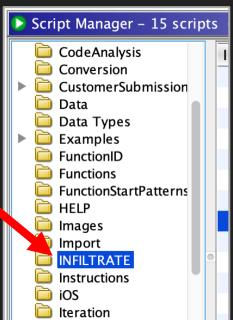
Importing Demo Scripts



Click the "Display Script

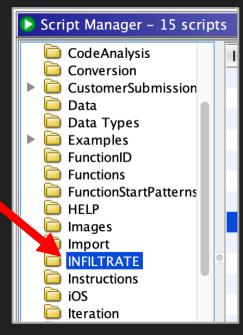
Running Script Demos

Find the
"INFILTRATE" folder
in the script
manager

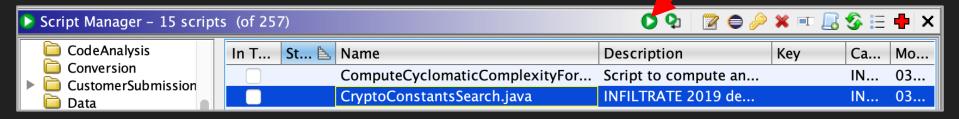


Running Script Demos

Find the
"INFILTRATE" folder
in the script
manager

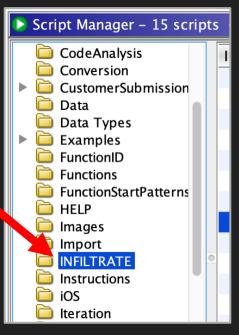


Choose a script and click "Run Script" to run

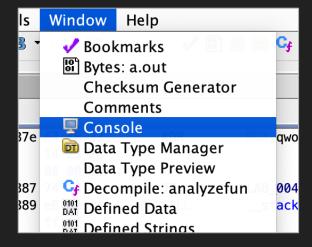


Running Script Demos

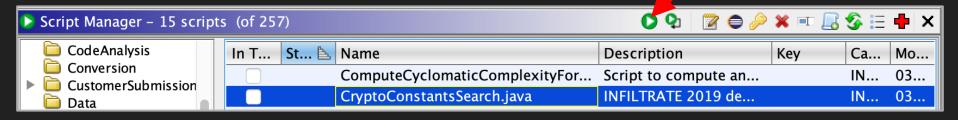
Find the
"INFILTRATE" folder
in the script
manager



Make sure
the
"Console"
window is
open if you
want to see
output



Choose a script and click "Run Script" to run



DEMO: Hello World

- A simple script to print "Hello World" and then iterate over all functions in the program, printing out their names
- HelloWorld.java
- HelloWorld.py

```
HelloWorld.java> Running...
HelloWorld.java> Hello world
HelloWorld.java> _init
HelloWorld.java> FUN_00400ad0
HelloWorld.java> getenv
HelloWorld.java> __errno_location
HelloWorld.java> strcpy
HelloWorld.java> puts
HelloWorld.java> write
HelloWorld.java> _stack_chk_fail
HelloWorld.java> alarm
HelloWorld.java> read
HelloWorld.java> read
HelloWorld.java> __libc_start_main
```

```
import ghidra.app.script.GhidraScript;

public class HelloWorld extends GhidraScript {
    public void run() throws Exception {
        println("Hello world");

        Function currentFunc = getFirstFunction();
        while (currentFunc != null) {
            println(currentFunc.getName());
            currentFunc = getFunctionAfter(currentFunc);
        }
    }
}
```

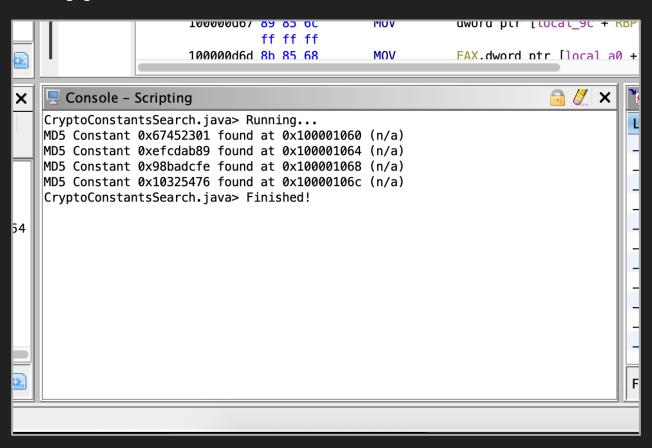
DEMO: Crypto Search

 Find MD5 constants present in a binary, report offset and function name

Take binary endianness into account automatically without user specification

CryptoConstantsSearch.java

DEMO: Crypto Search



DEMO: Calculating Cyclomatic Complexity

- Leverage Ghidra's API for calculating cyclomatic complexity* to easily analyze a whole program
- Pop a GUI window if running interactively, else print to the terminal in headless mode
- ComputeCyclomaticComplexityForAllFunctions.java

^{*} cyclomatic complexity is a measure of the number of unique paths which may be taken through a given function. It can be helpful in finding complex functions likely to have vulnerabilities, e.g, complex parsing routines or state machines.

DEMO: Calculating Cyclomatic Complexity

				_
	elf-Linux-lib-x64.so - Function Cyclomat	tic Complexity		
			■ 7	<u>•</u>
Location	Function Name	Cyclomatic Complexity	È	5
0012b610	cairo_device_flush		7	7
001c5ca0	FUN_001c5ca0		7	7
0019dfc0	FUN_0019dfc0		7	7
001614e0	cairo_region_destroy		7	7
001ba8b0	cairo_gl_surface_create_for_egl		7	7
0016ce70	FUN_0016ce70		7	7
001b3640	FUN_001b3640		7	7
001b40a0	FUN_001b40a0		7	7
00172ee0	FUN_00172ee0		7	7
001a70d0	FUN_001a70d0		7	7
001a3b20	FUN_001a3b20		7	7
0012f1c0	FUN_0012f1c0		7	7
0019c350	FUN_0019c350		8	3
001a59f0	FUN_001a59f0		8	3
0018e490	FUN_0018e490		8	3
001ce780	FUN_001ce780		8	3
001511f0	cairo_mesh_pattern_set_control_point		8	3
00123710	cairo_clip_extents		8	3
0016a010	FUN_0016a010		8	3
001dba60	FUN_001dba60		8	3
001d0070	FUN_001d0070		8	3
0015c250	FUN_0015c250		8	3
0013cd50	FUN_0013cd50		8	3
00183920	FUN_00183920		8	3
001c9140	FUN_001c9140		8	3
0015d120	FUN_0015d120		8	3
001ad4b0	FUN_001ad4b0		8	3
001cdd50	FUN_001cdd50		8	3
001b9b70	cairo_gl_surface_set_size		8	3
Filter:			₽ =	<u>.</u>
	Diemies			
	Dismiss			

```
support — -bash — \%1
INFO FUN 001d0070 complexity: 8 (GhidraScript)
INFO FUN 001d0170 complexity: 8 (GhidraScript)
INFO FUN_001d0260 complexity: 18 (GhidraScript)
INFO FUN 001d05a0 complexity: 29 (GhidraScript)
INFO FUN 001d0b30 complexity: 2 (GhidraScript)
INFO FUN 001d0bb0 complexity: 19 (GhidraScript)
INFO FUN 001d1060 complexity: 17 (GhidraScript)
INFO FUN_001d1490 complexity: 36 (GhidraScript)
INFO FUN_001d1c50 complexity: 6 (GhidraScript)
INFO FUN 001d1dc0 complexity: 28 (GhidraScript)
INFO FUN_001d2260 complexity: 22 (GhidraScript)
INFO FUN 001d26a0 complexity: 34 (GhidraScript)
INFO FUN 001d2db0 complexity: 16 (GhidraScript)
INFO FUN_001d3040 complexity: 24 (GhidraScript)
INFO FUN 001d3440 complexity: 37 (GhidraScript)
INFO FUN 001d3930 complexity: 29 (GhidraScript)
INFO FUN 001d3c40 complexity: 23 (GhidraScript)
INFO FUN 001d3f50 complexity: 27 (GhidraScript)
INFO FUN_001d4340 complexity: 191 (GhidraScript)
INFO FUN_001d6c20 complexity: 16 (GhidraScript)
INFO cairo pdf surface create for stream complexity: 3 (GhidraScript)
INFO cairo_pdf_surface_create complexity: 3 (GhidraScript)
INFO cairo pdf surface_restrict_to_version complexity: 3 (GhidraScript)
INFO cairo pdf get versions complexity: 3 (GhidraScript)
INFO cairo_pdf_version_to_string complexity: 2 (GhidraScript)
INFO cairo pdf surface set size complexity: 4 (GhidraScript)
INFO FUN 001d7070 complexity: 1 (GhidraScript)
INFO FUN 001d7080 complexity: 1 (GhidraScript)
INFO FUN 001d7090 complexity: 1 (GhidraScript)
INFO FUN 001d70b0 complexity: 1 (GhidraScript)
INFO FUN 001d70d0 complexity: 2 (GhidraScript)
INFO FUN 001d71c0 complexity: 2 (GhidraScript)
INFO FUN_001d7230 complexity: 2 (GhidraScript)
INFO FUN_001d72a0 complexity: 6 (GhidraScript)
INFO FUN 001d7370 complexity: 18 (GhidraScript)
INFO FUN_001d76c0 complexity: 14 (GhidraScript)
INFO FUN 001d7820 complexity: 1 (GhidraScript)
INFO FUN_001d7870 complexity: 1 (GhidraScript)
INFO FUN_001d78d0 complexity: 14 (GhidraScript)
INFO FUN 001d7950 complexity: 1 (GhidraScript)
INFO FUN 001d7980 complexity: 5 (GhidraScript)
INFO FUN 001d79b0 complexity: 30 (GhidraScript)
INFO FUN 001d8090 complexity: 3 (GhidraScript)
INFO FUN 001d8140 complexity: 10 (GhidraScript)
INFO FUN 001d8280 complexity: 2 (GhidraScript)
```

DEMO: Python Scripting

- Ghidra can run Python in a Jython environment, the Ghidra Java API is exposed to Python
- This script takes a user address selection and XORs the bytes in place with 0×41
- XorMemoryScript.py

DEMO: Python Scripting

7 addresses	selected)			
100001ce8	00	??	00h	
100001ce9	00	??	00h	
100001cea	24	??	24h	\$
100001ceb	2f	??	2Fh	/
100001cec	22	??	22h	
100001ced	33	??	33h	3
100001cee	38	??	38h	8
100001cef	31	??	31h	1
100001cf0	35	??	35h	5
100001cf1	24	??	24h	\$
100001cf2	25	??	25h	%
100001cf3	61	??	61h	a
100001cf4	2c	??	2Ch	,
100001cf5	24	??	24h	\$
100001cf6	32	??	32h	2
100001cf7	32	??	32h	2
100001cf8	20	??	20h	
100001cf9	26	??	26h	&
100001cfa	24	??	24h	\$
100001cfb	00	??	00h	
100001cfc	00	??	00h	

7 addresses	selected)			
100001ce8	00	??	00h	
100001ce9	00	??	00h	
100001cea	65	??	65h	е
100001ceb	6e	??	6Eh	n
100001cec	63	??	63h	С
100001ced	72	??	72h	r
100001cee	79	??	79h	у
100001cef	70	??	70h	р
100001cf0	74	??	74h	t
100001cf1	65	??	65h	е
100001cf2	64	??	64h	d
100001cf3	20	??	20h	
100001cf4	6d	??	6Dh	m
100001cf5	65	??	65h	е
100001cf6	73	??	73h	S
100001cf7	73	??	73h	S
100001cf8	61	??	61h	a
100001cf9	67	??	67h	g
100001cfa	65	??	65h	е
100001cfb	00	??	00h	
100001cfc	00	??	00h	

Outline

- 1. Intro
- 2. Interactive Exercises
 - a. Manual Static Analysis
 - b. Scripting Ghidra
- 3. P-Code & SLEIGH
- 4. Discussion
- 5. Conclusion

P-Code

- Ghidra's intermediate language
 - Dates back to at least 2005 according to documentation
- Code for different processors can be lifted into p-code, data-flow analysis and decompilation can then run over the p-code
- Pseudo-assembly, represents lifted instructions as small atomic operations without side-effects
- Built-in floating point support

P-Code Design

- The language is machine independent.
- The language is designed to model general purpose processors.
- Instructions operate on user defined registers and address spaces.
- All data is manipulated explicitly.
 Instructions have no indirect effects.
- Individual p-code operations mirror typical processor tasks and concepts.

Processor to p-code modeling:

- RAM → address space
- Register → varnode
- Instruction → operation

```
SCASB.REPNE RDI
                      $U22d0:1 = INT EQUAL RCX, 0:8
                      CBRANCH *[ram]0x401548:8, $U22d0
                      RCX = INT SUB RCX, 1:8
                      $U1d90:8 = COPY RDI
                      $U1da0:8 = INT ADD RDI, 1:8
                      $U1db0:8 = INT ZEXT DF
                      $U1dc0:8 = INT MULT 2:8, $U1db0
                     RDI = INT SUB $U1da0, $U1dc0
                     $U1de0:1 = LOAD ram($U1d90)
                     CF = INT_LESS AL, $U1de0
                      $U1de0:1 = LOAD ram($U1d90)
                      OF = INT_SBORROW AL, $U1de0
                     $U1de0:1 = LOAD ram($U1d90)
                     $Uac60:1 = INT_SUB AL, $U1de0
                     SF = INT_SLESS $Uac60, 0:1
                      ZF = INT EQUAL $Uac60, 0:1
                      $U22f0:1 = BOOL\_NEGATE ZF
                      CBRANCH *[ram]0x401546:8. $U22f0
NOT
             RCX
```

RCX = INT NEGATE RCX

Quoted from docs/languages/html/sleigh.html

Category	P-Code Operations
Data Moving	COPY, LOAD, STORE
Arithmetic	INT_ADD, INT_SUB, INT_CARRY, INT_SCARRY, INT_SBORROW, INT_2COMP, INT_MULT, INT_DIV, INT_SDIV, INT_REM, INT_SREM
Logical	INT_NEGATE, INT_XOR, INT_AND, INT_OR, INT_LEFT, INT_RIGHT, INT_SRIGHT
Int Comparison	INT_EQUAL, INT_NOTEQUAL, INT_SLESS, INT_SLESSEQUAL, INT_LESS, INT_LESSEQUAL
Boolean	BOOL_NEGATE, BOOL_XOR, BOOL_AND, BOOL_OR
Floating Point	FLOAT_ADD, FLOAT_SUB, FLOAT_MULT, FLOAT_DIV, FLOAT_NEG, FLOAT_ABS, FLOAT_SQRT, FLOAT_NAN
FP Compare	FLOAT_EQUAL, FLOAT_NOTEQUAL, FLOAT_LESS, FLOAT_LESSEQUAL
FP Conversion	INT2FLOAT, FLOAT2FLOAT, TRUNC, CEIL, FLOOR, ROUND
Branching	BRANCH, CBRANCH, BRANCHIND, CALL, CALLIND, RETURN
Extension / Truncation	INT_ZEXT, INT_SEXT, PIECE, SUBPIECE

DEMO: Source-Sink Analysis

- Use Ghidra p-code and the decompiler's analysis to identify the sources for values passed to function calls of interest (malloc), particularly function calls accepting user input
- Solving for the actual arguments requires a solver, this is a much simpler analysis that can empower a human analyst to hone in on interesting calls
- Start at the varnode for each argument to malloc, then trace back to the p-code operation that it's derived from
 - From there, recursively trace back the p-code operation(s) defining the varnode(s) that define that the inputs to those operations
- At function call sites, trace in, and find how the returned values are derived
- When a parameter is used, trace back to call sites which set the parameter

SLEIGH

- Ghidra's language for describing instruction sets to facilitate RE
- Disassembly: translate bit-encoded machine instructions into human-readable assembly language statements
- **Semantics:** translate machine instructions into p-code instructions (one-to-many) for decompilation, analysis, and emulation
- Based off of SLED (Specification Language for Encoding and Decoding), a 1997 academic IL

Raw bytes: $0 \times EB \quad 0 \times 03$ x86 instruction: JMP \$+5

Raw bytes: $0 \times EB \quad 0 \times 03$

x86 instruction: JMP \$+5

Raw bytes: $0 \times EB \quad 0 \times 03$ x86 instruction: JMP \$+5

```
SLEIGH:
:JMP rel8 is vexMode=0 & byte=0xeb; rel8 {
   goto rel8;
}
```

00401f16 eb 03

JMP

LAB_00401f1b

BRANCH *[ram]0x401f1b:8

```
Raw bytes: 0 \times EB = 0 \times 03
x86 instruction: JMP $+5
               rel8: reloc is simm8 [ reloc=inst next+simm8; ] {
                  export *[ram]:$(SIZE) reloc;
 SLEIGH:
 :JMP rel8 is vexMode=0 & byte=0xeb; rel8 {
    goto rel8;
```

```
00401f16 eb 03
```

JMP

LAB_00401<u>f1b</u>

```
Raw bytes: 0 \times EB = 0 \times 03
x86 instruction: JMP $+5
               re18: reloc is simm8 [ reloc=inst next+simm8; ] {
                   export *\[ram\]:\(\$(\$\)IZE\) reloc;
 SLEIGH:
 :JMP rel8 is vexMode=0 & byte=0xeb; rel8 {
     qoto rel8;
```

00401f16 eb 03

JMP

LAB_00401f1b

BRANCH *[ram]0x401f1b:8

```
Raw bytes: 0 \times EB = 0 \times 03
x86 instruction: JMP $+5
               rel8: reloc is simm8 [ reloc=inst next+simm8; ] {
                  export *\[ram]:$(SIZE) reloc;
 SLEIGH:
 :JMP rel8 is vexMode=0 & byte=0xeb; rel8 {
    qoto rel8;
```

00401f16 eb 03

JMP

LAB_00401f1b

BRANCH *[ram]0x401f1b:8

Raw bytes: $0 \times 34 \quad 0 \times 57$ x86 instruction: XOR AL, 0×57

Raw bytes: 0x34 0x57 **x86 instruction:** XOR AL, 0x57

```
34 57 XOR AL,0x57

CF = COPY 0:1

OF = COPY 0:1

AL = INT_XOR AL, 0x57:1

SF = INT_SLESS AL, 0:1

ZF = INT_EQUAL AL, 0:1
```

```
Raw bytes: 0x34 0x57 x86 instruction: XOR AL, 0x57
```

SLEIGH:

```
:XOR AL, imm8 is vexMode=0 & byte=0x34; AL & imm8 {
   logicalflags();
   AL = AL ^ imm8;
   resultflags (AL);
                              34 57
                                      X0R
                                             AL, 0x57
                                              CF = COPY 0:1
                                              OF = COPY 0:1
                                              AL = INT_XOR AL, 0x57:1
                                              SF = INT SLESS AL, 0:1
                                              ZF = INT_EQUAL AL, 0:1
```

```
macro logicalflags() {
Raw bytes: 0 \times 34 \quad 0 \times 57
                                                CF = 0;
x86 instruction: XOR AL, 0x57
                                                OF = 0;
SLEIGH:
:XOR AL, imm8 is vexMode=0 & byte=0x34; AL & imm8 {
   logicalflags();
   AL = AL ^ imm8;
   resultflags (AL);
                                34 57
                                         X0R
                                                AL, 0x57
                                                  CF = COPY 0:1
                                                  OF = COPY 0:1
 macro resultflags(result) {
                                                  AL = INT XOR AL, 0x57:1
  SF = result s < 0;
                                                  SF = INT SLESS AL, 0:1
  ZF = result == 0;
                                                  ZF = INT_EQUAL AL, 0:1
  # PF, AF not implemented
```

```
macro logicalflags() {
Raw bytes: 0 \times 34 \quad 0 \times 57
                                                 CF = 0;
x86 instruction: XOR AL
                              0x57
                                                 OF = 0;
SLEIGH:
:XOR AL, imm8 is vexMode=0
                                & byte=0 \times 34; AL & imm8 {
   logicalflags();
   AL = AL ^ imm8;
   resultflags (AL);
                                34 57
                                          X0R
                                                 AL, 0x57
                                                   CF = COPY 0:1
                                                   0F = COPY 0:1
 macro resultflags(result) {
                                                   AL = INT XOR AL, 0x57:1
  SF = result s < 0;
                                                   SF = INT SLESS AL, 0:1
  ZF = result == 0;
                                                   ZF = INT_EQUAL AL, 0:1
  # PF, AF not implemented
```

```
macro logicalflags() {
Raw bytes: 0 \times 34 \quad 0 \times 57
                                                CF = 0;
x86 instruction: XOR AL
                             0x57
                                                OF = 0;
SLEIGH:
:XOR AL, imm8 is vexMode=0 & byte=0x34; AL & imm8 {
   logicalflags();
   AL = AL ^ imm8;
   resultflags (AL);
                               34 57
                                       X0R
                                                AL, 0x57
                                                 CF = COPY 0:1
                                                 0F = COPY 0:1
 macro resultflags(result) {
                                                 AL = INT XOR AL, 0x57:1
  SF = result s < 0;
                                                 SF = INT SLESS AL, 0:1
  ZF = result == 0;
                                                 ZF = INT_EQUAL AL, 0:1
  # PF, AF not implemented
```

```
macro logicalflags() {
Raw bytes: 0 \times 34 \quad 0 \times 57
                                                CF = 0;
x86 instruction: XOR AL
                             0x57
                                                OF = 0;
SLEIGH:
:XOR AL, imm8 is vexMode=0 & byte=0x34; AL & imm8 {
   logicalflags();
   AL = AL ^imm8;
                               34 57
   resultflags (AL);
                                        X0R
                                               AL, 0x57
                                                 CF = COPY 0:1
                                                 0F = COPY 0:1
 macro resultflags(result) {
                                                 AL = INT XOR AL, 0x57:1
  SF = result s < 0;
                                                 SF = INT SLESS AL, 0:1
  ZF = result == 0;
                                                 ZF = INT_EQUAL AL, 0:1
  # PF, AF not implemented
```

SLEIGH Example - x86 XOR AL, imm8

```
macro logicalflags() {
Raw bytes: 0 \times 34 \quad 0 \times 57
                                                CF = 0;
x86 instruction: XOR AL
                             0x57
                                                OF = 0;
SLEIGH:
:XOR AL, imm8 is vexMode=0 & byte=0x34; AL & imm8 {
   logicalflags();
   AL = AL ^ imm8;
   resultflags ( AL );
                               34 57
                                        X0R
                                                AL, 0x57
                                                 CF = COPY 0:1
                                                 0F = COPY 0:1
 macro resultflags(result) {
                                                 AL = INT XOR AL, 0x57:1
  SF = result s < 0;
                                                 SF = INT SLESS AL, 0:1
  ZF = result == 0;
                                                 ZF = INT_EQUAL AL, 0:1
  # PF, AF not implemented
```

Raw bytes: $0 \times 0 = 0 \times 31$ x86 instruction: RDTSC

Raw bytes: $0 \times 0 = 0 \times 31$ x86 instruction: RDTSC

```
0f 31  RDTSC
$U9c60:8 = CALLOTHER "rdtsc"
EDX = SUBPIECE $U9c60, 4:4
EAX = SUBPIECE $U9c60, 0:4
```

```
Raw bytes: 0 \times 0 = 0 \times 31 x86 instruction: RDTSC
```

```
SLEIGH:
:RDTSC is vexMode=0 & byte=0xf; byte=0x31 {
   tmp:8 = rdtsc();
   EDX = tmp(4);
   EAX = tmp(0);
}

Of 31 RDTSC
```

```
RDTSC

$U9c60:8 = CALLOTHER "rdtsc"

EDX = SUBPIECE $U9c60, 4:4

EAX = SUBPIECE $U9c60, 0:4
```

```
Raw bytes: 0 \times 0 = 0 \times 31 x86 instruction: RDTSC
```

```
SLEIGH:
:RDTSC is vexMode=0 & byte=0xf; byte=0x31 {
   tmp:8 = rdtsc();
   EDX = tmp(4);
                                  define pcodeop rdtsc;
   EAX = tmp(0);
            0f 31
                      RDTSC
                               $U9c60:8 = CALLOTHER "rdtsc"
                               EDX = SUBPIECE $U9c60, 4:4
                               EAX = SUBPIECE $U9c60, 0:4
```

```
Raw bytes: 0 \times 0 = 0 \times 31
x86 instruction: RDTSC
  SLEIGH:
   :RDTSC is vexMode=0
                         & byte=0xf; byte=0x31 {
      tmp:8 = rdtsc();
      EDX = tmp(4);
                                       define pcodeop rdtsc;
      EAX = tmp(0)
                           RDTSC
                                    $U9c60:8 = CALLOTHER "rdtsc"
                                    EDX = SUBPIECE $U9c60, 4:4
                                    EAX = SUBPIECE $U9c60, 0:4
```

```
Raw bytes: 0 \times 0 = 0 \times 31
x86 instruction: RDTSC
  SLEIGH:
   :RDTSC is vexMode=0
                         & byte=0xf; byte=0x31 {
      tmp:8 = rdtsc();
      EDX = tmp(4);
                                       define pcodeop rdtsc;
      EAX = tmp(0)
                           RDTSC
                0f 31
                                    $U9c60:8 = CALLOTHER "rdtsc"
                                    EDX = SUBPIECE $U9c60, 4:4
                                    EAX = SUBPIECE $U9c60, 0:4
```

```
Raw bytes: 0 \times 0 = 0 \times 31
x86 instruction: RDTSC
  SLEIGH:
   :RDTSC is vexMode=0
                         & byte=0xf; byte=0x31 {
      tmp:8 = rdtsc();
      EDX = tmp(4);
                                       define pcodeop rdtsc;
      EAX = tmp(0)
                           RDTSC
                0f 31
                                    $U9c60:8 = CALLOTHER "rdtsc"
                                    EDX = SUBPIECE $U9c60, 4:4
                                    EAX = SUBPIECE $U9c60, 0:4
```

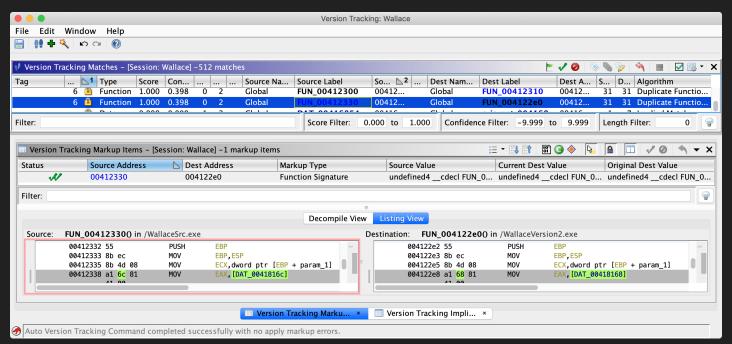
Outline

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Other Major Features

- Multi-user collaboration
- Version tracking
- Extensibility for new binary loaders and architectures

- Headless mode
- GhidraDev Eclipse plugin
- Debugger promised at RSA
- Undocumented p-code emulator



IDA vs Binary Ninja vs Ghidra

IDA

- Maturity
- Windows support
- Decompiler
- Existing corpus of powerful plugins
- Debugger
- Support for paid customers
- Well tested
- Industry standard

Binary Ninja

- Innovation and modern design
- Program analysis features (SSA)
- Multi-level IL
- Rich API
- Embeddable
- Python-native scripting
- Clean modern UI
- Community

Ghidra

- Maturity
- Embedded support
- Decompiler
- Massive API
- Documentation
- Breath of features
- Collaboration
- Version tracking
- Price and open source extensibility

Decompiler - IDA Hex-Rays vs Ghidra

IDA Hex-Rays

- Optional add-on for IDA for IDA
- Microcode-based
- Supports limited architectures
- Better built-in support for Windows
- Variables, data, and functions can be xrefed from decompiler
- Variables can be mapped
- Variable representation can be changed in the decompiler (decimal, hex, char immediate, etc)
- Click to highlight

Ghidra Decompiler Decompiler

- Deeply integrated with Ghidra
- P-code based
- Supports all architectures
- No way to xref from decompiler
- Produces fewer goto statements and seemingly more idiomatic C
- Built in program analysis features,
 e.g., slicing and data flow
- Variables cannot be mapped
- Variable representation cannot be changed in the decompiler
- Middle click to highlight

ILs - Binary Ninja vs Ghidra

Binary Ninja

- Multi-level: LLIL, MLIL, forthcoming HLIL
- Machine consumable and human readable
- SSA form
- Designed in light of years of program analysis research
- Feels nicer to work with
- Deferred flag calculations

Ghidra

- Single level p-code, but can be enhanced by decompiler analysis
- Designed for machine consumption first, not human readability
- Uses SSA during decompilation, but raw p-code is not SSA
- Design origins based off of program analysis research from 20+ years ago

ILs - Binary Ninja vs Ghidra

```
phase_4:
rsp = rsp - 0x18
rcx = rsp + 0xc {var_c}
rdx = rsp + 8 {var_10}
esi = 0x4025cf {"%d %d"}
eax = 0
call(__isoc99_sscanf)
if (eax != 2) then 7 @ 0x401035 else 9 @ 0x401033
```

LLIL

```
phase_4:
  int32_t* rcx = &var_c
  int32_t* rdx = &var_10
  rax = __isoc99_sscanf(arg1, 0x4025cf, rdx, rcx) {"%d %d"}
  if (rax.eax != 2) then 4 @ 0x401035 else 6 @ 0x401033
```

MLIL

```
0040100c - phase 4
undefined phase_4()
      undefined
                        AL:1
                                       <RETURN>
                        Stack[-0xc]:4 local c
      undefined4
      undefined4
                        Stack[-0x10]:4 local 10
      CF = INT_LESS RSP, 24:8
      OF = INT SBORROW RSP, 24:8
      RSP = INT SUB RSP, 24:8
      SF = INT SLESS RSP, 0:8
      ZF = INT EQUAL RSP, 0:8
      $U770:8 = INT ADD 12:8, RSP
      RCX = COPY $U770
      $U770:8 = INT ADD 8:8, RSP
      RDX = COPY $U770
      RSI = COPY 0x4025cf:8
      RAX = COPY 0:8
      RSP = INT SUB RSP, 8:8
      STORE ram(RSP), 0x401029:8
      CALL *[ram]0x400bf0:8
      CF = INT_LESS EAX, 2:4
      OF = INT SBORROW EAX, 2:4
      $U58c0:4 = INT SUB EAX, 2:4
      SF = INT SLESS $U58c0, 0:4
      ZF = INT EQUAL $U58c0, 0:4
      $U2080:1 = BOOL NEGATE ZF
      CBRANCH *[ram]0x401035:8, $U2080
```

We Like Ghidra For...

- Scripting reverse engineering
- Firmware / embedded systems analysis
- Analysis of software that Hex-Rays can't decompile
- Collaborative long-term professional RE
- Professional reversing at a computer workstation with multiple monitors, full keyboard with function keys, mouse with middle click and scroll wheel, etc...

Scripting - Java vs Python

 Java will catch errors at compile time, Ghidra's API is highly object-oriented and benefits from this

 Complex Python scripts feel like binding together Java API calls with Python control flow and syntax

 Recommended workflow: prototype and experiment with APIs / objects in the Python interpreter, write final code in Java

For Reverse Engineers, By Reverse Engineers

- Built for multi-monitor use
- "Moving ants" highlight on control flow graphs
- Configurable "tool" views
- Hotkeys mappable to actions and scripts
- Right click > "extract and import"
- Processor manual integration
- Undo button
- Import directly from zip file
- Snapshot views
- Configurable listings
- Version tracker

- Project-based multi-binary RE
- F1 to open help on whatever the mouse is pointing at
- File System browser
- Highly configurable assembly code listing
- Data flow analysis built into UI
- Embedded image detection
- Search for matching instructions
- Unique windows
 - o Checksum Generator
 - o Disassembled View
 - Data Type Preview
 - Function Tags
 - Symbol tree

Contributing to Ghidra

- Ghidra code is available on Github
 - Apache License 2.0
- NSA has been responsive to community questions and bug reports posted on Github
 - O The agency has already published two minor-version updates

Official site: ghidra-sre.org

Open source: github.com/NationalSecurityAgency/ghidragithub.com/NationalSecurityAgency/ghidra-data

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Conclusion



@0xAlexei / @0xJeremy

Ghidra is a powerful binary reverse engineering tool built by the US National Security Agency

- For reverse engineers, by reverse engineers
- Interactive and headless scripting
- Built for program analysis
- We have yet to see what the community will do with Ghidra, this is just the beginning

Acknowledgements:

- NSA's Ghidra team
- Rob Joyce
- Rolf Rolles
- Evan Jensen
- Sophia d'Antoine
- Dave Aitel and the INFILTRATE team



Demo material: github.com/0xAlexei/INFILTRATE2019

IDA keybindings: github.com/JeremyBlackthorne/Ghidra-Keybindings

Official NSA sites:

github.com/NationalSecurityAgency/ghidra

ghidra-sre.org

Ghidra training: ringzer0.training



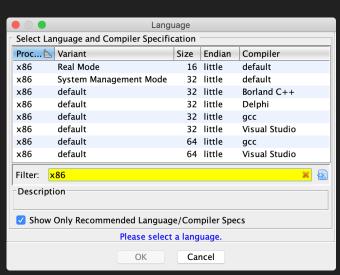


Architecture Support

- Supports a variety of common desktop, embedded, and VM architectures
- Can handle unique compiler idioms and CPU modes
- Users can extend Ghidra with their own custom

processor modules

Ghidra can decompile anything it can disassemble





Documentation

- Help > Contents
- F1 or Help key while pointing at any field or menu option
- docs directory
 - JavaDoc
 - Several classes
 - P-code and SLEIGH
- Doxygen in source files



Python Interpreter Window

- Unlike Java, which must be compiled in order to run, Python can be run inside Ghidra in an interactive REPL shell
- The shell can be helpful for exploring unfamiliar objects Ghidra has great Python object str implementations

```
Python - Interpreter

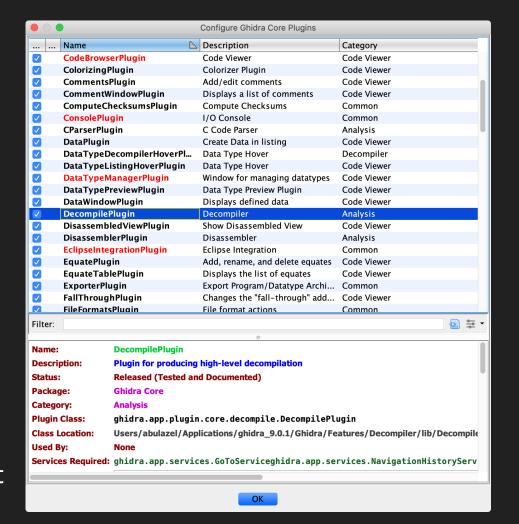
>>> memcpy = getFunctionAt(currentAddress)
>>> refs = getReferencesTo(memcpy.getEntryPoint())
>>> refs
array(ghidra.program.model.symbol.Reference, [From: 004006ac To: 00400510 Type: UNCONDITIONAL_CALL Op: 0
DEFAULT, From: 004006d5 To: 00400510 Type: UNCONDITIONAL_CALL Op: 0 DEFAULT])
>>>
```

Script vs. Plugin vs. Extension

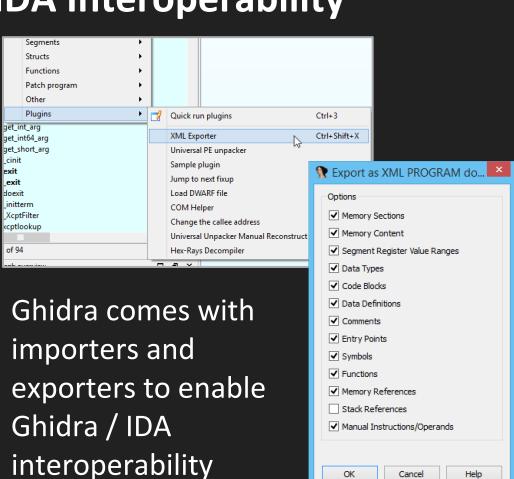
- Scripts: do a single thing with defined start and end point
- Plugins: base components for everything you interact with in Ghidra,
 such as UI panes
- Extensions: sets of plugins for extended functionality, e.g., custom binary format loaders, interfaces to external tools, libraries for use by other scripts
 - o Example: Rolf Rolles' GhidraPAL
 qithub.com/RolfRolles/GhidraPAL/releases

Tools

- Ghidra tools are assemblies of plugins
- Ghidra comes with two tools:
 CodeBrowser and
 VersionTracker
- Tools can be configured and customized to suit unique RE needs - though currently there doesn't seem to be much point



IDA Interoperability



```
Decompile: FUN 00401070 - (13-2)
      undefined *h;
      word local 18:
      dword local 16;
      dword local e;
      dword cv:
27
28
      uVar1 = (*(code *)(undefined *)0xffffffff)(0):
29
      uVar2 = (*(code *)(undefined *)0xffffffff)(1);
30
      hWnd = (*(code *)(undefined *)0xffffffff)();
      hDC = (*(code *)(undefined *)0xffffffff)(hWnd);
      uVar3 = (*(code *)(undefined *)0xffffffff)(hDC);
      uVar4 = (*(code *)(undefined *)0xffffffff)(hDC,uVar1,uVar2);
      (*(code *)(undefined *)0xffffffff)(uVar3,uVar4);
      (*(code *)(undefined *)0xffffffff)(uVar3,0,0,uVar1,uVar2,hDC,0,0,0xcc0020);
      (*(code *)(undefined *)0xffffffff)(uVar4,0x18,&pv);
      bmi.bmiHeader.biSize = 0x28;
38
      bmi.bmiHeader.biPlanes = 1;
39
      bmi.bmiHeader.biBitCount = 0x20:
      bmi.bmiHeader.biCompression. 0 2 = 0;
41
      bmi.bmiHeader.biCompression. 2 2 = 0;
42
      bmi.bmiHeader.biSizeImage = 0:
43
      bmi.bmiHeader.biXPelsPerMeter = 0:
44
      bmi.bmiHeader.biYPelsPerMeter = 0:
45
      bmi.bmiHeader.biClrUsed = 0;
46
      bmi.bmiHeader.biClrImportant = 0;
      iVar6 = local_6c * 0x20 + 0x1f;
      iVar6 = ((int)(iVar6 + (iVar6 >> 0x1f & 0x1fU)) >> 5) * 4 * cLines;
      uVar1 = (*(code *)(undefined *)0xffffffff)(0x42,iVar6);
      bmi.bmiColors = (*(code *)(undefined *)0xffffffff)(uVar1);
      (*(code *)(undefined *)0xffffffff)(hDC,uVar4,0,cLines,bmi.bmiColors,&bmi,0);
      local 18 = 0 \times 4 \times 4 \times 2;
      uVar2 = (*(code *)(undefined *)0xffffffff)(0x42,iVar6 + 0x36);
      iVar5 = (*(code *)(undefined *)0xffffffff)(uVar2);
      _memcpy(iVar5,&local_18,0xe);
      memcpy(iVar5 + 0xe, \&bmi, 0x28);
57
      memcpy(iVar5 + 0x36,bmi.bmiColors,iVar6);
      (*(code *)(undefined *)0xffffffff)(uVar1);
      (*(code *)(undefined *)0xffffffff)(uVar1);
      (*(code *)(undefined *)0xffffffff)(hWnd,hDC);
      (*(code *)(undefined *)0xffffffff)(uVar3);
      (*(code *)(undefined *)0xffffffff)(uVar4);
63
      *(int *)param_1 = iVar5;
      *(int *)param 2 = iVar6 + 0x36;
      return:
```

Decompiler Windows Structs - Hex-Rays vs Ghidra

```
v12 = GetSystemMetrics(0);
    cu = GetSustemMetrics(1):
    hWnd = GetDesktopWindow();
    hDC = GetDC(hWnd);
    hdc = CreateCompatibleDC(hDC);
    h = CreateCompatibleBitmap(hDC, v12, cy);
    SelectObject(hdc, h);
    BitBlt(hdc, 0, 0, v12, cy, hDC, 0, 0, 0xCC0020u);
    GetObjectA(h, 24, &pv);
    bmi.bmiHeader.biSize = 40:
    bmi.bmiHeader.biWidth = v6;
    bmi.bmiHeader.biHeight = cLines:
    bmi.bmiHeader.biPlanes = 1:
    bmi.bmiHeader.biBitCount = 32;
    bmi.bmiHeader.biCompression = 0;
    bmi.bmiHeader.biSizeImage = 0;
    bmi.bmiHeader.biXPelsPerMeter = 0;
    bmi.bmiHeader.biYPelsPerMeter = 0:
    bmi.bmiHeader.biClrUsed = 0;
    bmi.bmiHeader.biClrImportant = 0;
    dwButes = cLines * 4 * ((32 * v6 + 31) / 32):
    hMem = GlobalAlloc(0x42u, cLines * 4 * ((32 * v6 + 31) / 32));
    bmi.bmiColors[0] = (RGBQUAD)GlobalLock(hMem);
    GetDIBits(hDC, (HBITMAP)h, 0, cLines, (LPVOID)bmi.bmiColors[0], &bmi, 0);
    016 = 54:
    v15 = dwBytes + 54;
46
    v14 = 19778:
    v8 = GlobalAlloc(0x42u, dwBytes + 54);
    v9 = GlobalLock(v8);
    memcpu(v9, &v14, 0xEu);
    memcpy((char *)v9 + 14, &bmi, 0x28u);
    memcpy((char *)v9 + 54, (const void *)bmi.bmiColors[0], dwBytes);
    GlobalUnlock(hMem):
    GlobalFree(hMem);
    ReleaseDC(hWnd, hDC);
55
    DeleteDC(hdc);
    DeleteObject(h);
    result = v9:
    *a1 = u9:
    *a2 = dwButes + 54:
    return result;
```

```
local 50 = (HDC)0\times0;
      local_20 = GetSystemMetrics(0);
      local 8 = GetSystemMetrics(1);
     DAT_004078cc = GetDesktopWindow();
     DAT 004078c8 = GetDC(DAT 004078cc);
      local_50 = CreateCompatibleDC(DAT_004078c8);
     local_1c = CreateCompatibleBitmap(DAT_004078c8, local_20, local_8);
     SelectObject(local 50, local 1c);
     BitBlt(local_50,0,0,local_20,local_8,DAT_004078c8,0,0,0xcc0020);
     GetObjectA(local 1c,0x18,local 70);
      local 4c = 0x28:
      local 48 = local 6c;
      local 44 = local 68:
      local 40 = 1;
      local 3e = 0x20:
      uStack60 = 0;
     uStack58 = 0:
     local 38 = 0;
     local 34 = 0:
     local 30 = 0;
      local 2c = 0:
      local 28 = 0:
      iVar1 = local 6c * 0x20 + 0x1f;
     dwBvtes = ((int)(iVar1 + (iVar1 >> 0x1f & 0x1fU)) >> 5) * 4 * local 68;
      hMem = GlobalAlloc(0x42,dwBytes);
60
      local 24 = (undefined4 *)GlobalLock(hMem):
      GetDIBits(DAT 004078c8, local 1c,0, local 68, local 24, (LPBITMAPINFO)&local 4c,0);
62
      dwBytes_00 = dwBytes + 0x36;
63
      local e = 0x36;
      local_18 = 0x4d42;
      local 16 = dwBytes 00:
      local_58 = GlobalAlloc(0x42,dwBytes_00);
      local 54 = (undefined4 *)GlobalLock(local 58);
     FUN 00401930(local 54, (undefined4 *)&local 18,0xe);
      FUN 00401930((undefined4 *)((int)local 54 + 0xe), (undefined4 *)&local 4c.0x28);
     FUN 00401930((undefined4 *)((int)local 54 + 0x36),local 24,dwBytes);
     GlobalUnlock(hMem);
     GlobalFree(hMem);
     ReleaseDC(DAT_004078cc,DAT_004078c8);
     DeleteDC(local 50);
     DeleteObject(local_1c);
     *(undefined4 **)param_1 = local_54;
     *param_2 = dwBytes_00;
     return:
```

P-Code Decompilation / Analysis

- "Raw p-code" = direct translation of one CPU instructions to p-code ops
- During decompilation, p-code is analyzed, and may be modified
 - Insertion of MULTIEQUAL instructions (SSA phi-nodes)
 - O Association of parameters with CALL ops and return values with RETURN ops
 - Construction of abstract syntax tree
 - o etc... see linked documents
- The Decompiler is a C++ binary that runs on the host system.
- When writing scripts interacting with p-code expect to experiment, read source code, and glean usage from example included scripts

```
docs/languages/html/additionalpcode.html
Ghidra/Features/Decompiler/src/decompile/cpp/docmain.hh
```



Recommended Readings

Elias Bachaalany's quick overview: 0xeb.net/2019/03/ghidra-a-quick-overview

Danny Quist on getting started with Ghidra:

github.com/dannyquist/re/blob/master/ghidra/ghidra-getting-started.md

Rolf Rolles' GhidraPAL program analysis library:

github.com/RolfRolles/GhidraPAL msreverseengineering.com/blog/2019/4/17/an-abstract-interpretation-based-deobfuscation-plugin-for-ghidra

Travis Goodspeed on reversing MD380 firmware with Ghidra:

github.com/travisgoodspeed/md380tools/wiki/GHIDRA

Additional Recommended Readings

Links to many loaders and processor modules:

groups.google.com/forum/?utm medium=email&utm source=footer#!msg/sleigh/pD12wcoKUQM/rG8esJAVBAAJ

Writing a WASM Loader: habr.com/en/post/443318/

Sega Genesis loader: zznop.github.io/romhacking/2019/03/14/sega-genesis-romhacking-with-ghidra.html

Because Security's first impressions, with many Ghidra video links: blog.because-security.com/t/ghidra-wiki/431#Firstimpress283

Elias Bachaalany's "Daenerys" IDA Pro / Ghidra interoperability framework: 0xeb.net/2019/03/daenerys-ida-pro-and-ghidra-interoperability-framework

Academic Work Related to SLEIGH / P-code

Norman Ramsey and Mary F. Fernández. 1997. Specifying representations of machine instructions. ACM Transactions on Programming Languages and Systems (TOPLAS) Vol. 19, Issue 3 (1997)

citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.51.360&rep=rep1&type=pdf

Cristina Cifuentes and Mike Van Emmerik. "UQBT: Adaptable binary translation at low cost." Computer 33.3 (2000)

personales.ac.upc.edu/vmoya/docs/00825697.pdf

The New Jersey Machine-Code Toolkit (1990s)

www.cs.tufts.edu/~nr/toolkit/

See docs/languages/html/sleigh.html