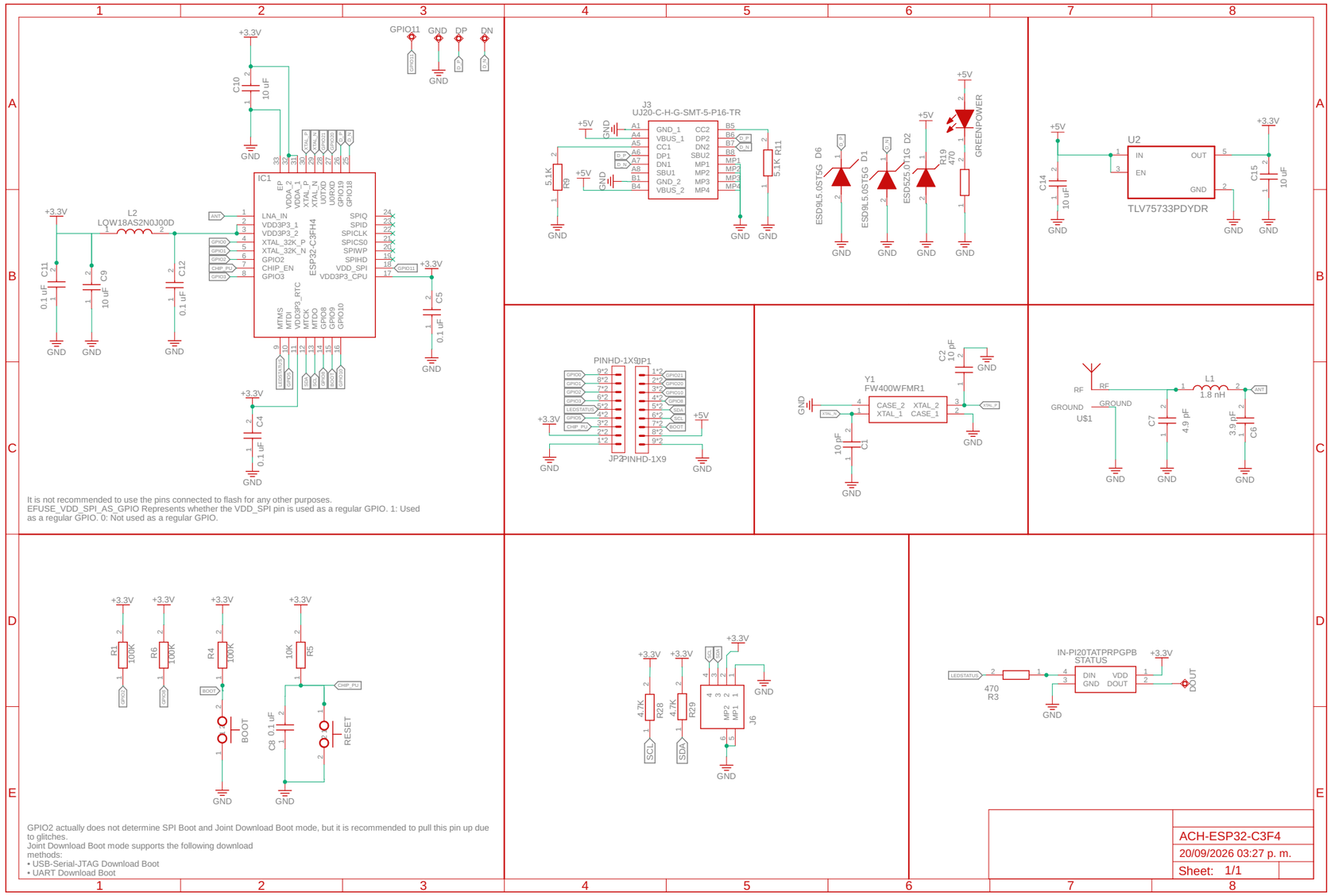

GPIO11
GND
DP
DN
+3.3V
C10
10 uF
IC1
ESP32-C3FH4
L2
LQW18AS2N0J00D
C11
0.1 uF
C9
10 uF
C12
0.1 uF
C4
0.1 uF
C5
0.1 uF
J3
UJ20-C-H-G-SMT-5-P16-TR
5.1K
R9
5.1K
R11
D6
ESD9L5.0ST5G
D1
ESD9L5.0ST5G
D2
ESD5Z5.0T1G
R19
470
GREENPOWER
U2
TLV75733PDYDR
C14
10 uF
C15
10 uF
JP1
PINHD-1X9
JP2
PINHD-1X9
Y1
FW400WFMR1
C2
10 pF
C1
10 pF
L1
1.8 nH
C7
4.9 pF
C6
3.9 pF
U$1
RF
GROUND
It is not recommended to use the pins connected to flash for any other purposes.
EFUSE_VDD_SPI_AS_GPIO Represents whether the VDD_SPI pin is used as a regular GPIO. 1: Used as a regular GPIO. 0: Not used as a regular GPIO.
R1
100K
R6
100K
R4
100K
BOOT
10K
R5
RESET
C8 0.1uF
GPIO2 actually does not determine SPI Boot and Joint Download Boot mode, but it is recommended to pull this pin up due to glitches.
Joint Download Boot mode supports the following download methods:
• USB-Serial-JTAG Download Boot
• UART Download Boot
4.7K
R28
4.7K
R29
J6
IN-PI20TATPRPGB
STATUS
470
R3
DOUT
ACH-ESP32-C3F4
20/09/2026 03:27 p. m.
Sheet: 1/1