

0x2: OSF and OSHW synergy with Open ISA

vPub Event

3mdeb Team



We believe building and growing community around OSF, OSHW and Open ISA is especially important in recent times, so thank you for spending time with us! First of all have fun!

- Be kind to each other.
- Adult beverages allowed.
- Mute by default.
- Feel free to introduce yourself and tell us why you joined.
- Feel free to queue the topics, questions, problems, by sending private message to me (aka pietrushnic)
- No audio or video recording
- We save chat messages for future blog posts, urls and ideas tracking and improving the meeting
- "Last human standing party"

- This is second event
- Last event was called vBeer and had place on 18th Feb 2021
- There were 44 attendees in peak
- Event last for 11.5h (3PM UTC - 2:30AM UTC)
- Blog about this event can be found here:
https://blog.3mdeb.com/2021/2021-02-23-osf_vpub_01/

New advances at open-source firmware/hardware world, like this new initiative for open-source FPGAs: <https://osfpga.org/>

- Alexey B. can talk something about it

Is the new emerging RISC-V hardware such as BeagleV - truly open?

Results of "Ideal platform for coreboot training" survey

- thank you very much for all time spent answering the questions (11 answers received)
- next time we will do better

Libre-SOC: <https://libre-soc.org/>

- https://bugs.libre-soc.org/show_bug.cgi?id=164

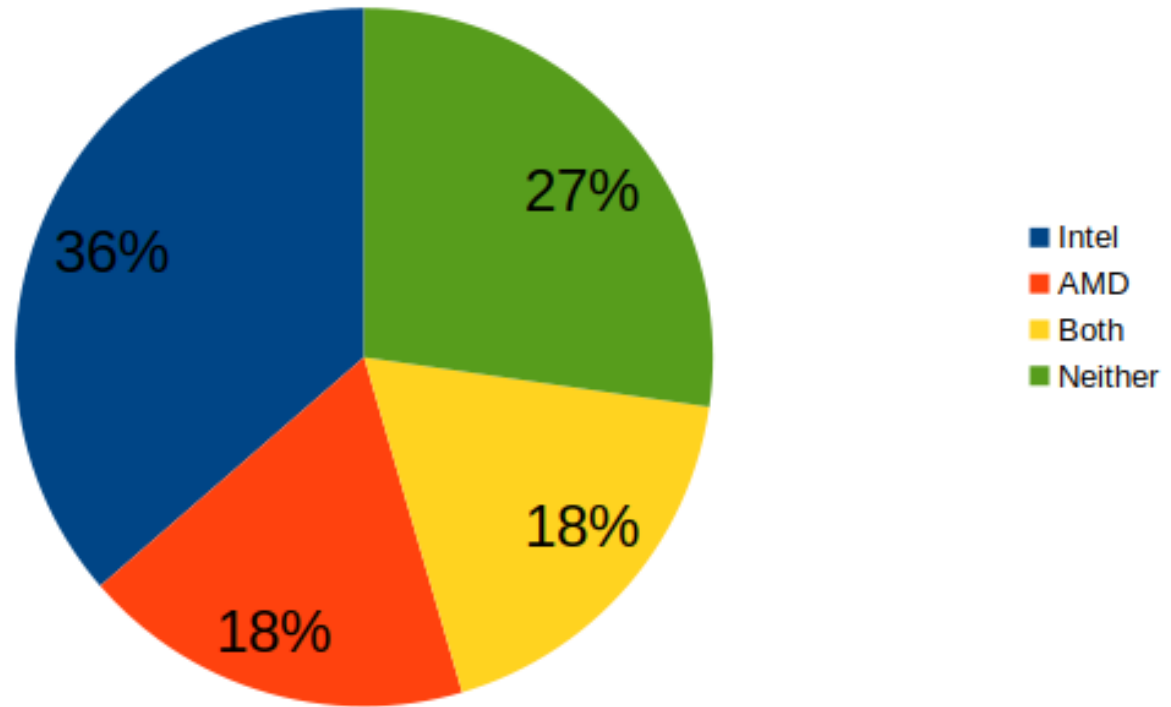
OSFTTL

[https://docs.dasharo.com/osf-trolling-list/osf issues taxonomy/](https://docs.dasharo.com/osf-trolling-list/osf%20issues%20taxonomy/)

OSFTQL

<https://docs.dasharo.com/osf-trivia-list/introduction/>

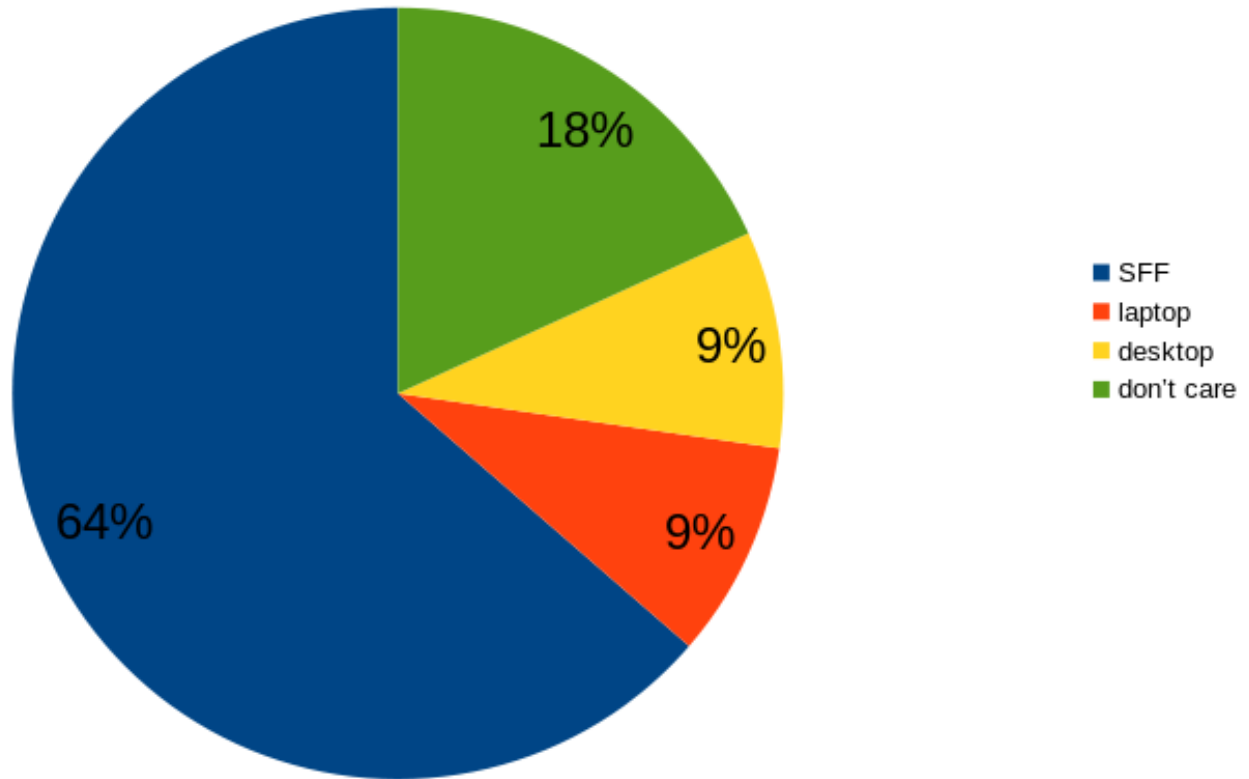
Intel FSP or AMD AGESA -based platform, or both, - and why?



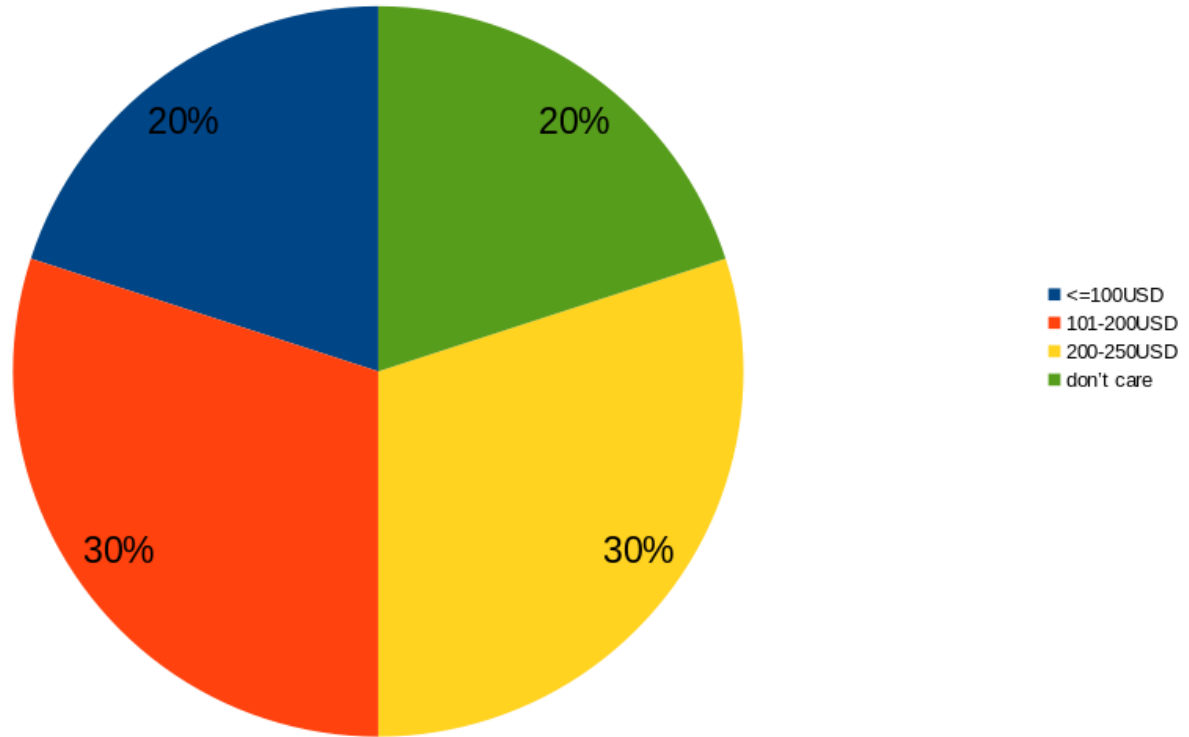
What is a good example(s) of a platform that could be suitable for coreboot training?

- ideally QEMU or cheap x86 SBC
 - Intel NUC or z8350 based stuff (atomic pi?)
 - some suggested t420, but those are hard to disassemble
 - ASRock Desk Mini A300 (who could submit that answer ;P?)
 - MinnowBoard - were good but now outdated (FSP1.0)
 - Sandy/Ivy Bridge or Haswell
- after market Lenovo x220
- easy flashing and serial logs gathering
 - socketed SPI (not soldered) or in reasonable package (SOIC8)
- preferably no EC
- porting would be good exercise
- Important note: it depends what are the goals, older platforms (without FSP or AGESA) have coreboot concepts visible better

What is a form-factor of this platform(s)?



How much does this platform(s) costs, and how to get it?



Q&A

class: center, middle, outro