

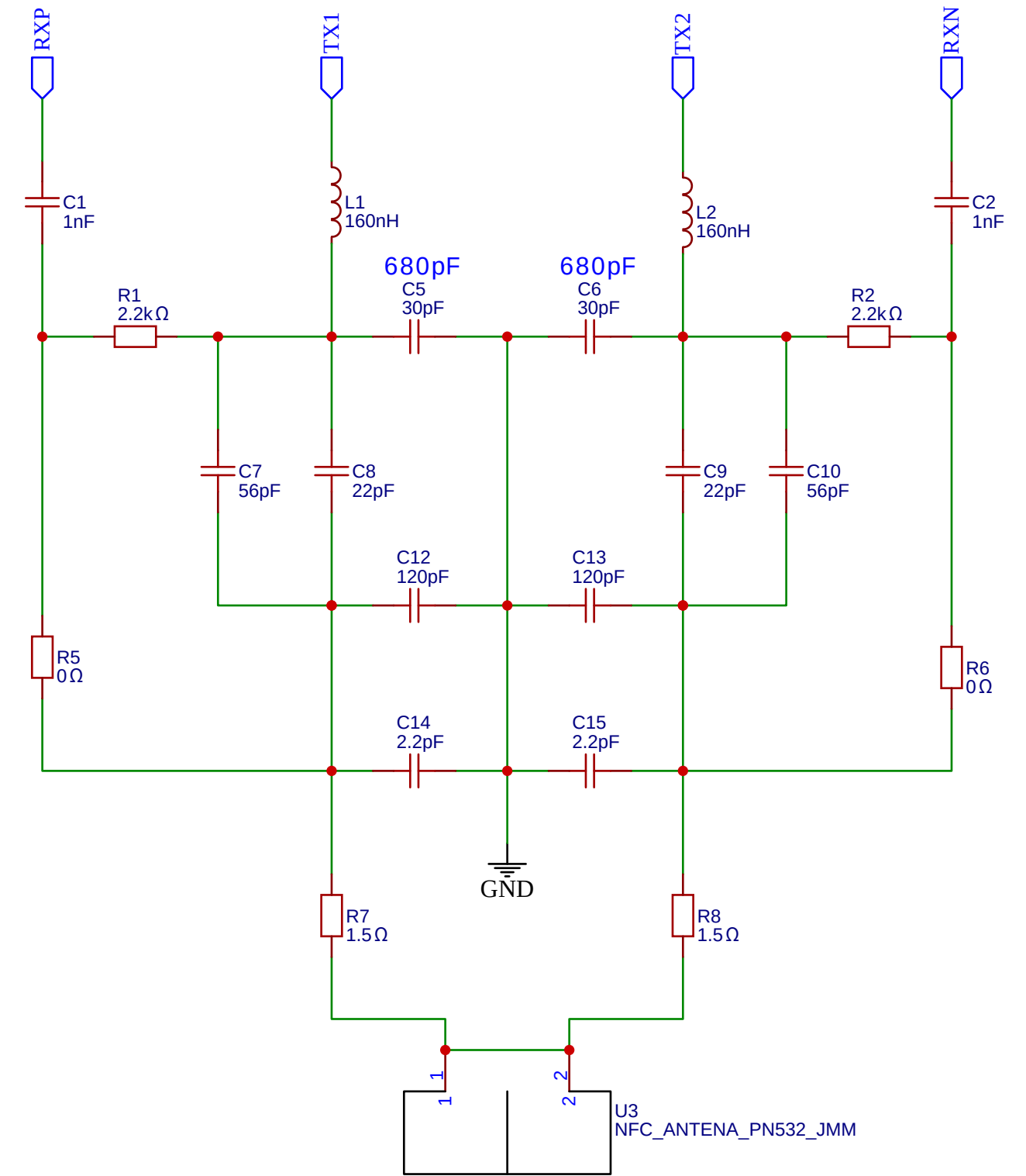


Table 13. I²C-bus interface addressing

I2CADDR1	I2CADDR0	I ² C-bus address (R/W = 0, write)	I ² C-bus address (R/W = 1, read)
0	0	0x50	0x51
0	1	0x52	0x53
1	0	0x54	0x55
1	1	0x56	0x57