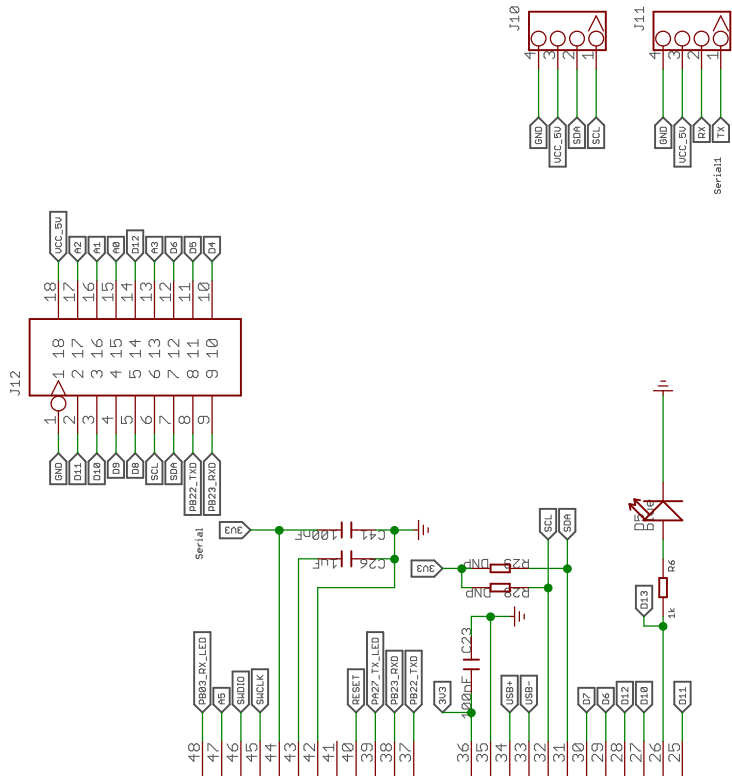
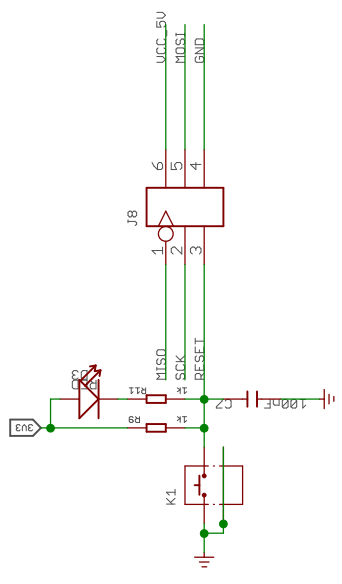


1 2 3 4 5 6



1	PA0/XIN32/EIC/SCOM1PAD0/TC2	PB3/EIC/AIN11/SCOM5PAD1
2	PA1/XOUT32/EIC/SCOM1PAD1/TC2	PB2/EIC/AIN10/SCOM5PAD0
3	PA2/EIC/AIN0	PA31/EIC/SCOM1PAD3/TC1
4	PA3/EIC/REF/AIN1	PA30/EIC/SCOM1PAD2/TC1
5	GNDANA	UDDIN
6	GNDANA	UDDCORE1
7	PB8/EIC/AIN2/SCOM4PAD0/TC4	GND3
8	PB9/EIC/AIN3/SCOM4PAD1/TC4	PA28/EIC
9	PA4/EIC/REF/AIN4/SCOM0PAD0/TC0	RESET
10	PA5/EIC/AIN5/SCOM0PAD1/TC0	PA27/EIC
11	PA6/EIC/AIN6/SCOM0PAD2/TC1	PB23/RXD
12	PA7/EIC/AIN7/SCOM0PAD3/TC1	PB22/TXD
13	PA8/I2C/AIN16/SCOM2PAD0+/TC0	3u3
14	PA9/I2C/EIC/AIN17/SCOM2PAD1+/TC0	20mF
15	PA10/EIC/AIN18/SCOM2PAD2+/TC1	LC23
16	PA11/EIC/AIN19/SCOM2PAD3+/TC1	3u3
17	UDDIO1	PA25/EIC/SCOM3PAD3+/TC5
18	GND1	PA24/EIC/SCOM3PAD2+/TC5
19	PB10/EIC/SCOM4PAD2/TC5	PA23/I2C/EIC/SCOM5PAD0+/TC4
20	PB11/EIC/SCOM4PAD3/TC5	PA22/I2C/EIC/SCOM5PAD1+/TC4
21	PA12/I2C/EIC/SCOM2PAD0+/TC2	PA21/EIC/SCOM5PAD3+
22	PA13/I2C/EIC/SCOM2PAD1+/TC2	PA20/EIC/SCOM5PAD2+
23	PA14/XIN/EIC/SCOM2PAD2+/TC3	PA19/EIC/SCOM1PAD3+/TC3
24	PA15/XOUT/EIC/SCOM2PAD3+/TC3	PA18/EIC/SCOM1PAD2+/TC3
		PA17/I2C/EIC/SCOM1PAD1+/TC2
		PA16/I2C/EIC/SCOM1PAD0+/TC2



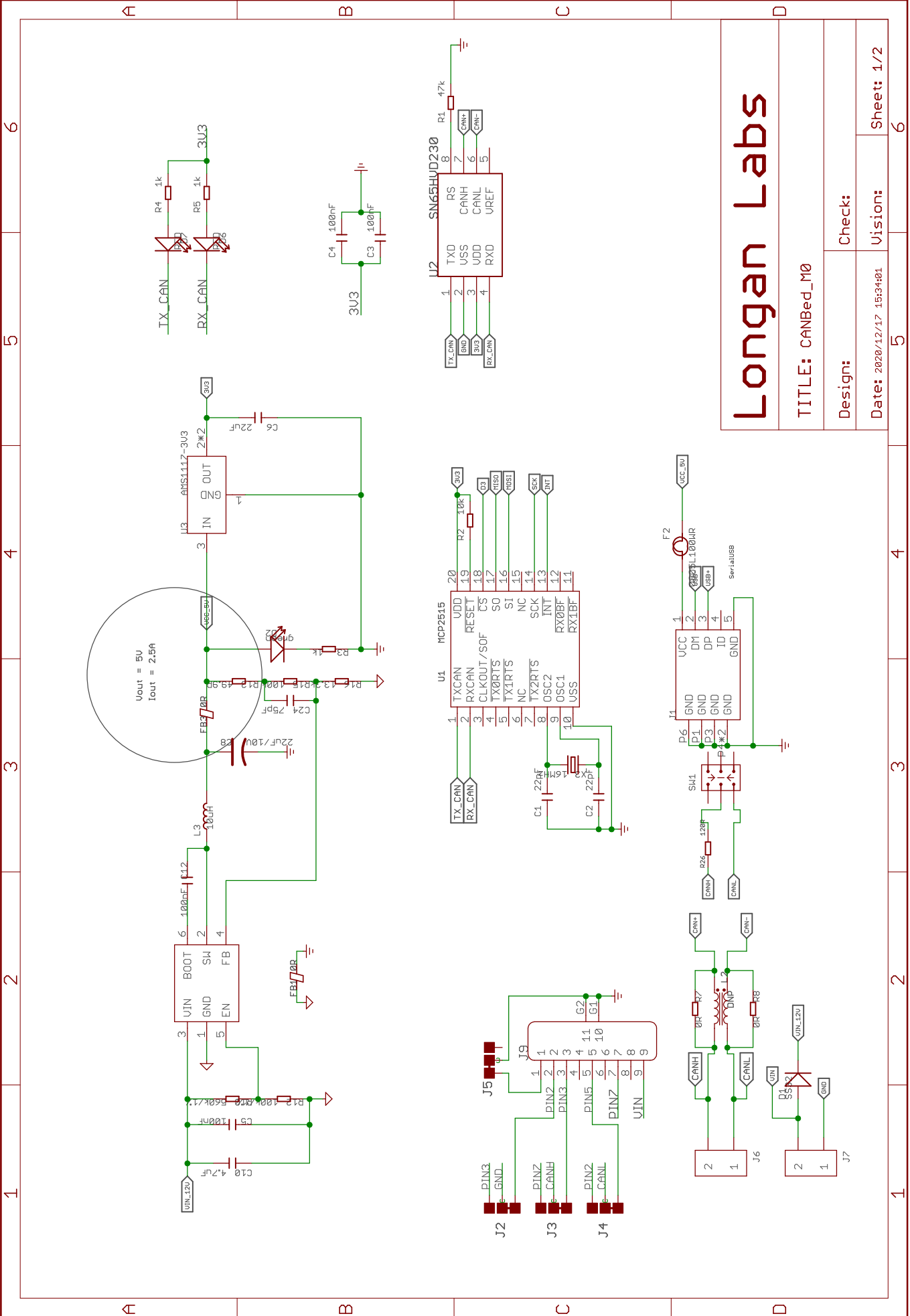
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Date: 2020/12/17 15:34:01 Vision: Sheet: 1/2

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TITLE: CANBed_M0

Design: Check:

Date: 2020/12/17 15:54:01 Vision: Sheet: 1/2

