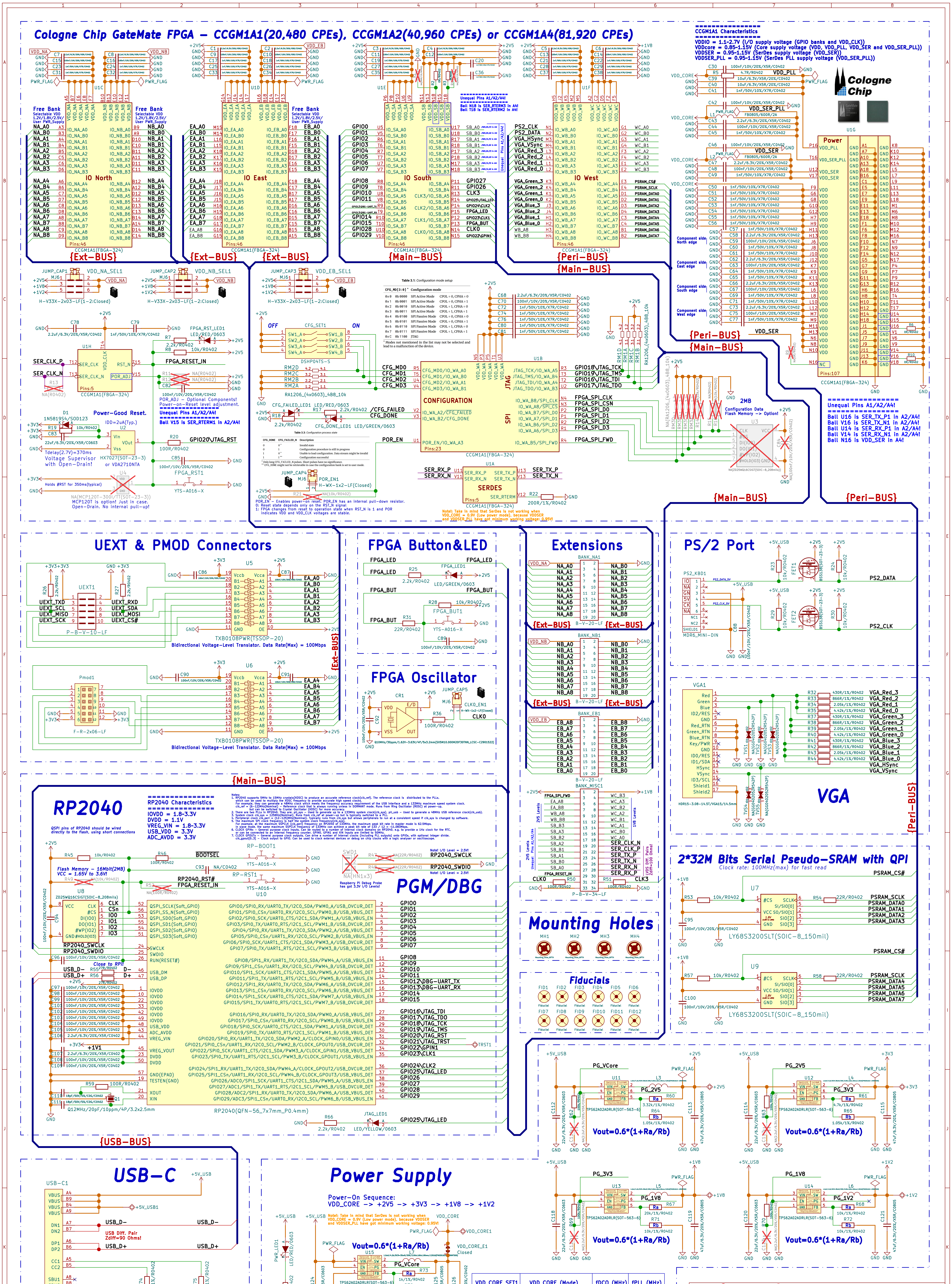

VDD_CORE_SET1	VDD_CORE (Mode)	fDCO (MHz)	fPLL (MHz)
Both Opened	0.90V (Low power mode)	500~1000	up to 250
1-2 Closed	1.00V (Economy mode)	1000~2000	up to 312.5
2-3 Closed	1.10V (Speed mode)	1250~2500	up to 416.75

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File: GateMateA1-EVB_Rev_C.kicad_sch

Title: GateMateA1-EVB

Size: A2 | Date: 2025-03-17 | Rev: C
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