

Maulana Abul Kalam Azad University of Technology, West Bengal
(Formerly West Bengal University of Technology)

Syllabus for B. Tech in CSE (Artificial Intelligence & Machine Learning)

(Applicable from the academic session 2020-2021)

**PRACTICAL
SYLLABUS
Semester III**

Analog & Digital Electronics Lab

Code: ESC-391

Contacts: 4P

Name of the Course:	Analog & Digital Electronics Lab
Course Code: ESC-391	Semester: III
Duration: 6 months	Maximum Marks: 100
Teaching Scheme:	
Theory: hrs./week	Continuous Internal Assessment
Tutorial: NIL	External Assessment: 60
Practical: 4 hrs./week	Distribution of marks: 40
Credit Points:	2

Laboratory Experiments:	
Analog Electronics	
1	Design a Class A amplifier
2	Design a Phase-Shift Oscillator
3	Design of a Schmitt Trigger using 555 timer
Digital Electronics	
4	Design a Full Adder using basic gates and verify its output / Design a Full Subtractor circuit using basic gates and verify its output.
5	Construction of simple Decoder & Multiplexer circuits using logic gates.
6	Realization of RS / JK / D flip flops using logic gates
7	Design of Shift Register using J-K / D Flip Flop
8	Realization of Synchronous Up/Down counter
9	Design of MOD- N Counter
10	Study of DAC

Any experiment specially designed by the college

(Detailed instructions for Laboratory Manual to be followed for further guidance)
