

Maulana Abul Kalam Azad University of Technology, West Bengal
(Formerly West Bengal University of Technology)
Syllabus for B. Tech in CSE (Artificial Intelligence & Machine Learning)
 (Applicable from the academic session 2020-2021)

Computer Organization
Code: PCC- CS302
Contacts: 3L

Name of the Course:	Computer Organization	
Course Code: PCC- DS301	Semester: III	
Duration:6 months	Maximum Marks: 100	
Teaching Scheme		Examination Scheme
Theory: 3 hrs./week		Mid Semester exam: 15
Tutorial: NIL		Assignment and Quiz : 10 marks
		Attendance: 5 marks
Practical: hrs./week		End Semester Exam: 70 Marks
Credit Points:	3	

Unit	Content	Hrs/Unit	Marks/Unit
1	Basic organization of the stored program computer and operation sequence for execution of a program. Role of operating systems and compiler/assembler. Fetch, decode and execute cycle, Concept of operator, operand, registers and storage, Instruction format. Instruction sets and addressing modes. [7L] Commonly used number systems. Fixed and floating point representation of numbers.[1L]	8	
2	Overflow and underflow. Design of adders - ripple carry and carry look ahead principles. [3L] Design of ALU. [1L] Fixed point multiplication -Booth's algorithm. [1L] Fixed point division - Restoring and non-restoring algorithms. [2L] Floating point - IEEE 754 standard. [1L]	8	
	Memory unit design with special emphasis on		
3	implementation of CPU-memory interfacing. [2L] Memory organization, static and dynamic memory, memory hierarchy, associative memory. [3L] Cache memory, Virtual memory. Data path design for read/write access. [5L]	10	
4.	Design of control unit - hardwired and microprogrammed control. [3L] Introduction to instruction pipelining. [2L] Introduction to RISC architectures. RISC vs CISC architectures. [2L] I/O operations - Concept of handshaking, Polled I/O, interrupt and DMA. [3L]	10	

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Text book and Reference books:

1. Mano, M.M., “Computer System Architecture”, PHI.
2. Behrooz Parhami “Computer Architecture”, Oxford University Press
3. Hayes J. P., “Computer Architecture & Organisation”, McGraw Hill,
4. Hamacher, “Computer Organisation”, McGraw Hill,
5. N. senthil Kumar, M. Saravanan, S. Jeevananthan, “Microprocessors and Microcontrollers” OUP
6. Chaudhuri P. Pal, “Computer Organisation & Design”, PHI,
7. P N Basu- “Computer Organization & Architecture” ,Vikas Pub
8. Rajaraman – “Computer Organization & Architecture”, PHI
9. B.Ram – “Computer Organization & Architecture”, Newage Publications

Course Outcomes:

On completion of the course students will be able to

- PCC-CS302.1 Understand basic structure of digital computer, stored program concept and different arithmetic and control unit operations.
- PCC-CS302.2 Understand basic structure of different combinational circuits- multiplexer, decoder, encoder etc.
- PCC-CS302.3 Perform different operations with sequential circuits.
- PCC-CS302.4 Understand memory and I/O operations.