

Opcodes' tables **Compact**

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1 grab its content via: *svn export https://corkami.googlecode.com/svn/trunk/oOo/opcodes_tables_compact*
2 rezip with subdirectories as opcodes_tables_compact.ods

opcodes tables are available as compact and complete form.

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	x0	x1	x2	x3	x4	x5	x6	x7	x8	x9	xA	xB	xC	xD	xE	xF	
0x	nop	aconst_null	*m1	*0	*1	iconst_*		*2	*3	*4	*5	*0	iconst_*	fconst_*	dconst_*	*0	
1x	*ipush	b* s*	i* 1	*w 2	*2_w 2	ldc*	i*	l*	f*	d*	a*	*0	iload_*	iload_*	lload_*	*1	
2x	...lload_*		*2	*3	fload_*		i*	l*	dload_*		*3	*0	aload_*	aload...		*1	
3x	...*aload		f*	d*	a*	b*	c*	s*	i*	l*	*store		*1	istore_*	istore_*	*0	
4x	...lstore_*		*1	*2	*3	fstore_*		*0	*1	*2	*3	*0	astore_*	astore_*	i*	*astore...	
5x	...*astore		i*	f*	d*	a*	b*	c*	s*	pop*		dup*		swap		swap	
6x	*add				*sub				*mul				*div				
7x	*rem				*neg				*shl		*shr		*ushr		*and		
8x	*or		*xor		iinc	i2*		i2*		I2*		f2*		d2*		d2*	
9x	*f	*b	*c	*s	lcmp	fcmp*		dcmp*		if*		if_icmp*		if_icmp*		if_icmp*	
Ax	...if_icmp*				*eq		*ne		*lt		*ge		*gt		*le		
Bx	...*return				a*		*static		*field		invoke*		switch		*return...		
Cx	check	instance of	monitor*	(pref.)	wide	multi	if*	*null	*nonnull	goto_w	jsr_w	break	point	*newarray	array length	athrow	
Fx	impdep*															*1	

misc	arithmetic	flow
constants	logical	method
immediates	conversion	object
locals	comparison	system
stack	conditional	undefined

JVM (Java)

	x0	x1	x2	x3	x4	x5	x6	x7	x8	x9	xA	xB	xC	xD	xE	xF	
0x	nop	break	*0	Idarg.*			*0	Idloc.*			*0	*1	*2	*3	*.s	*.a.s	
1x	starg.s	Idloc *.s *a.s		stlock.s	Idnull	*m1	*0	*1	*2	*3	Idc.i4.*			*4	*5	*6	
2x	Idc.* *i4 *i8 *r4 *r8				dup	pop	jmp	call* * *i		ret	br* .s *false.s		*true.s	b* eq ge		b*.s...	
3x	...b*.s gt le lt		b*.un.s ne ge gt			le	lt	br * *false		*true	*eq	*ge	*gt	*le	*lt		
4x	b*.un ne ge gt			le lt		switch	*i1	*u1	*i2	*u2	*i4	*u4	*i8	*i	*r4	*r8	
5x	...Idind.* *ref		*ref	*i1	*i2	*i4	*i8	*r4	*r8	add	sub	mul	div* * *.un		rem*	*.un	and
6x	or	xor	shl	shr * *.un		neg	not	*i1	*i2	*i4	*i8	*r4	*r8	*u4	*u8	callvirt	
7x	cobj	Idobj	Idstr	newobj	cast class	isinst	conv.r.un			unbox	throw	Idfld* * *a		stfld	Idsfld* * *a		
8x	stsfld	stobj	conv.ovf.i*.un 1 2 4 8			conv.ovf.u*.un 1 2 4 8			conv.ovf.*.un i u		box	newarr	Idlen	Idelema			
9x	Idelem.* *i1 *u1 *i2 *u2 *i4 *u4						*i8	*i	*r4	*r8	*ref	*i	*i1	*i2	*i4	*i8	
Ax	...stelem.* *r4 *r8 *ref			Idelem	stelem	unbox. any											
Bx				*i1	*u1	*i2	conv.ovf.* *u2 *i4		*u4	*i8	*u8						
Cx	ref anyval		ck finite			mkref any											
Dx	Idtoken	*u2	conv.* *u1 *i		conv.ovf.* *i *u		add.ovf* * *.un		mul.ovf* * *.un		sub.ovf* * *.un		end finally	leave* * *.s		stind.i	
Ex	conv.u																
FE	arglist		*eq	*gt	*gt.un	*lt	*lt.un	Id*ftn * virt		Idarg* * *a		starg	Idloc* * *a		stloc	localalloc	
FE			end filter	un aligned.	volatile.	tail.	init obj	constrained.		cpblk	init blk	no.	re throw	sizeof ref anytype		read only.	

misc	immediates	arithmetic	fields	prefix
system	stack	logical	array	
args	method	conversion	references	
locals	conditional	object	flow	
constants	indirects	exception	comparison	undefined

Common Intermediate Language (.Net)

	x0	x1	x2	x3	x4	x5	x6	x7	x8	x9	xA	xB	xC	xD	xE	xF				
0x	nop	*	move*	/from16	/16	*	move-wide*	/from16	/16	*	move-object*	/from16	/16	*	-wide	-object	move-exception	return*	-void	*
1x	return*-wide	-object	/4	/16	*	/high16	/16	/32	*	/high16	const-string	*-jumbo	const-class	monitor-enter	exit	check cast				
2x	instance of	array length	new*	*instance	*array	filled-new-array	*-range	fill-array-data	throw	*	goto	/16	/32	*-switch packed*	sparse*	cmp*-float	cmp*-double	l g	l	
3x	cmp*-double	cmp-long	eq	ne	lt	ge	gt	le	eq	ne	lt	ge	gt	le						
4x									aget								aput...			
5x	...aput	-char	-short	*	-wide	-object	-bool	-byte	-char	-short	*	-wide	-object	-bool	-byte	-char	-short			
6x					sget						sput						invoke-*...	virtual	super	
7x	...invoke-*	-direct	-static	-interface		virtual	super	-direct	-static	-interface				neg*	not*	neg*	not*	neg*...		
8x	...neg*	-double	int-to-*	long	float	double	int	float	double	int	long-to-*	float	double	float-to	double	double-to*	int-to*	char	short	
9x											*-int						*-long...			
Ax	add	sub	mul	div	rem	and	or	xor	shl	shr	ushr			add	sub	mul	div	rem		
Bx	...*-long	and	or	xor	shl	shr	ushr		add	sub	mul	div	rem				*-double			
Cx																		*-long/2addr...		
Dx																			*-long/2addr	
Ex																				
Fx	invoke-direct-empty			iget-*quick	*-wide	-object		*-wide	-object	virtual	input-*quick			invoke-*quick			execute inline			
	misc		object		conversion															
	moves		flow		arithmetic															
	method		conditional																	
	literals		transfer																	
	system		logical		undefined															

Dalvik Virtual Machine (android)

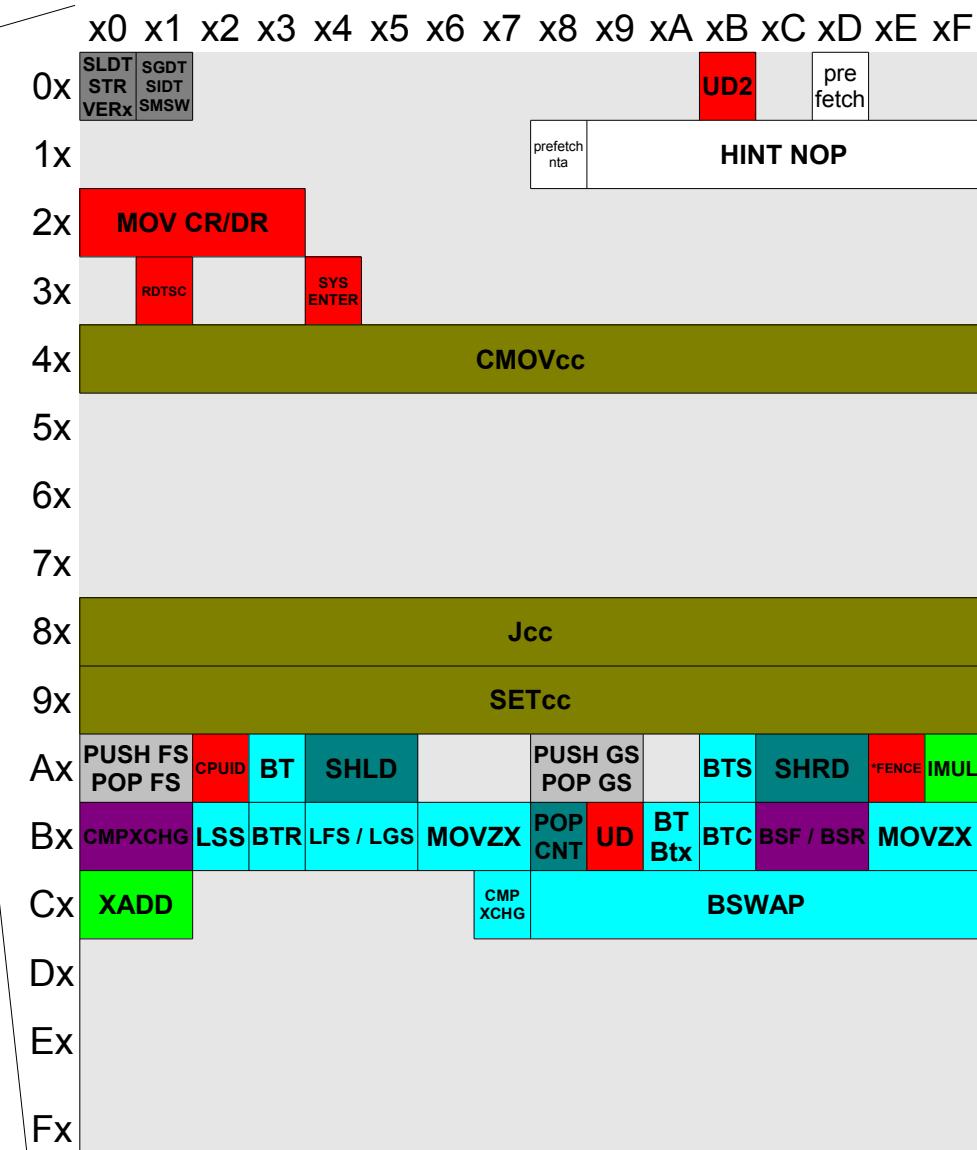
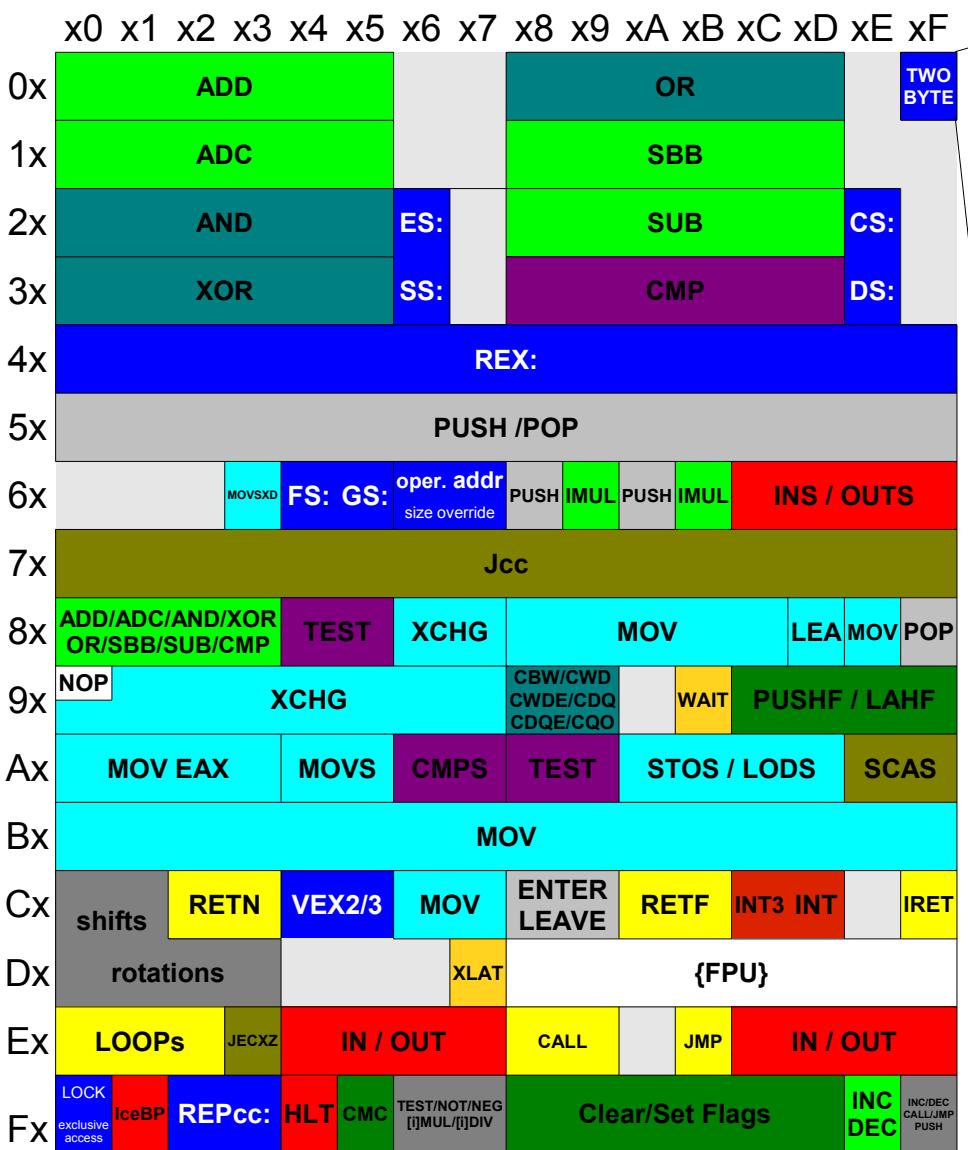
	x0	x1	x2	x3	x4	x5	x6	x7	x8	x9	xA	xB	xC	xD	xE	xF	
0x	ADD				PUSH/POP ES/SS			OR				PUSH CS	TWO BYTE				
1x	ADC							SBB				PUSH DS POP DS					
2x	AND				ES:	DAA		SUB				CS:	DAS				
3x	XOR				SS:	AAA		CMP				DS:	AAS				
4x	INC / DEC																
5x	PUSH /POP																
6x	PUSHA POPA	BOUND	ARPL	FS: GS:	oper. addr size override	PUSH	IMUL	PUSH	IMUL		INS / OUTS						
7x	Jcc																
8x	ADD/ADC/AND/XOR OR/SBB/SUB/CMP		TEST	XCHG	MOV				LEA	MOV	POP						
9x	NOP	XCHG				CBW/CWD CWDE/CDQ	CALL	WAIT	PUSHF / LAHF								
Ax	MOV EAX		MOVS	CMPS	TEST	STOS / LODS			SCAS								
Bx	MOV																
Cx	shifts	RETN	LES / LDS	MOV	ENTER LEAVE	RETF	INT3	INT	INTO	IRET							
Dx	rotations		AAM AAD	SALC	XLAT	{FPU}											
Ex	LOOPS		JECXZ	IN / OUT			CALL / JMP			IN / OUT							
Fx	LOCK exclusive access	IceBP	REPcc:	HLT	CMC	TEST/NOT/NEG [I]MUL/[I]DIV	Clear/Set Flags				INC DEC	INC/DEC CALL/JMP PUSH					

	x0	x1	x2	x3	x4	x5	x6	x7	x8	x9	xA	xB	xC	xD	xE	xF
	SLDT	SGDT														
	STR	SIDT														
	VERx	SMSW														
0x															UD2	pre fetch
1x																HINT NOP
2x																
3x															RDTSC	SYS ENTER
4x																CMOVcc
5x																
6x																
7x																
8x																Jcc
9x																SETcc
Ax	PUSH FS POP FS	CPUID	BT	SHLD					PUSH GS POP GS			BTS	SHRD	'FENCE	IMUL	
Bx	CMPXCHG	LSS	BTR	LFS / LGS	MOVZX	POP CNT	UD	BT Btx	BTC	BSF / BSR	MOVZX					
Cx	XADD				CMP XCHG											BSWAP
Dx																
Ex																
Fx																

misc	arithmetic	flow
memory/reg	logical	group
	flags	prefix
obsolete	comparison	system
stack	conditional	hidden

usermode only, no extra instruction set

x86



misc	arithmetic	flow
memory/reg	logical	group
	flags	prefix
obsolete	comparison	system
stack	conditional	hidden

usermode only, no extra instruction set

x86-64

Android 1 <http://source.android.com/tech/dalvik/dalvik-bytecode.html>

2 http://pallergabor.uw.hu/androidblog/dalvik_OPCODES.html

.Net 1 <http://www.ecma-international.org/publications/standards/Ecma-335.htm> p355-469

2 <http://www.asukaze.net/etc/cil/opcode.html>

Java http://java.sun.com/docs/books/jvms/second_edition/html/Instructions.doc.html

x0

0x

SubLeq

SubLeq