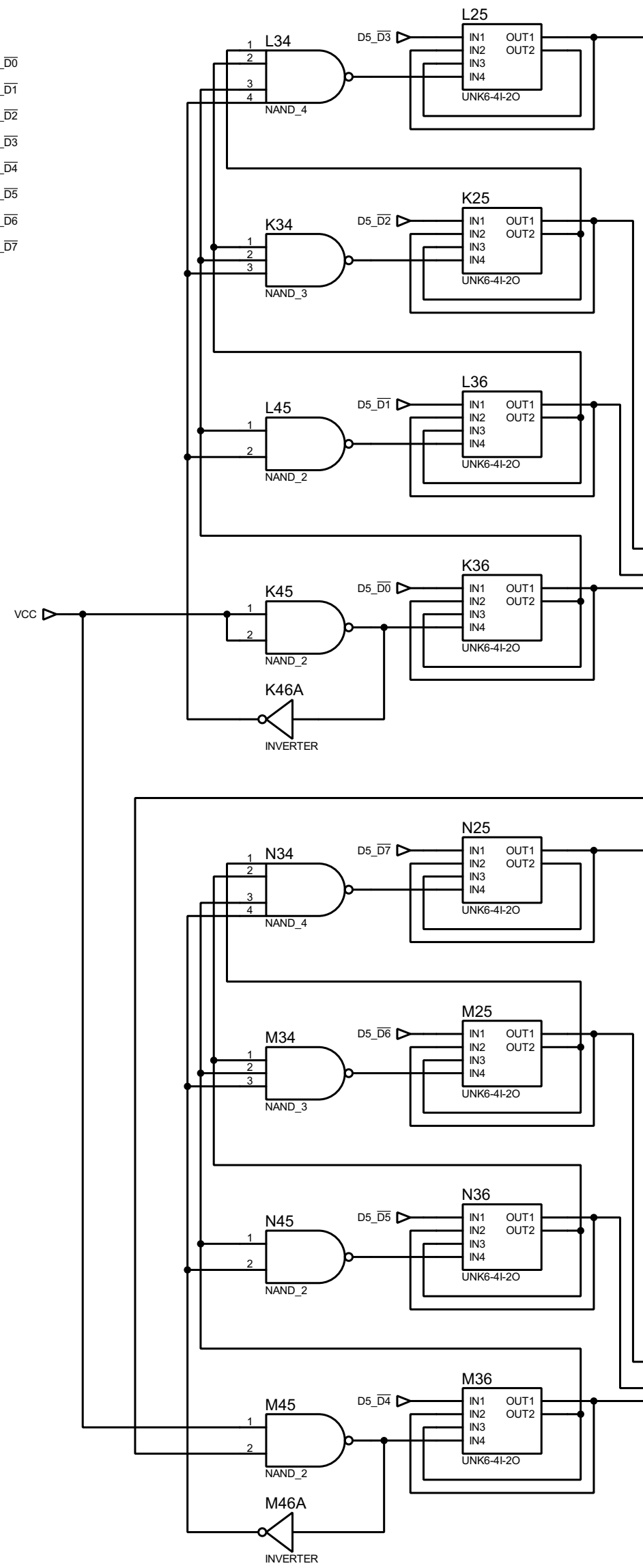
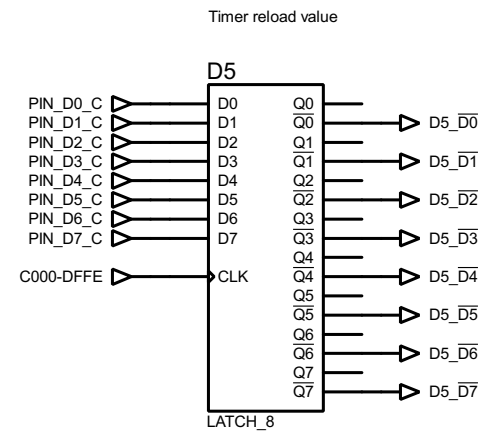


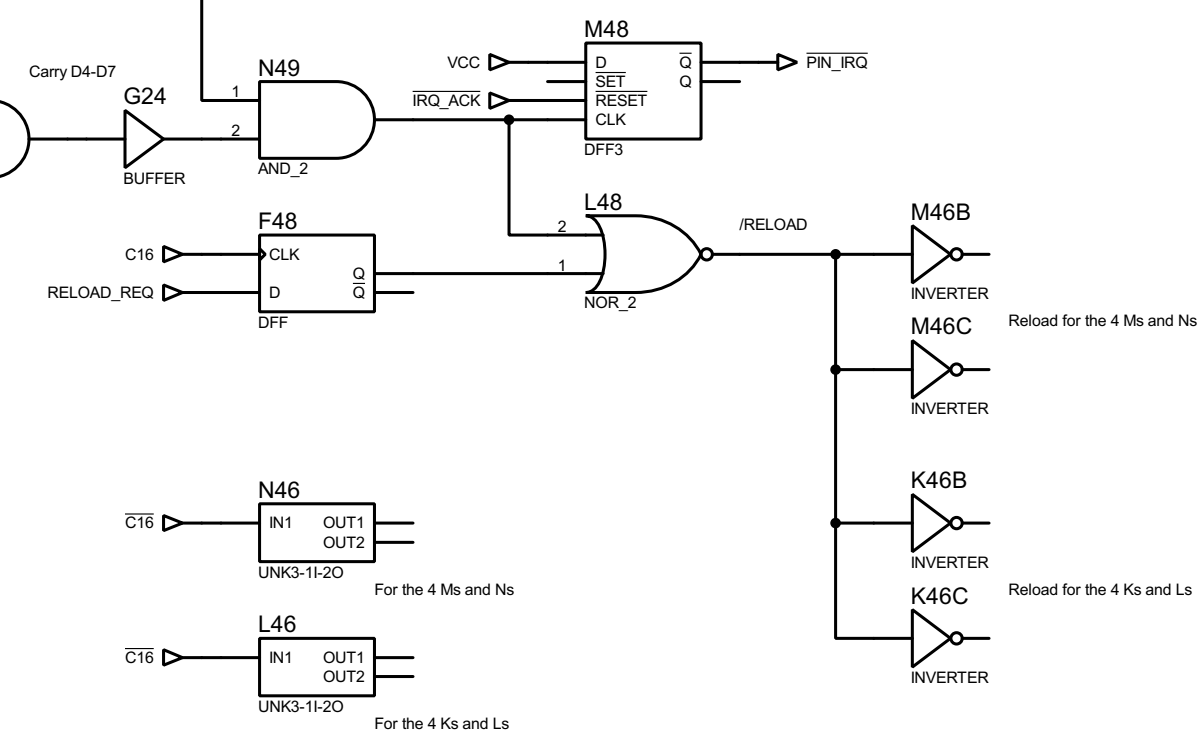
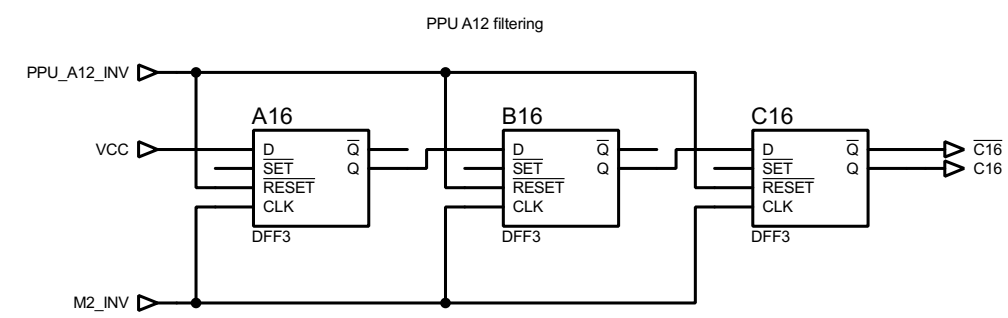
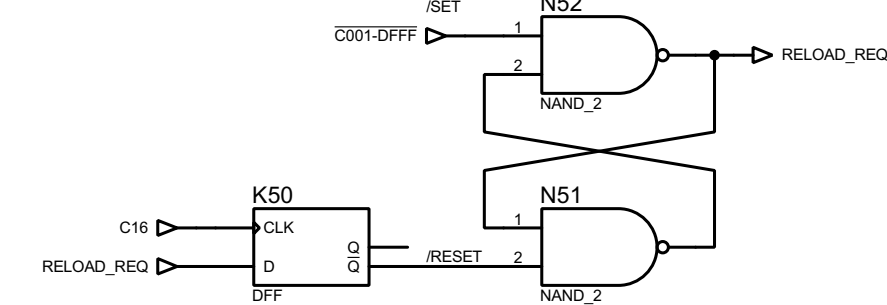
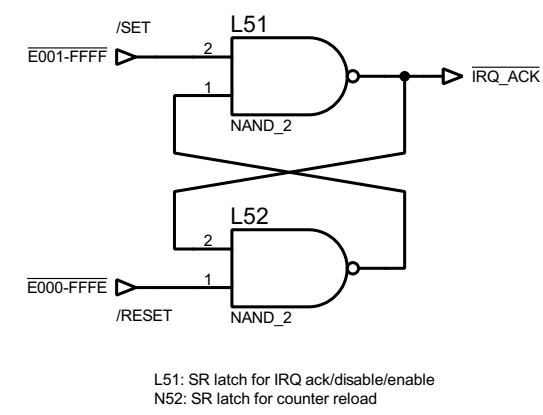
FILE NAME:	MMC3C_RE.pdsprj	DATE:	22/12/2020
DESIGN TITLE:	MMC3C	PAGE:	3 of 4
BY:	Sean Gonsalves	REV:	A



#### NOTE ABOUT UNK6 CELLS:

These may be JK FFs or special cells designed to build 4-bit presettable counter blocks. Please see UNK6\_study.png for a transistor-level view.

The /C16 clock and /RELOAD signals are distributed to all of them.



FILE NAME: <b>MMC3C_RE.pdsprj</b>	DATE: <b>22/12/2020</b>
DESIGN TITLE: <b>MMC3C</b>	PAGE: <b>4 of 4</b>
TIMER & IRQ CTRL	
BY: Sean Gonsalves	REV: A