



Diagram illustrating the connection between the `analogue_backend_1` component and the `interface` component.

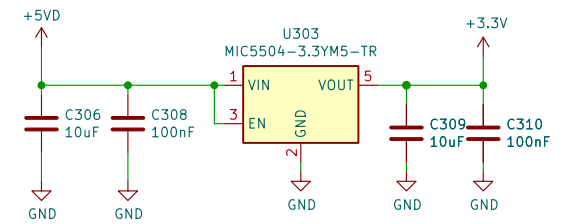
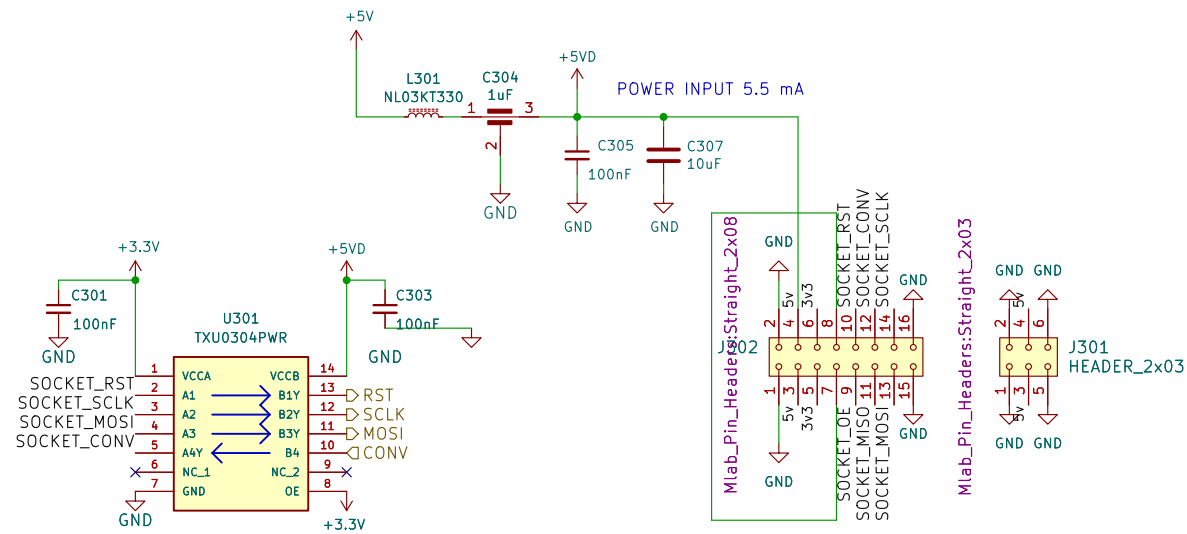
The `analogue_backend_1` component (left) has pins: SCLK, SD0, SD1, CONV, and RST. The `interface` component (right) has pins: SCLK, MISO, MOSI, CONV, and RST.

Connections are shown as green lines:

- SCLK (analogue_backend_1) to SCLK (interface)
- SD0 (analogue_backend_1) to MISO (interface)
- SD1 (analogue_backend_1) to MOSI (interface)
- CONV (analogue_backend_1) to CONV (interface)
- RST (analogue_backend_1) to RST (interface)

File: analogue_backend.kicad_sch

File: interface.kicad_sch



Title: PCRD08

Analogue backend with SPI interface

Author: KubFire <info@ust.cz>

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