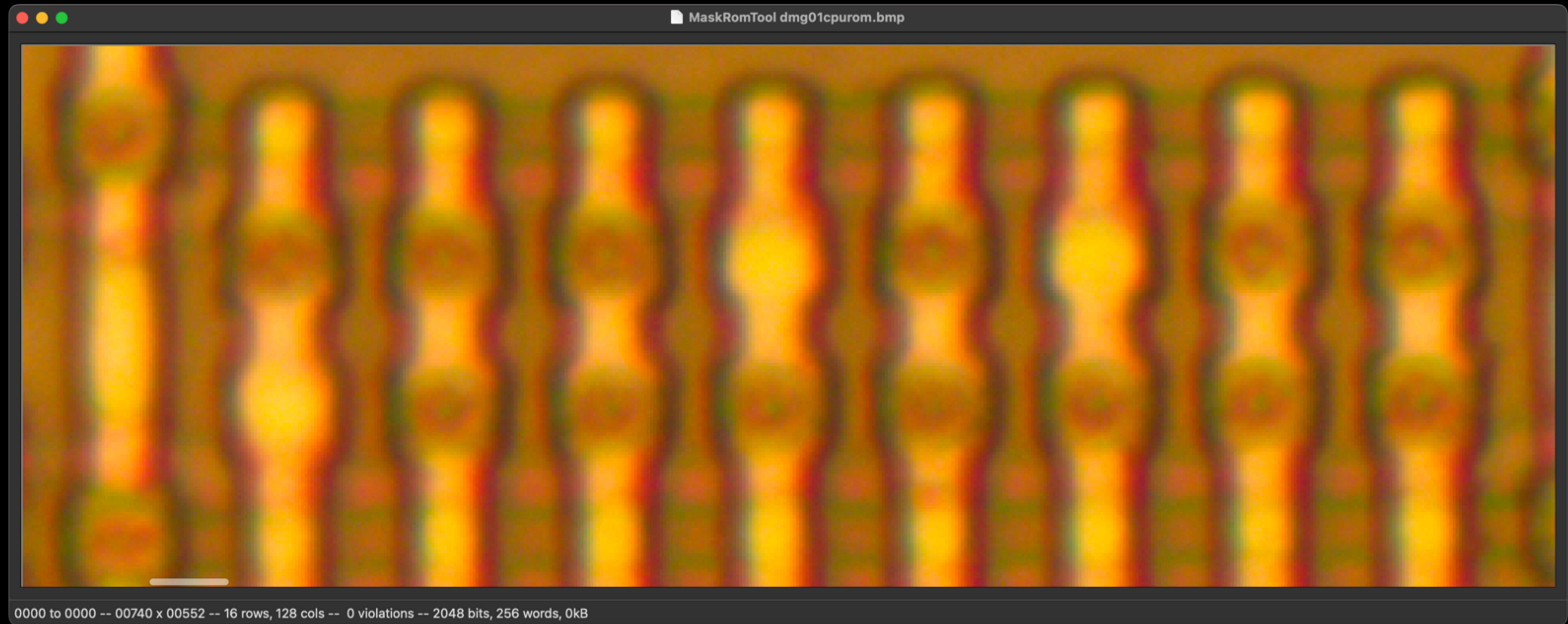
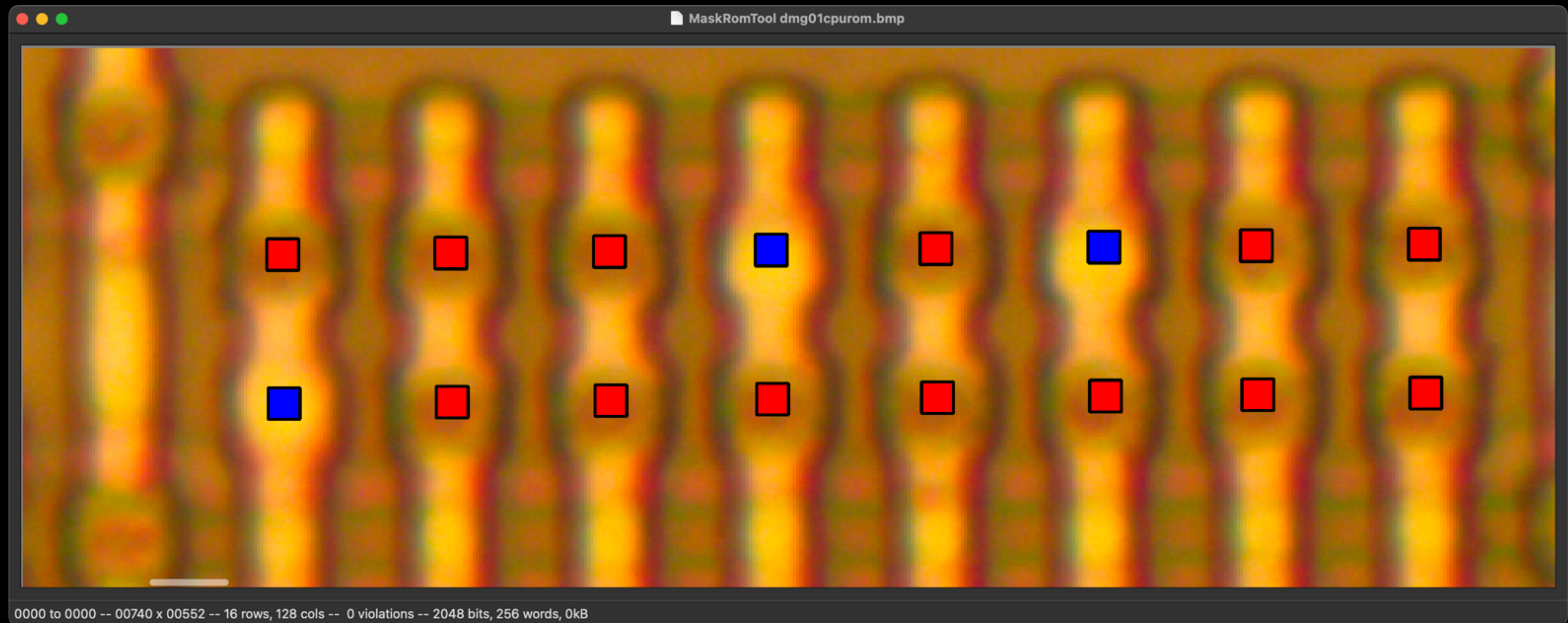


# GameBoy ROM Extraction!





# GameBoy ROM Extraction!





# GameBoy ROM Extraction

- First, some theory:
  - What's a mask ROM?
  - How can we identify thousands of bits?
  - How can we convert those bits to bytes?
- Second, some practice:
  - Nintendo's Game Boy ROM: 2048 bits.



# Software and target!

- <https://github.com/travisgoodspeed/maskromtool/>
  - Latest release for Windows or macOS.
  - Build from source with Qt6 for Linux.
- <https://github.com/travisgoodspeed/gbrom-tutorial>
  - Clone this locally, follow instructions in README.
- WIFI: UPCguest



# Flash ROM, EEPROM, and Mask ROM

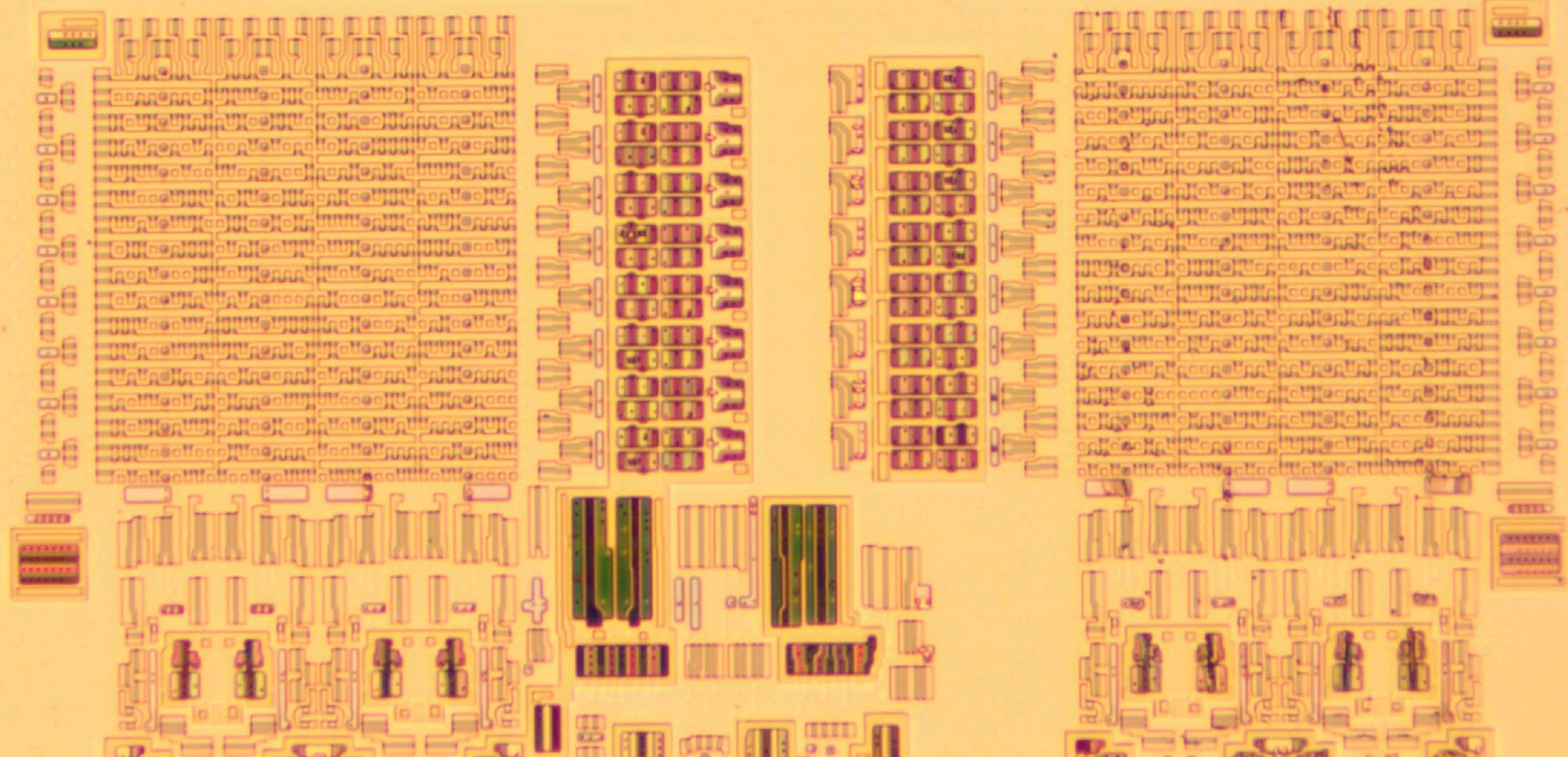
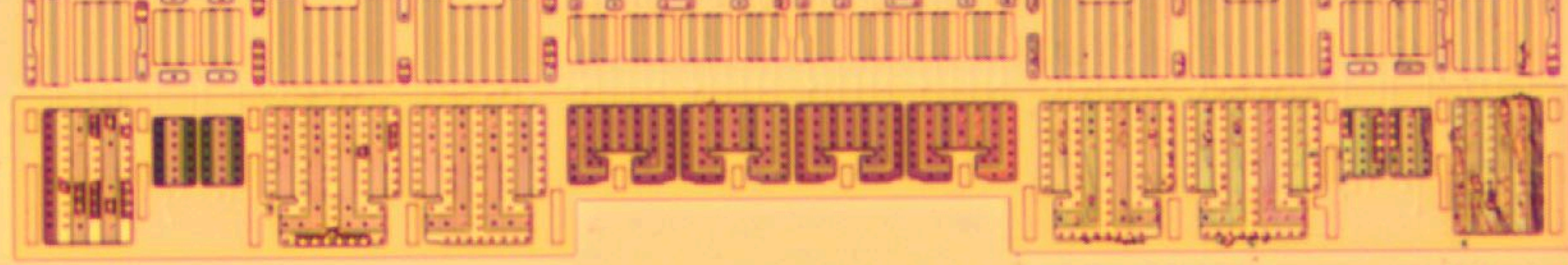
- Flash ROM and EEPROM
  - Electrically, Individually Programmable
  - Great for small quantities of code.
- Mask ROM
  - Mask Programmed
  - Only for very large quantities.



# Mask ROMS

- Mask Programmed at the Factory
- Contain code, data, or microcode.
- Often require chemical processing.
- Good targets for reverse engineering:
  - Video Games, Copy Protection
  - Cryptography









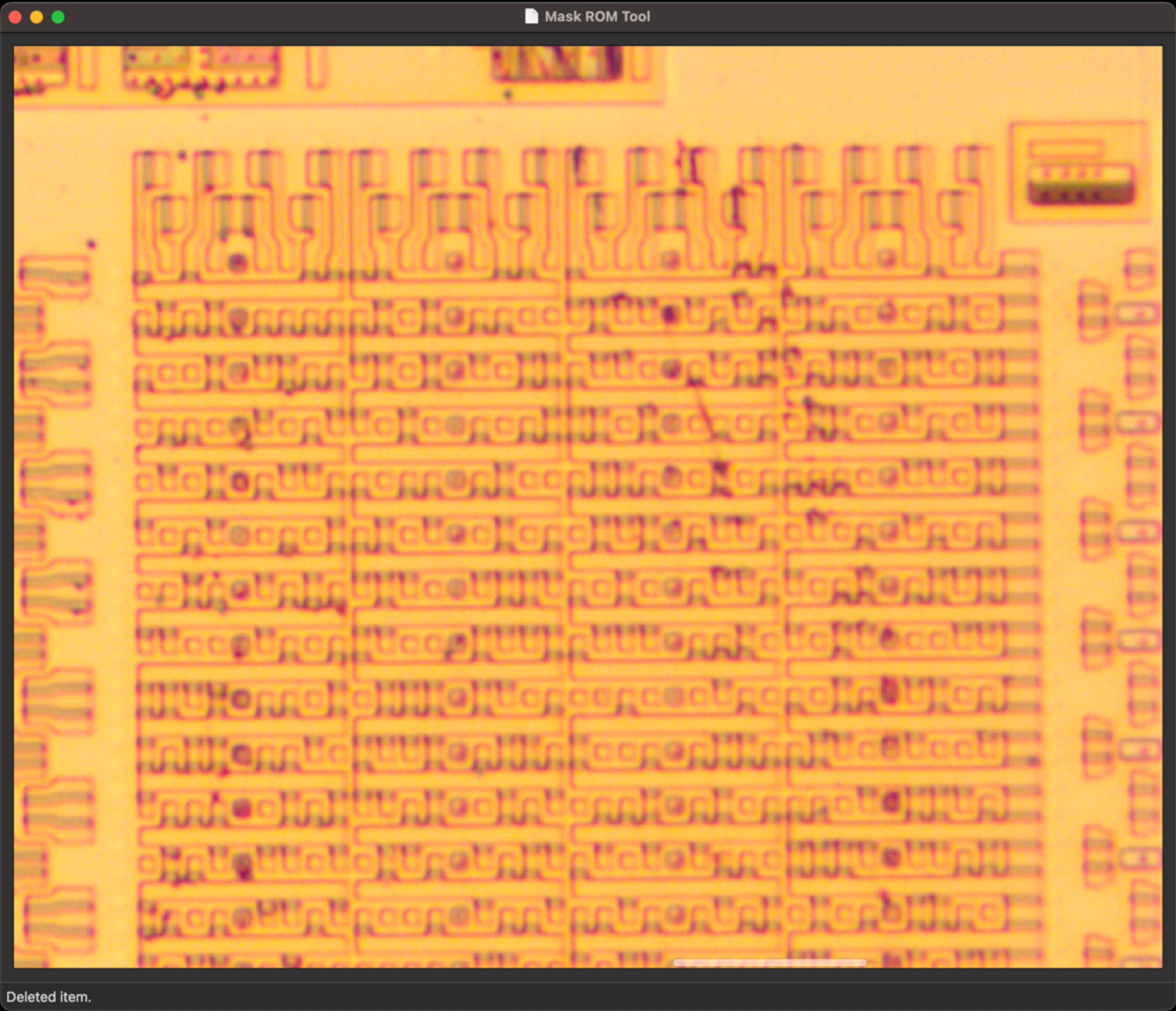
Handwritten text in a vertical column on the right side of the page, likely a title or a list of items.

Main body of handwritten text in a vertical column, consisting of multiple lines of script.

A vertical column of text or markings located between the main body of text and the left margin.

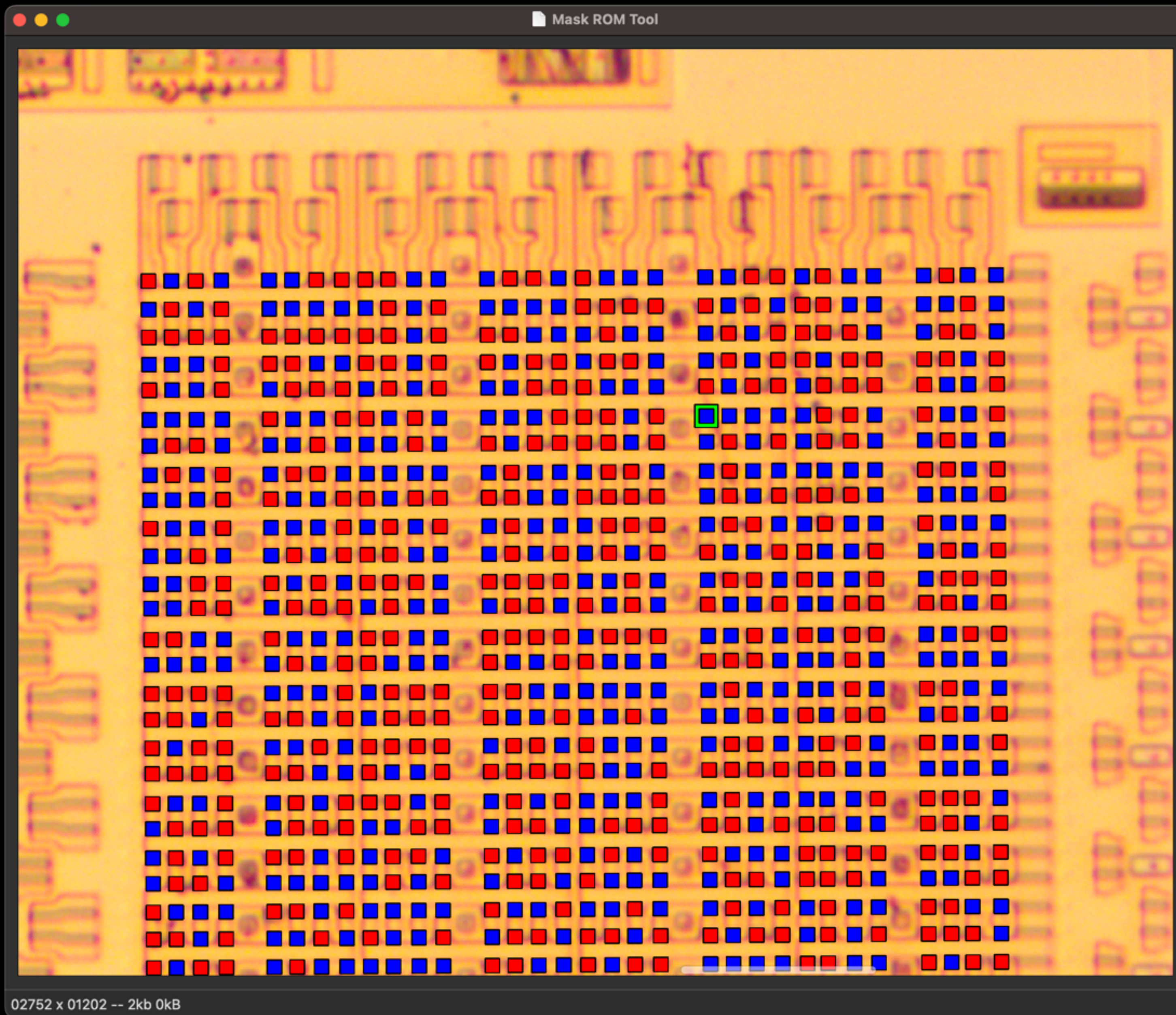
A vertical column of text or markings on the far left side of the page, possibly a list or index.





Deleted item.



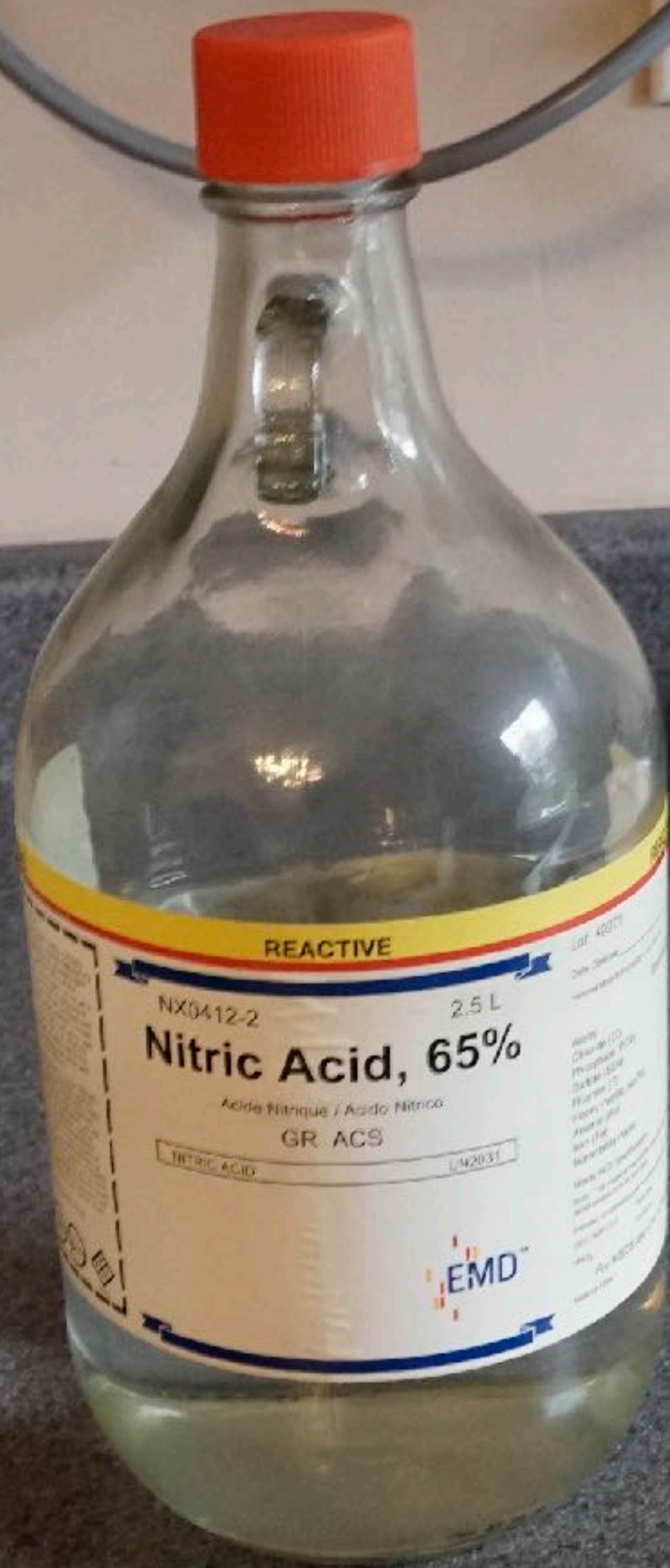




# A Sidebar into Chemistry

- Depackage the chip with 65%  $\text{HNO}_3$  and heat.
- Delayer the chip with  $\text{HF}$ .
- Stain the ROM with a Dash Etch.









LabDIRECT LLC

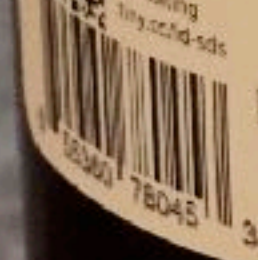
RED FUMING NITRIC ACID, 90%+  
RFNA, <12% NO<sub>2</sub>, UNINHIBITED  
HNO<sub>3</sub>

UN 2032  
250mL  
CAS# 7697-37-2  
EC# 231-714-2

Technical Grade  
SG: 1.50  
MW: 63.01  
LOT#: 02111-5-RFN

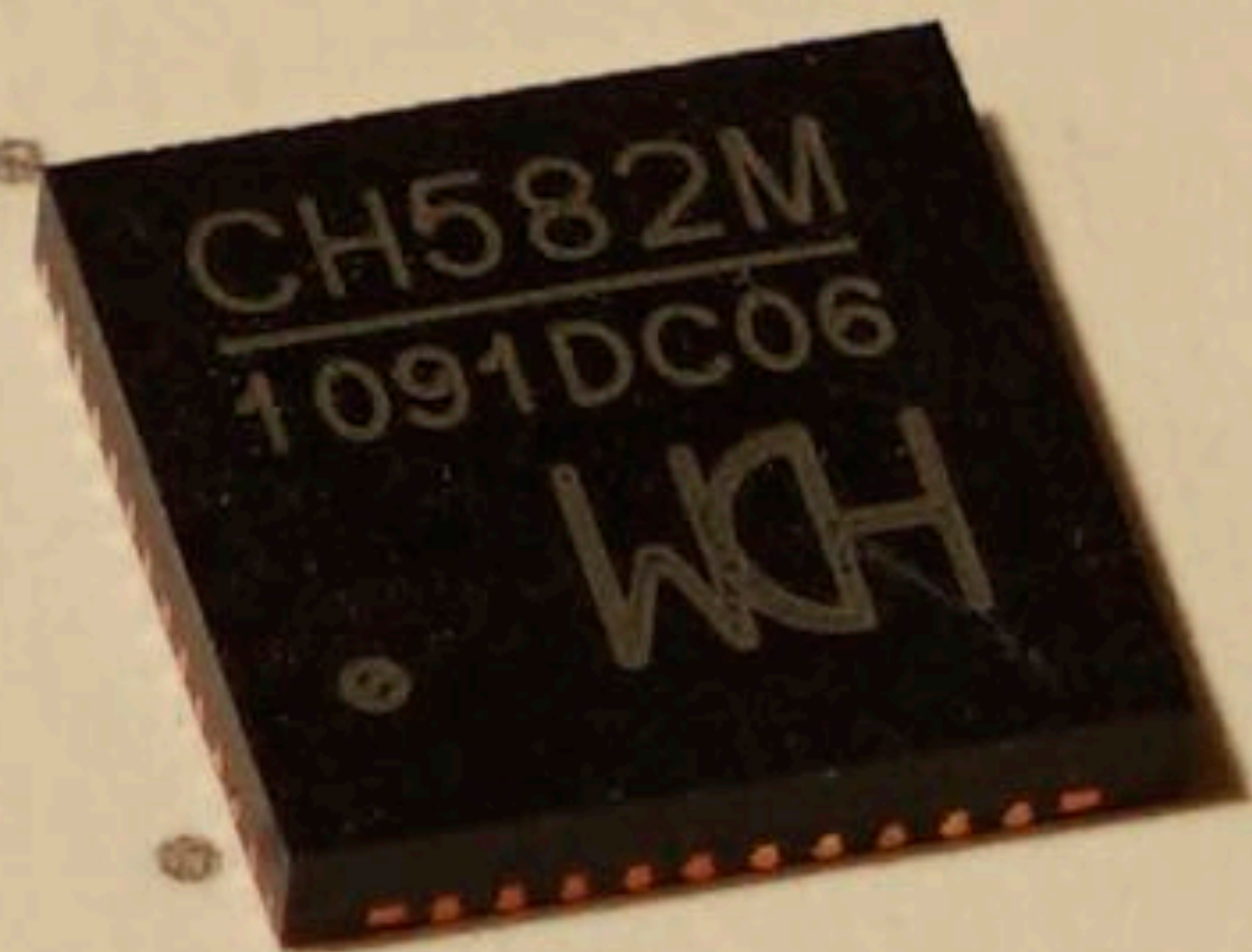
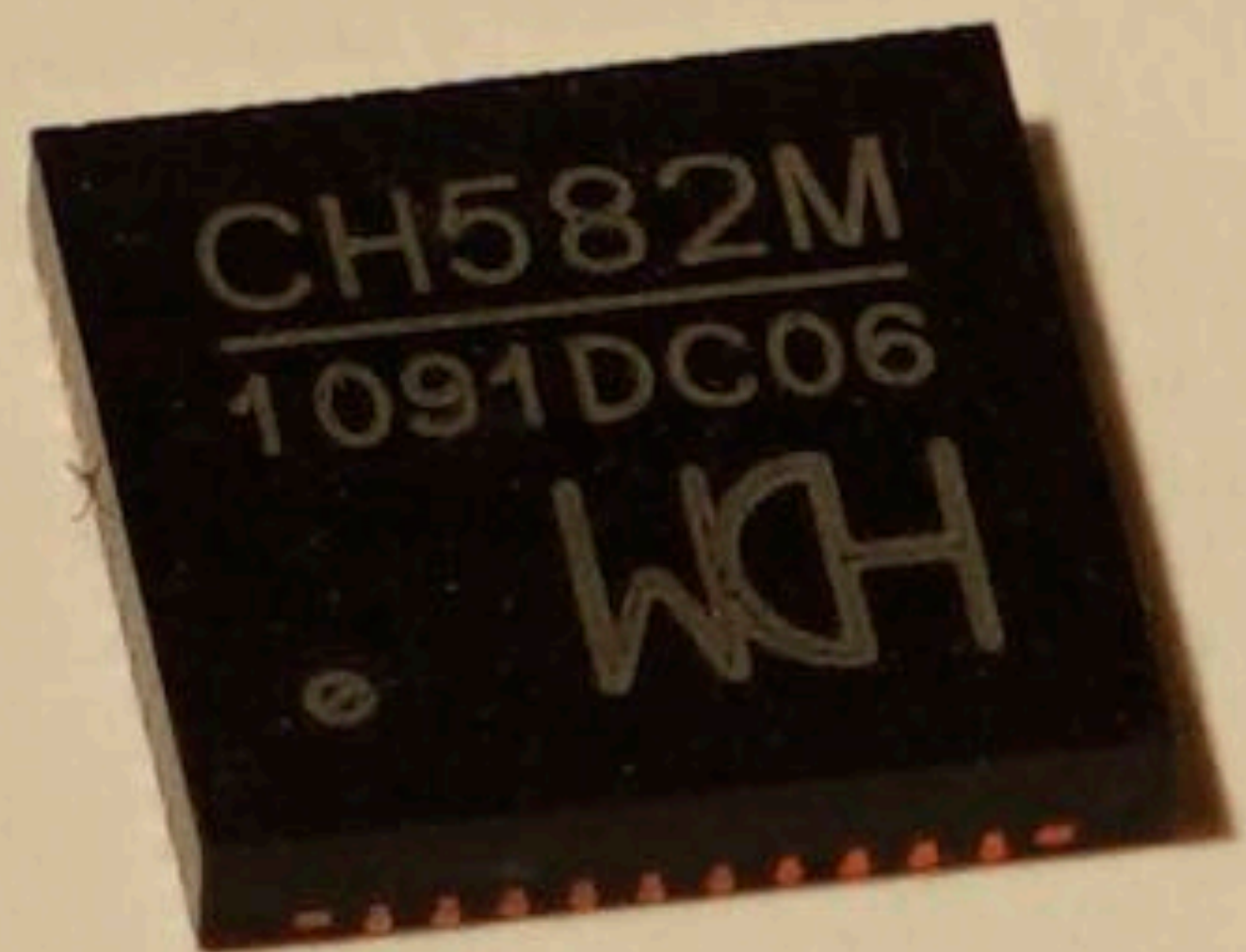
Packing Date: 3 of Mar 20 22  
Use within 3 years

Date Opened:



ACID / CORROSIVE / REACTIVE  
Storage: White  
Hazard: White  
Oxidizer













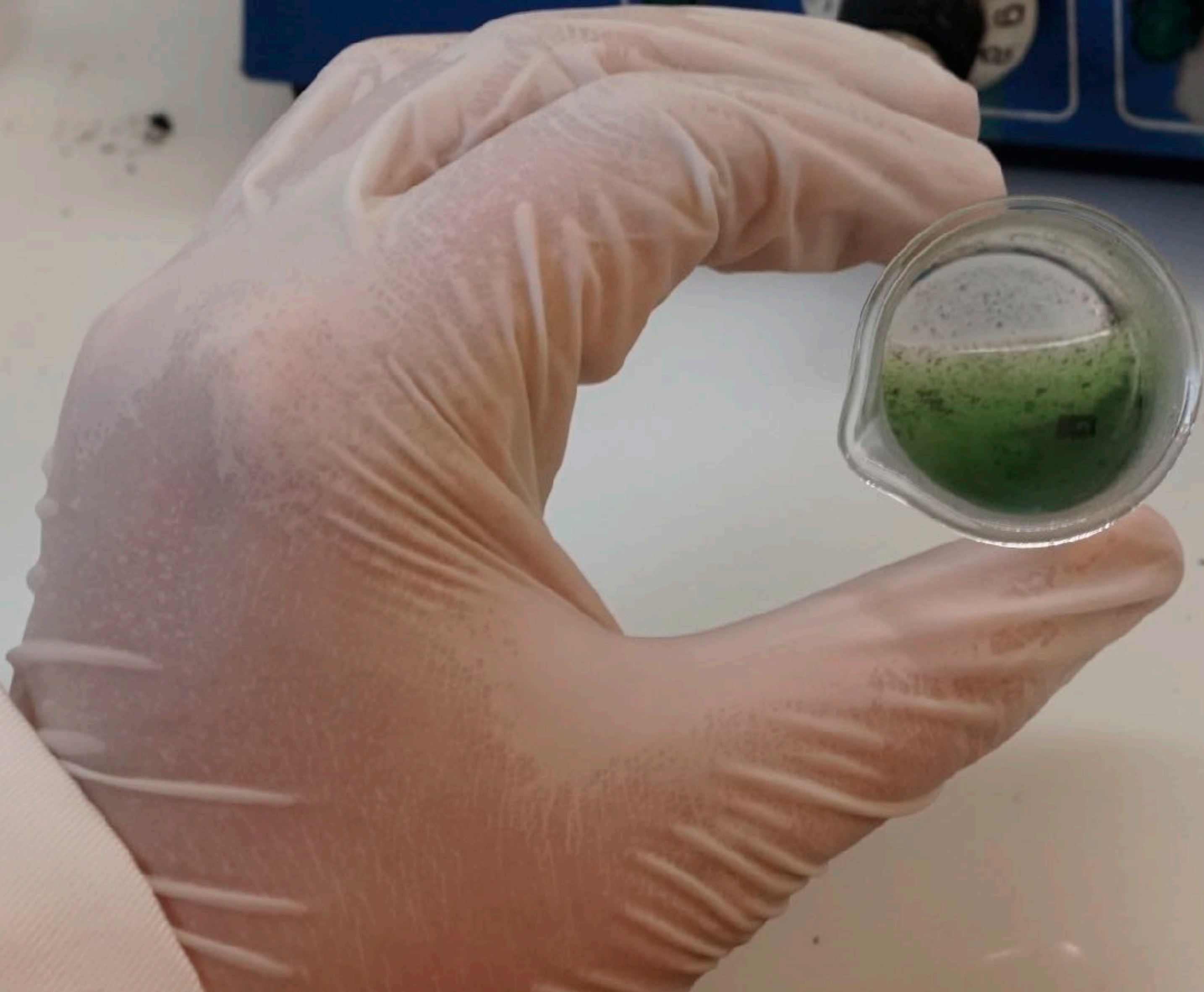


VWR

MODEL 320

STIR

HEAT











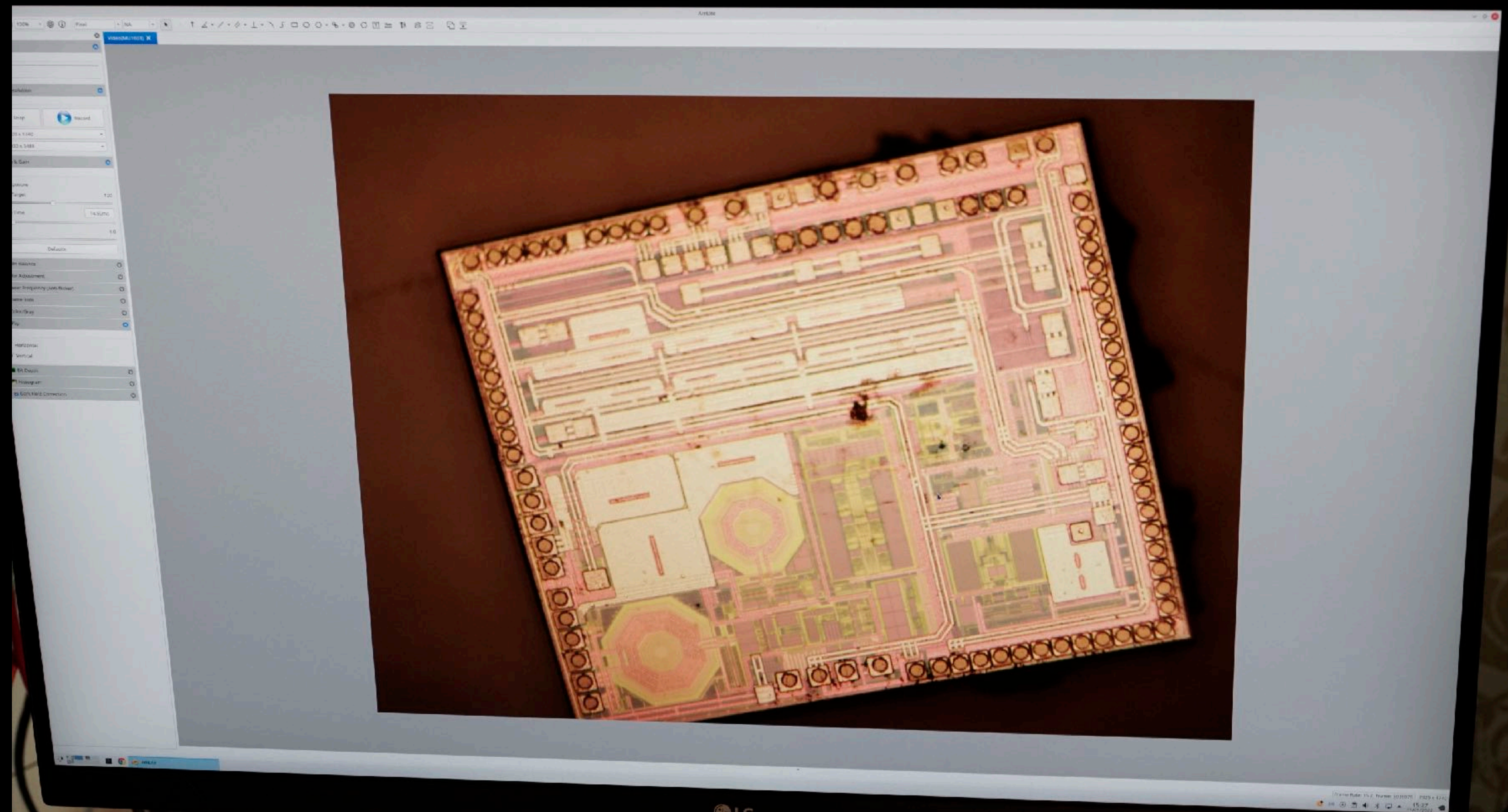




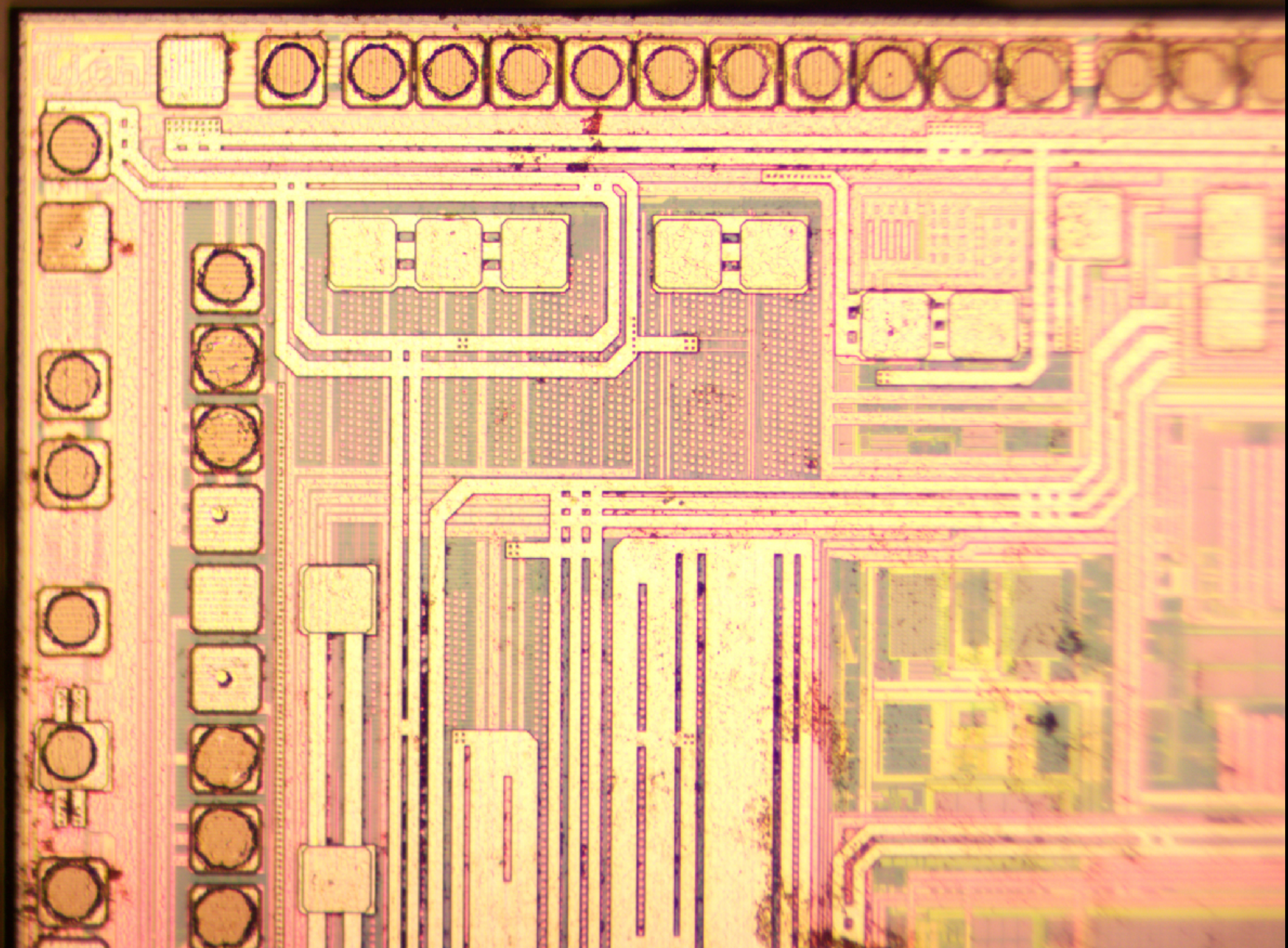




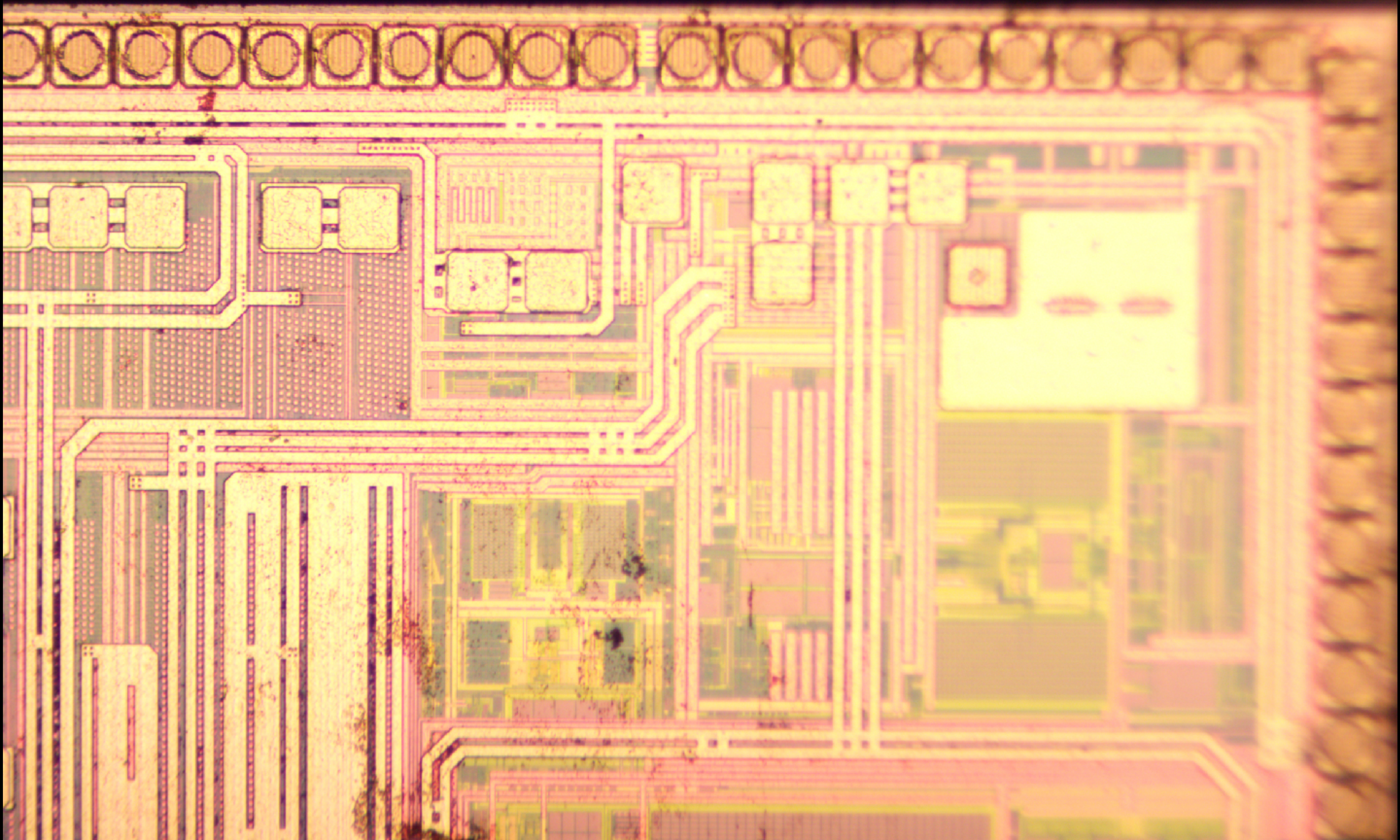




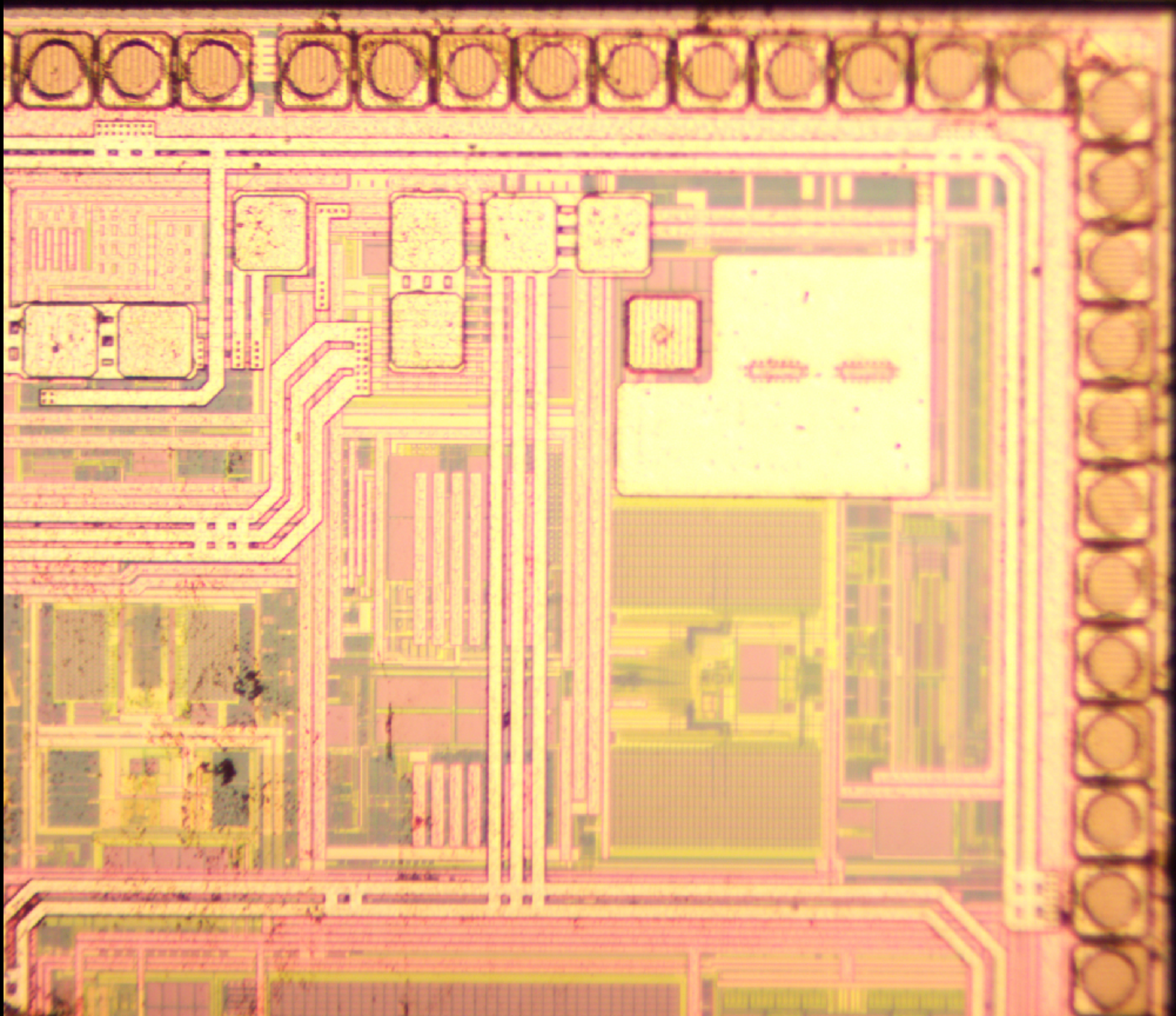




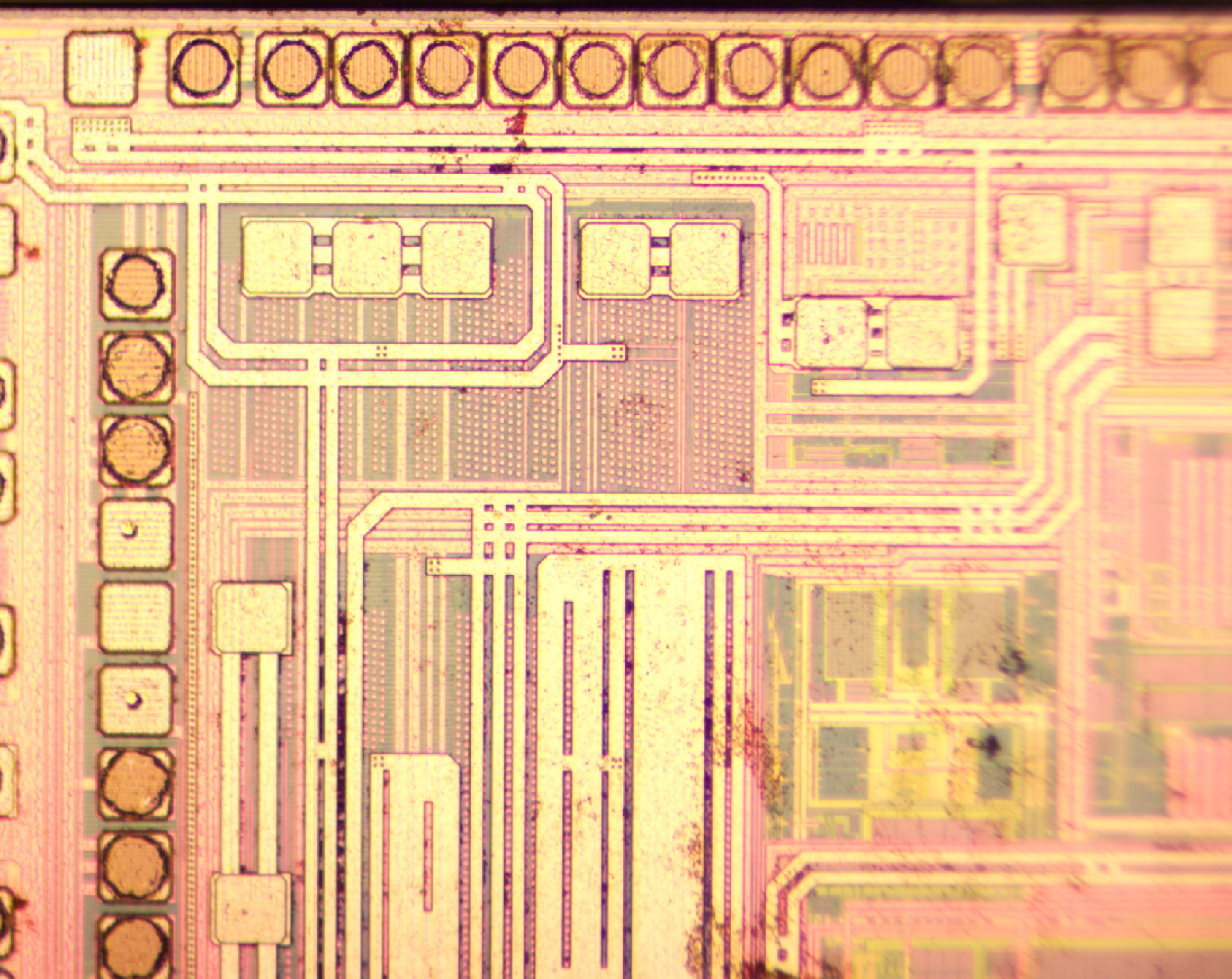




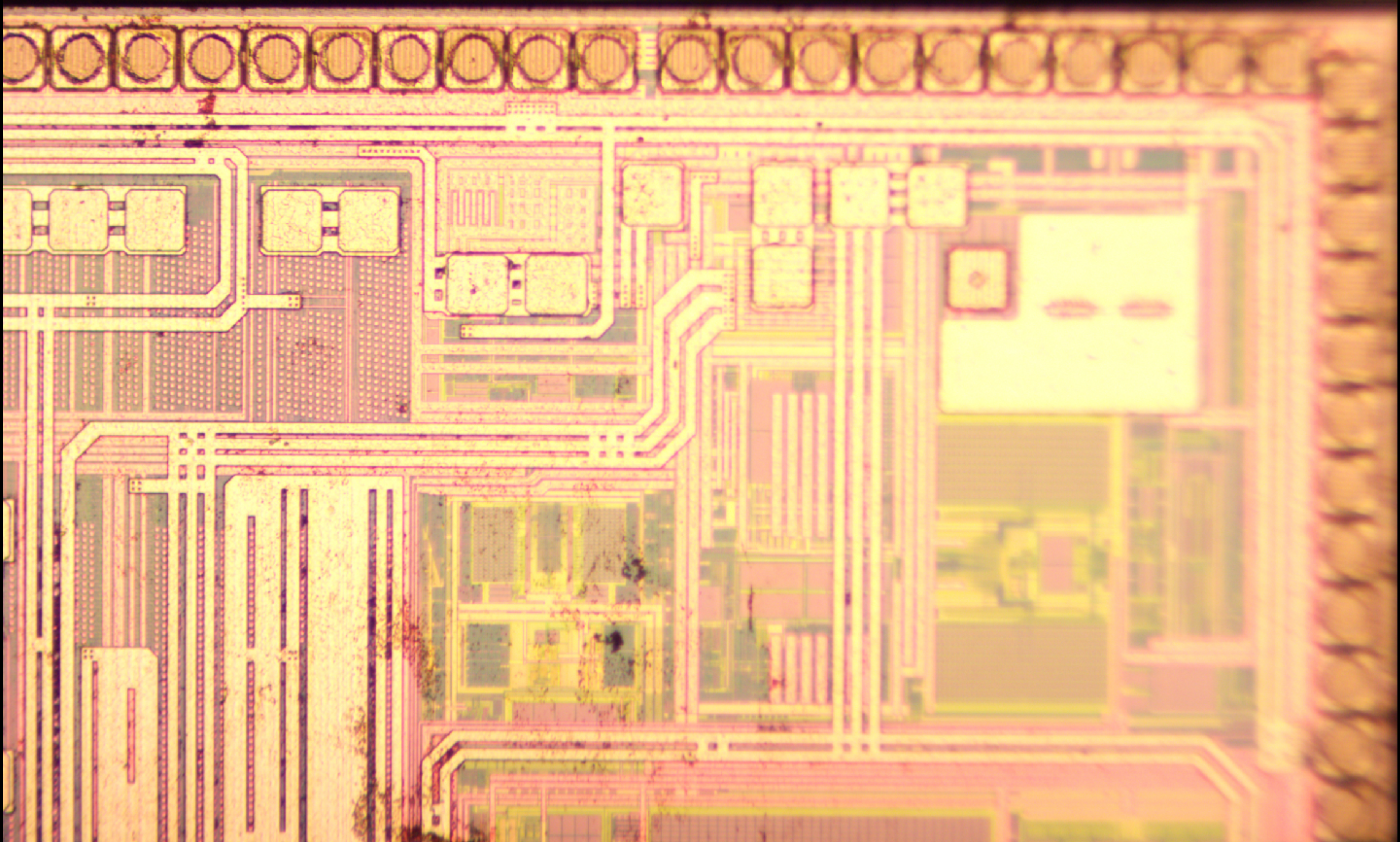




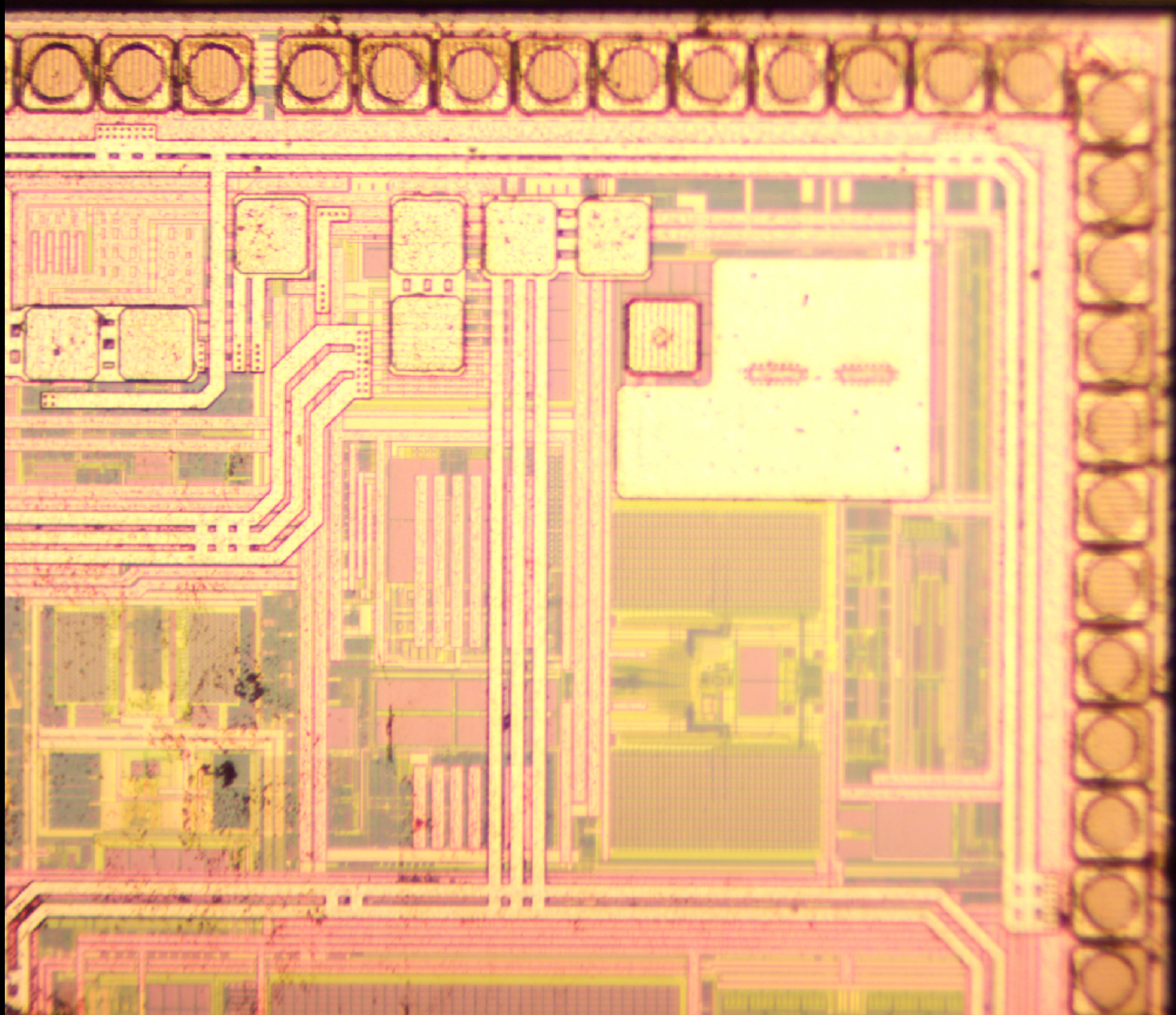




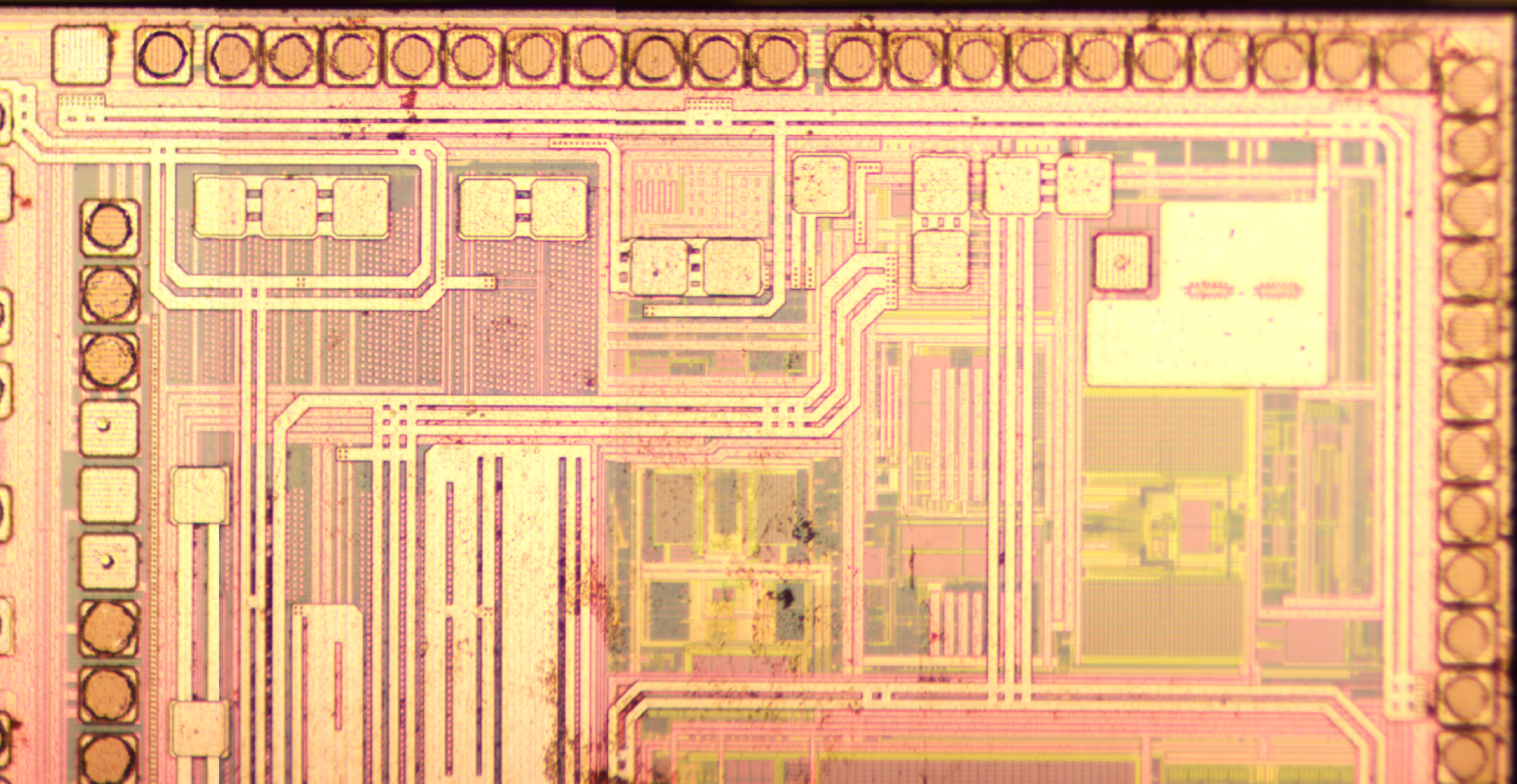




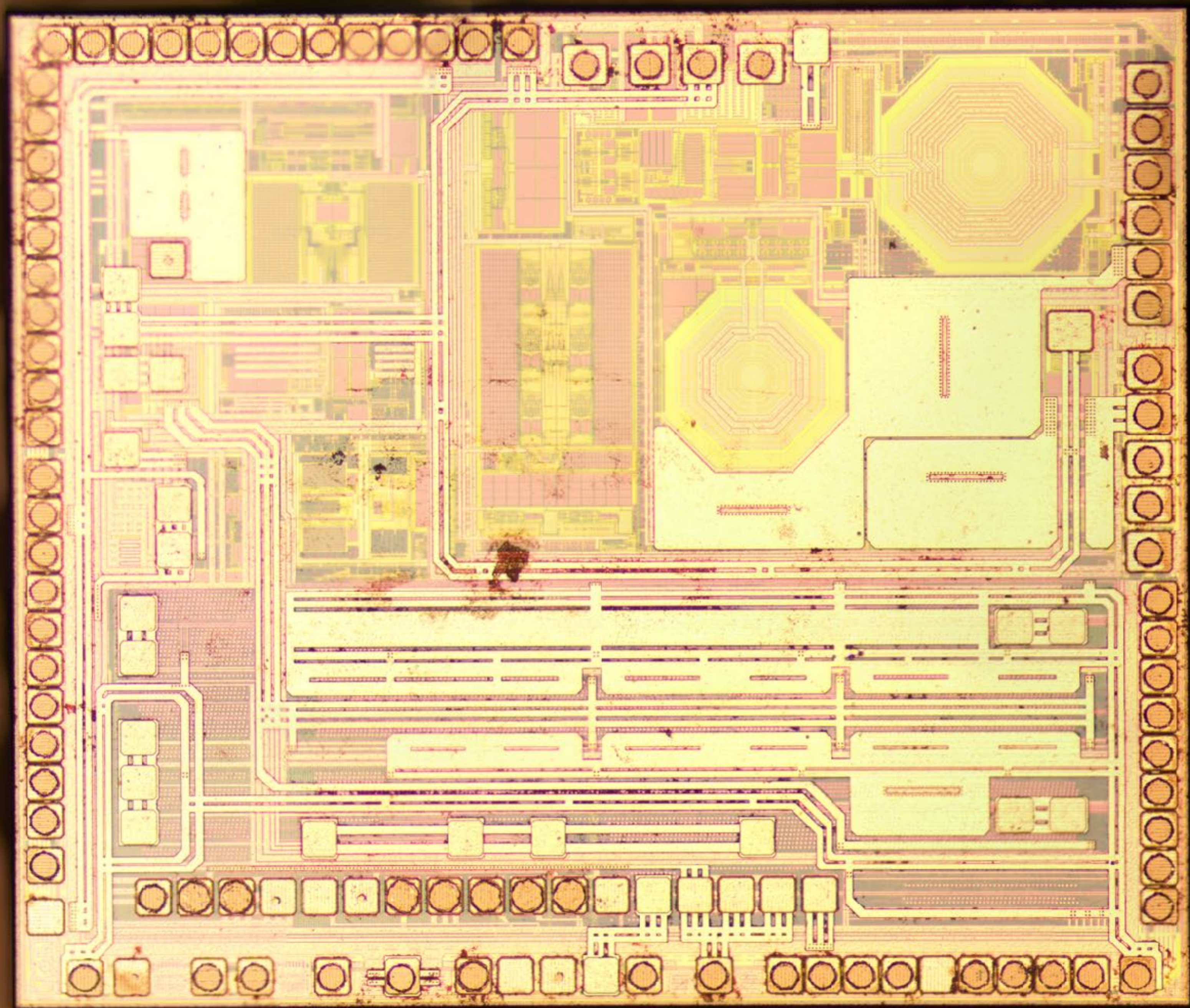














# Delayering

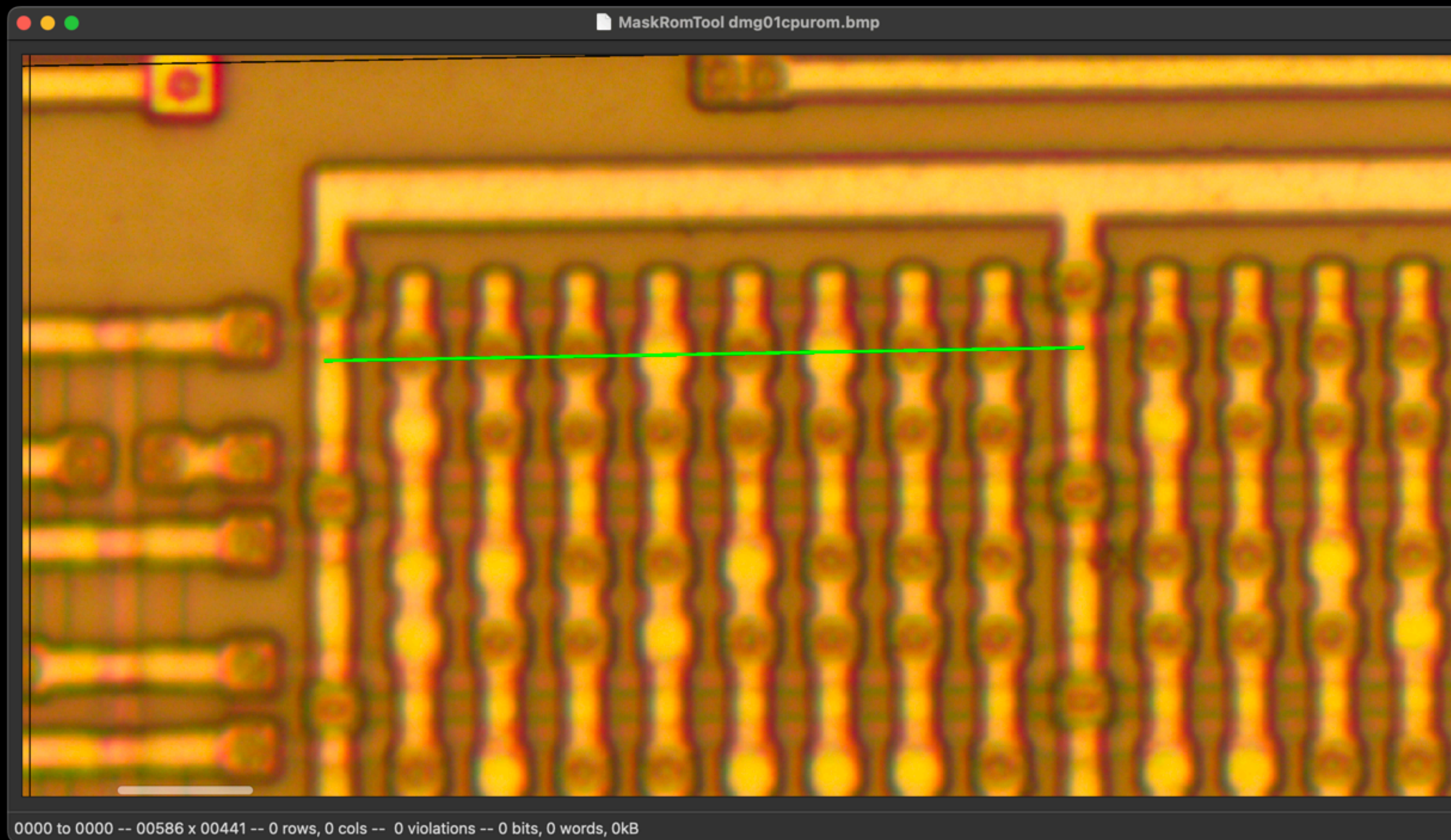
- $\text{HNO}_3$  or  $\text{H}_2\text{SO}_4$  gets us to the top metal layer.
- Many ROMs are lower in the chip:
  - Diffusion ROMs require HF delayering.
  - Implant ROMs require Dash Etch staining.



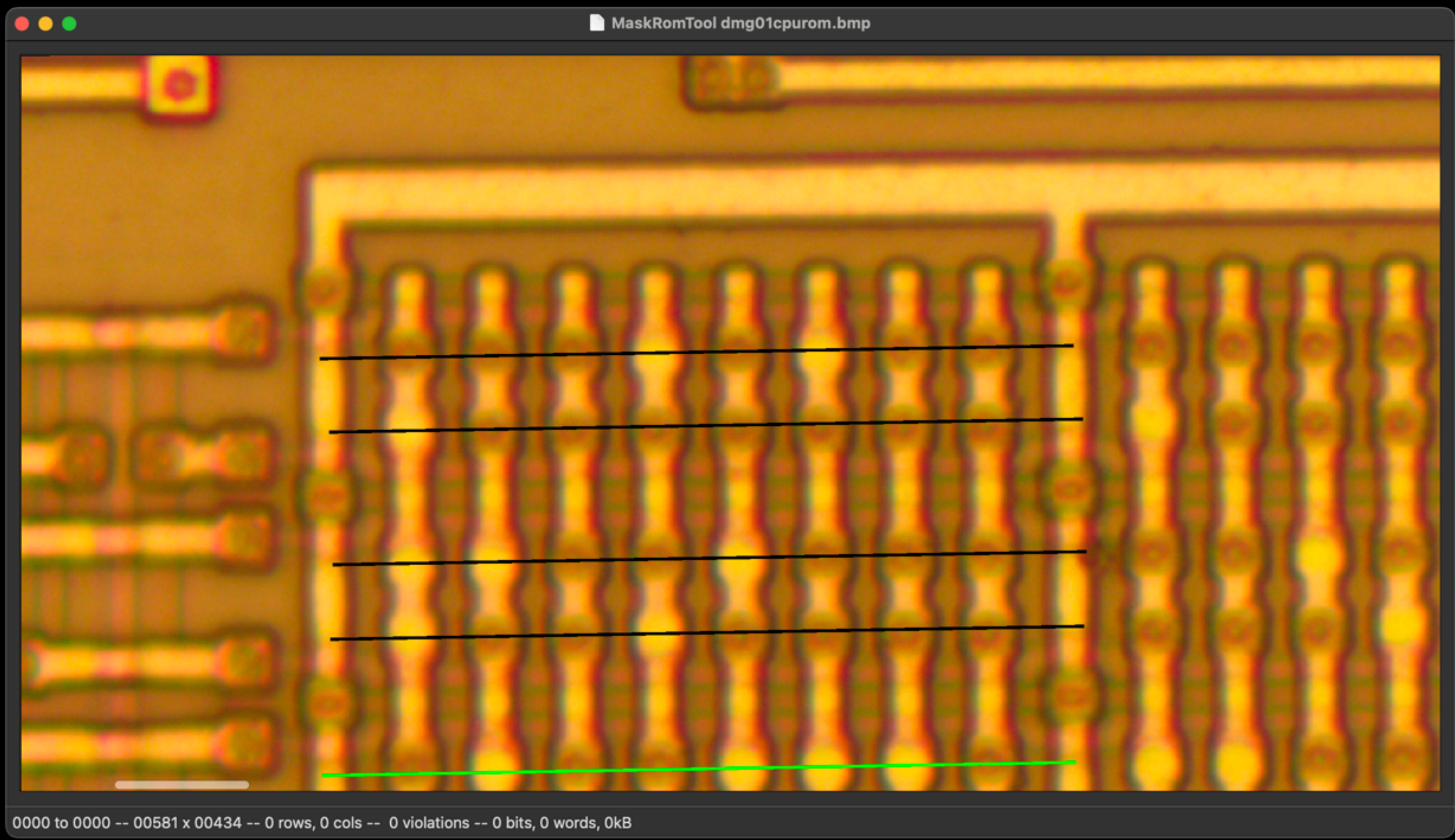
# A Quick Demo of Extraction

- **We annotate some features:**
  - **Row and Column lines.**
  - **Threshold between One and Zero.**
  - **Forced bits.**
- **Tool helps out:**
  - **Bits marked at Row/Column intersections.**
  - **Design Rule Checks (DRC) reduce bit errors.**
  - **Importing diffs, Exporting to other formats.**

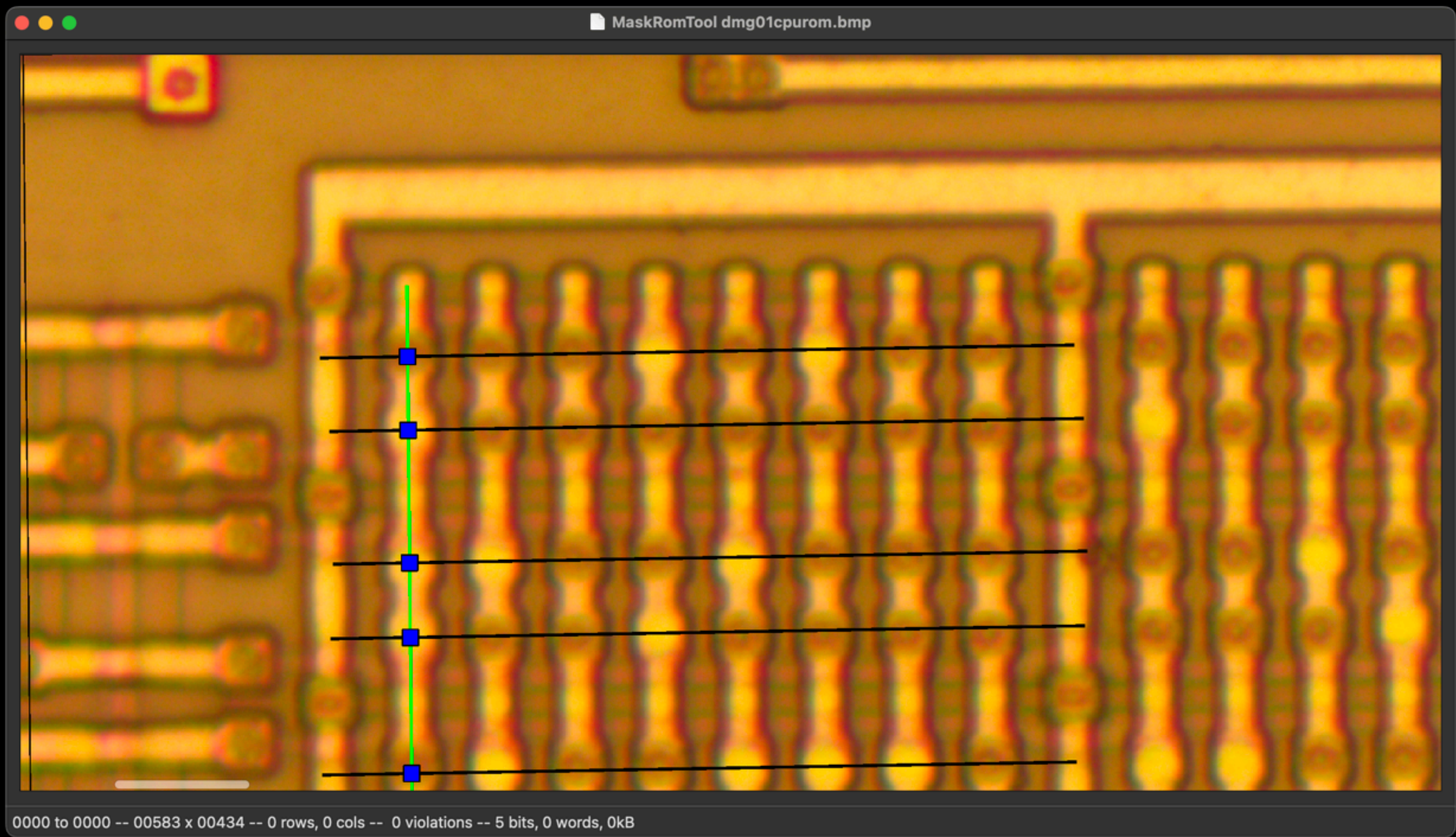




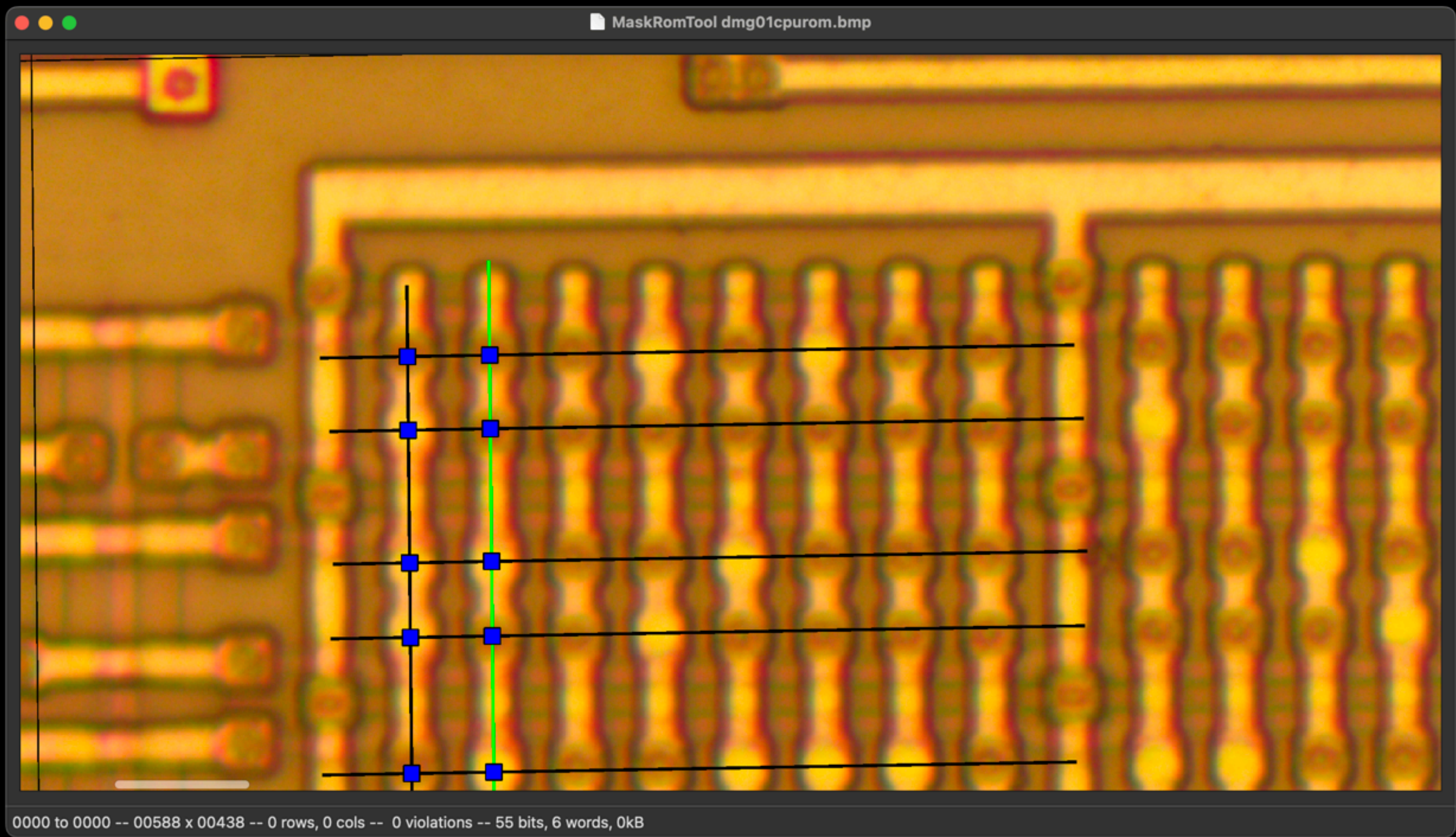




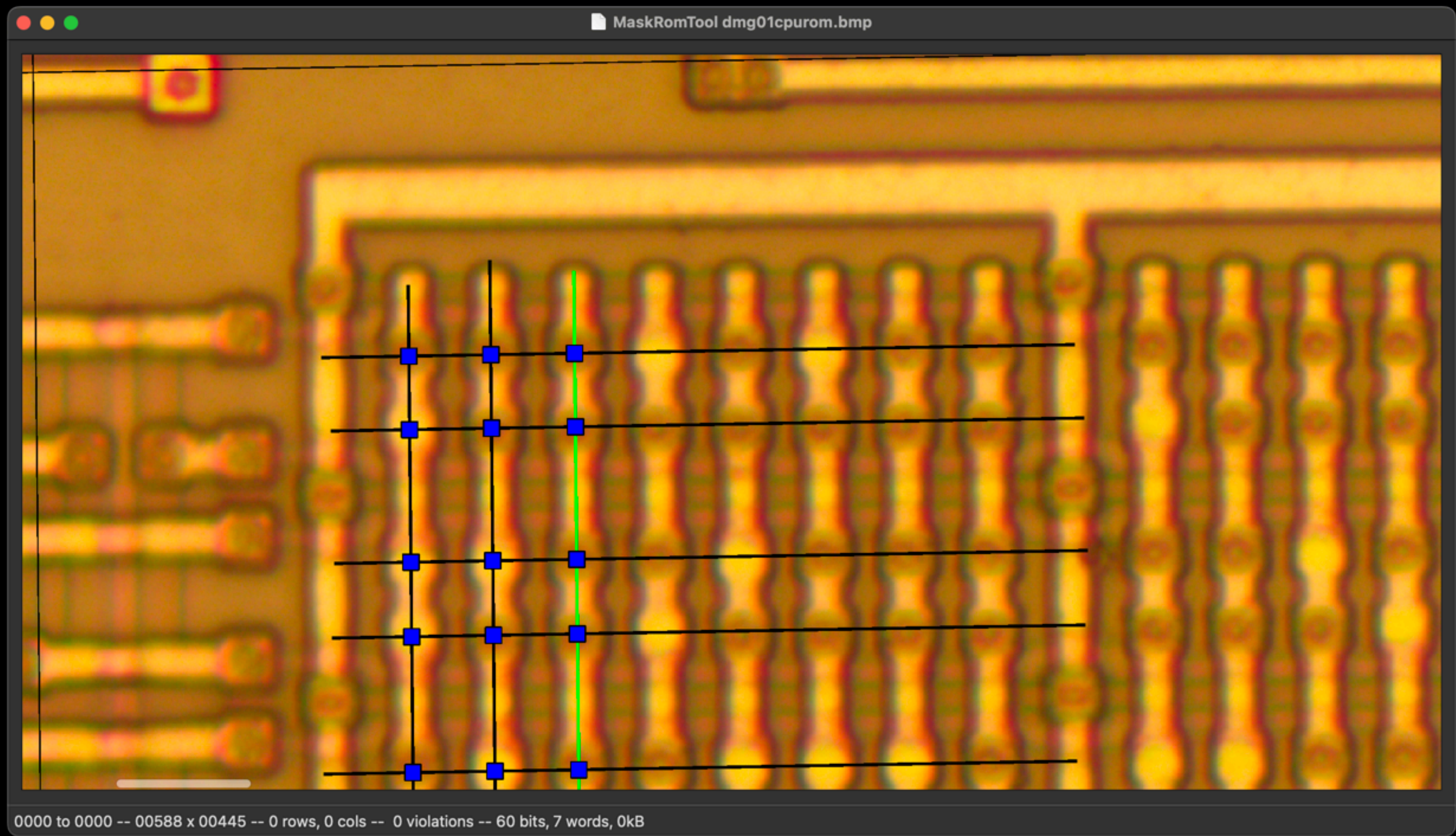




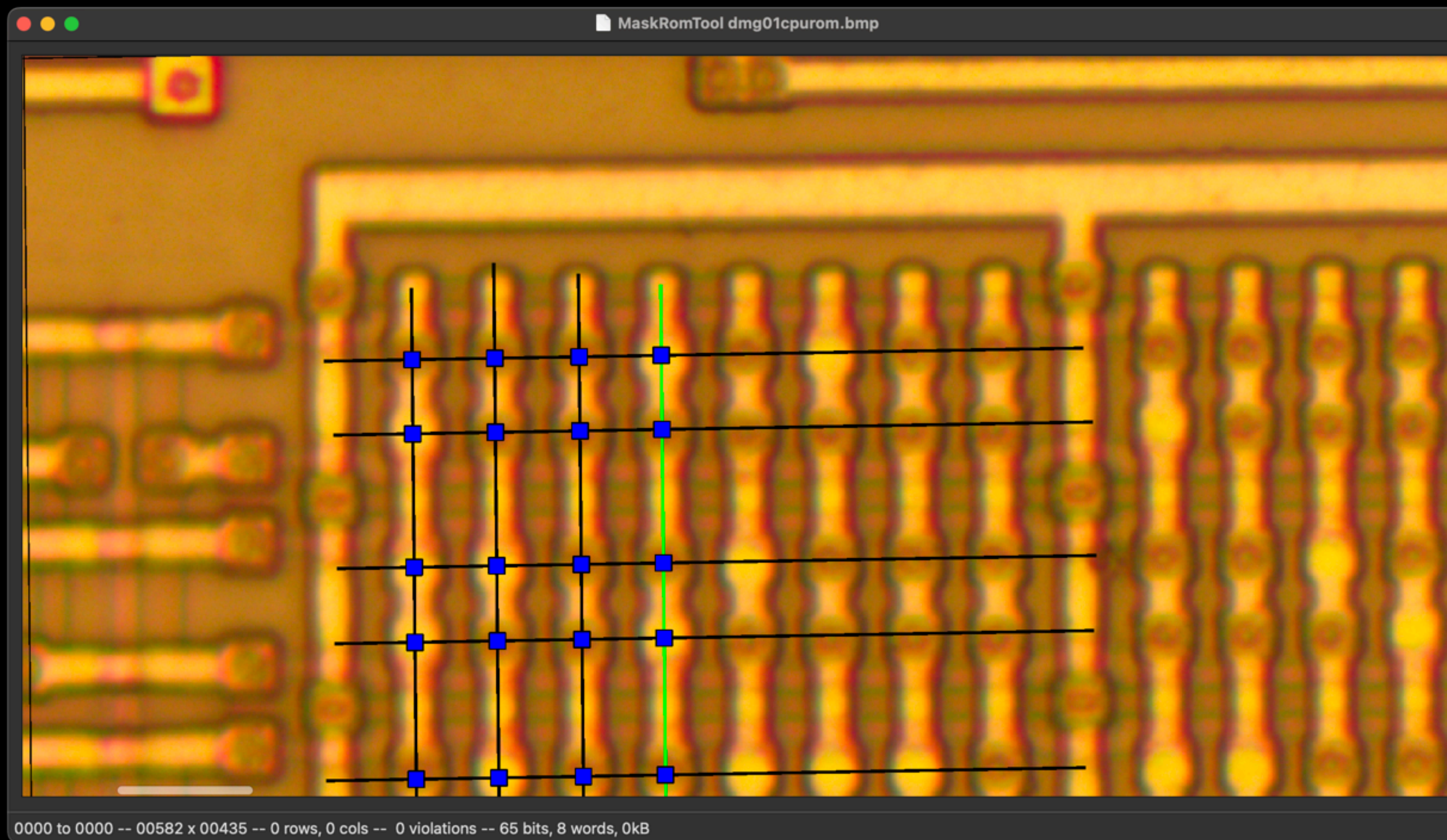




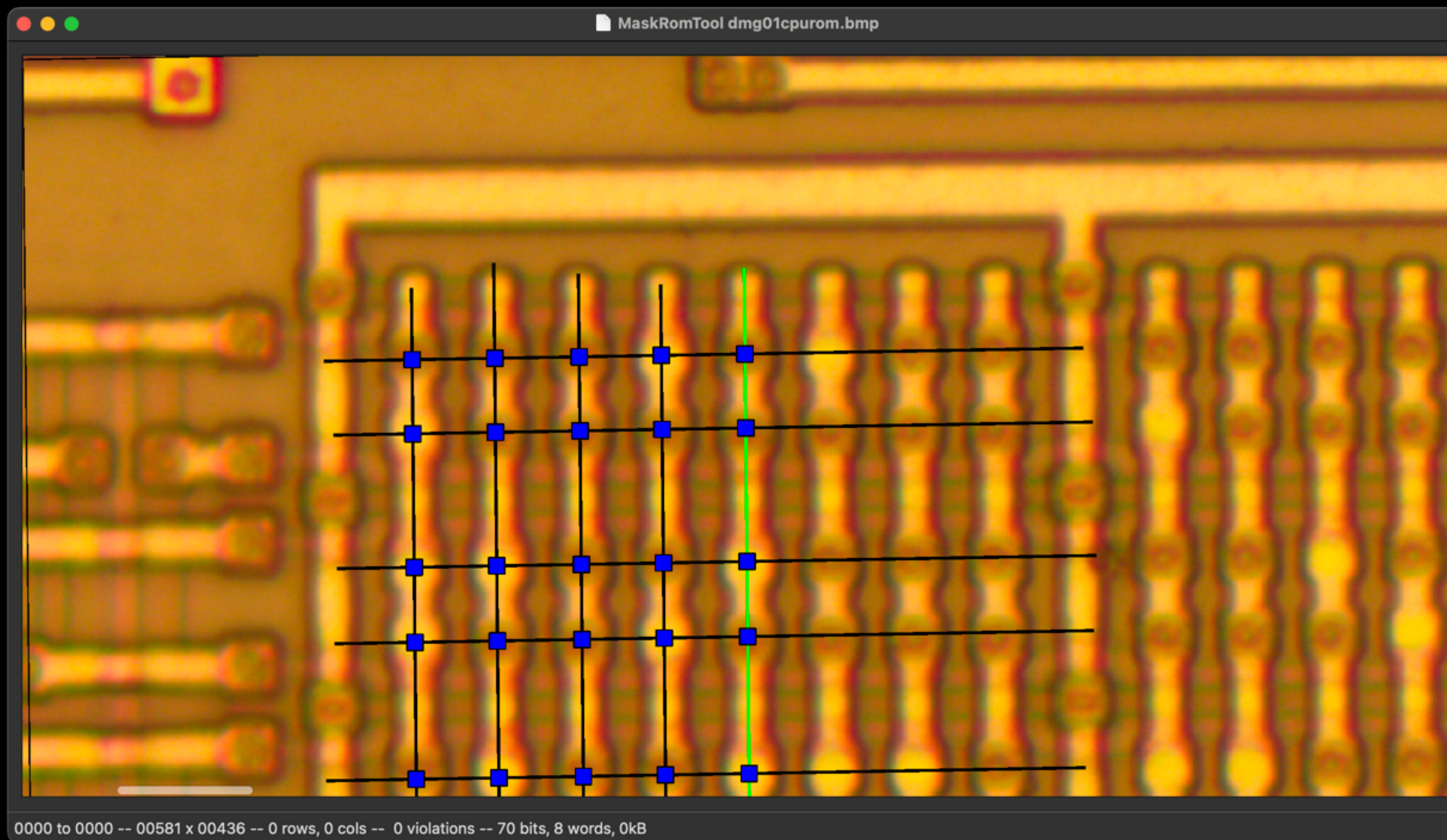




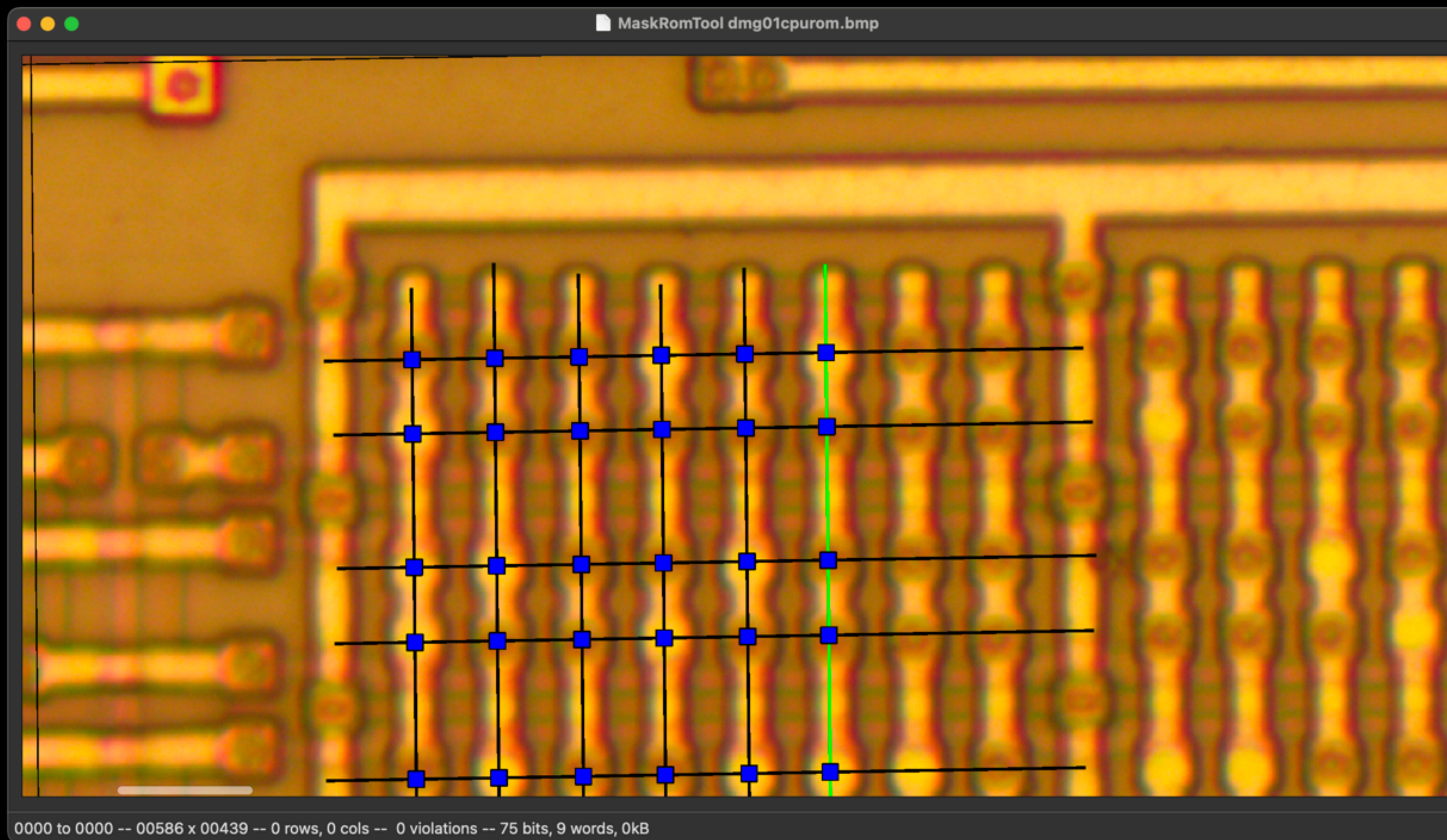




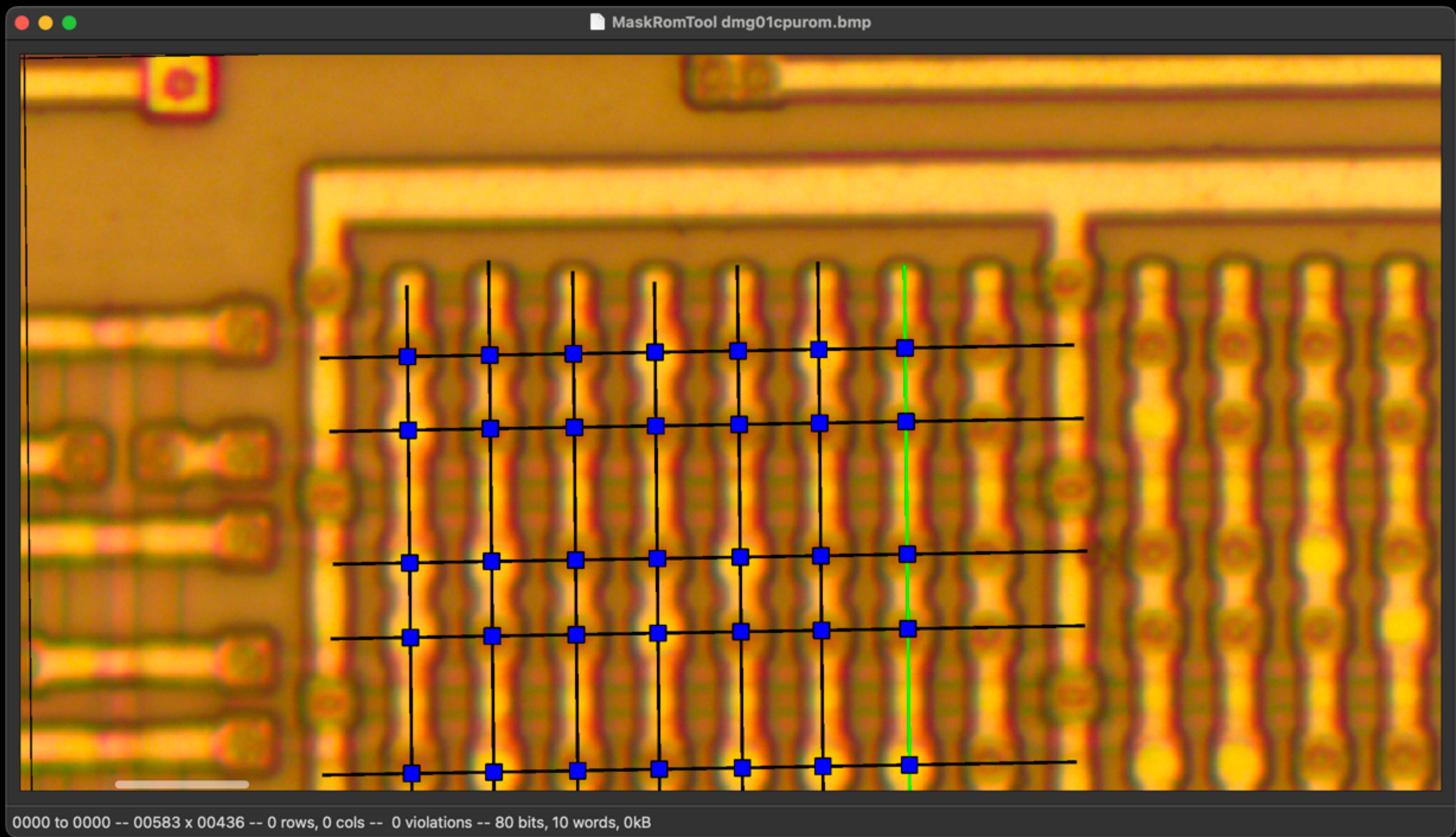




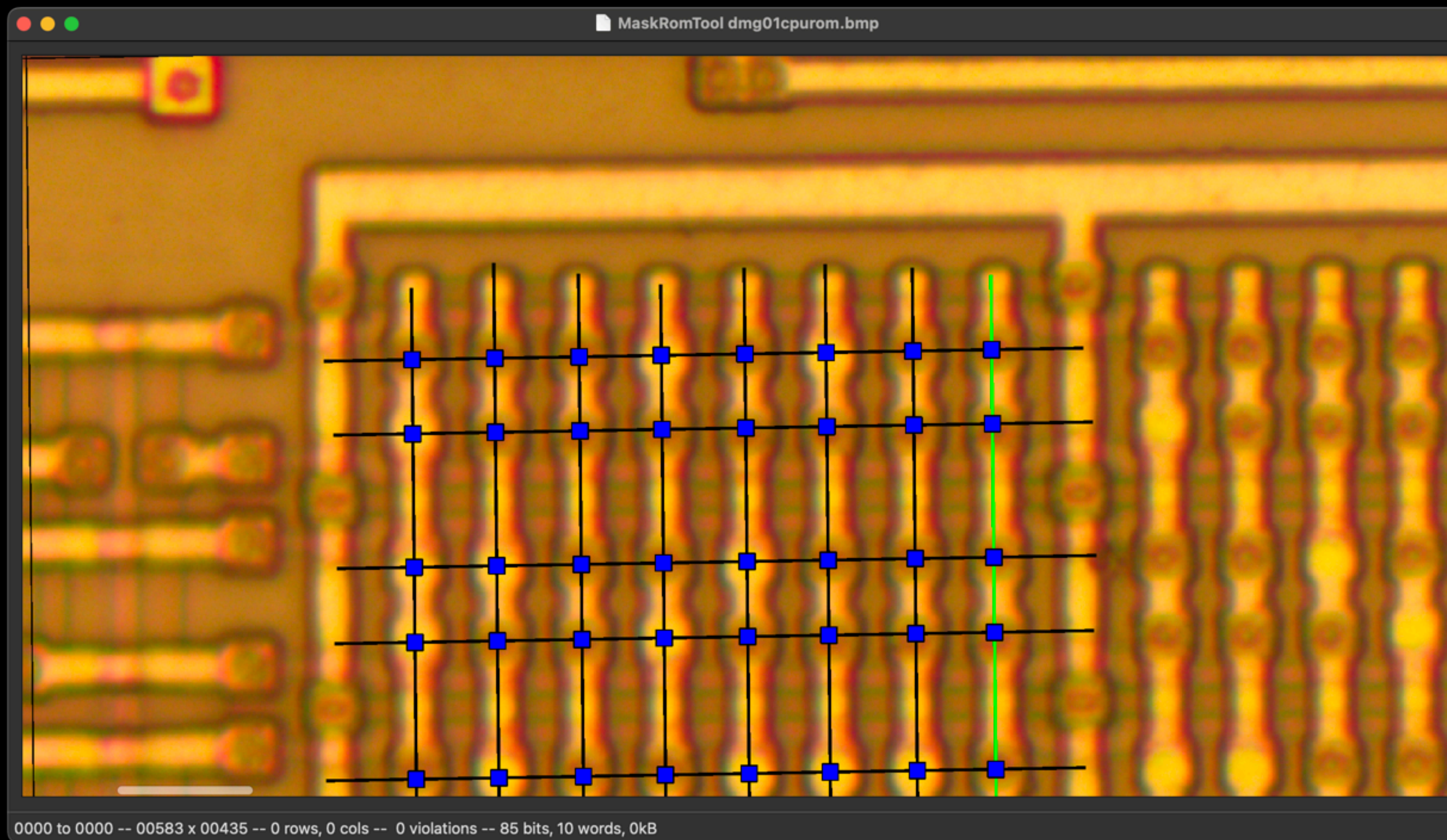




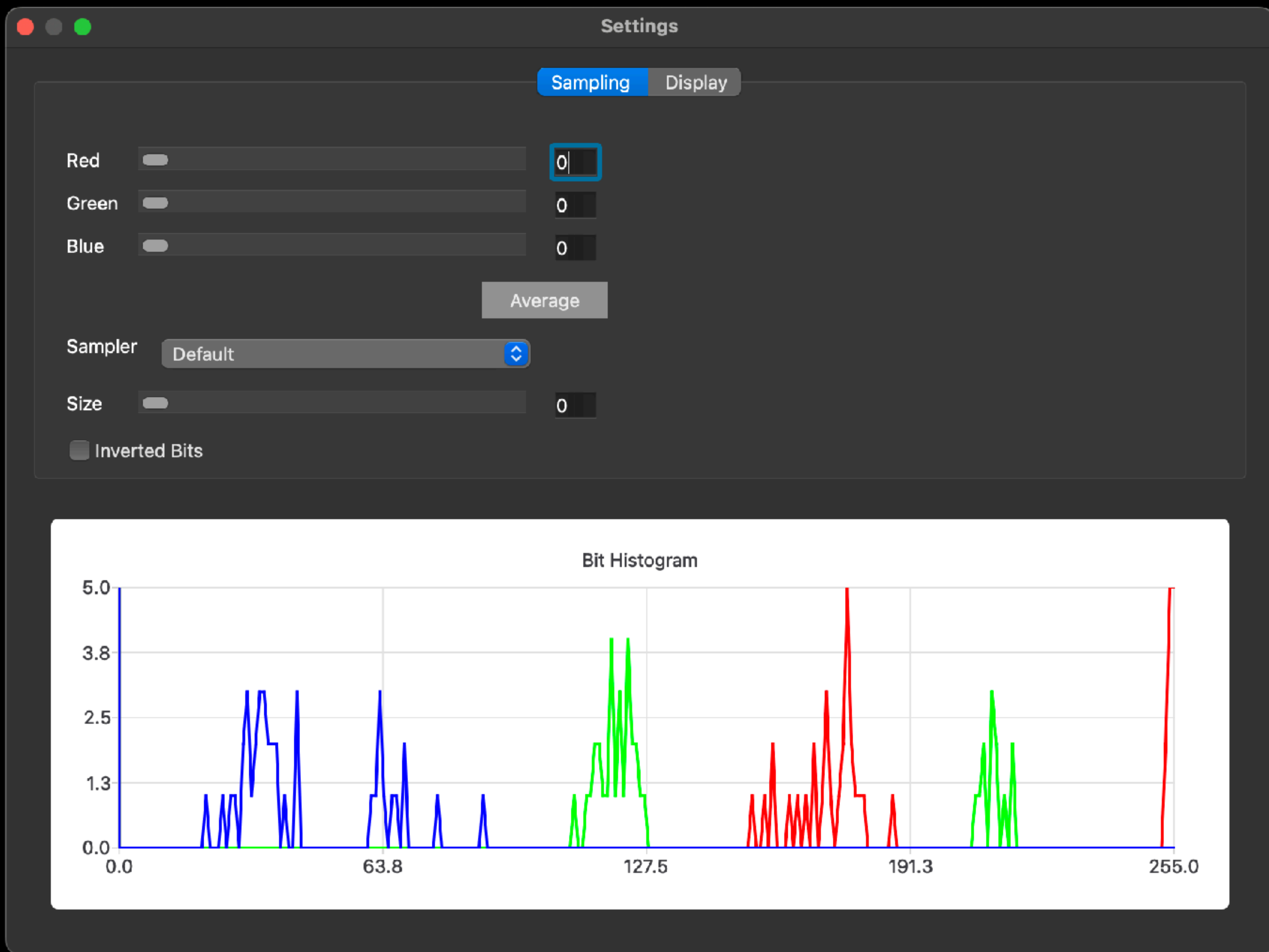














# Settings

Sampling Display

Red  0

Green  166

Blue  0

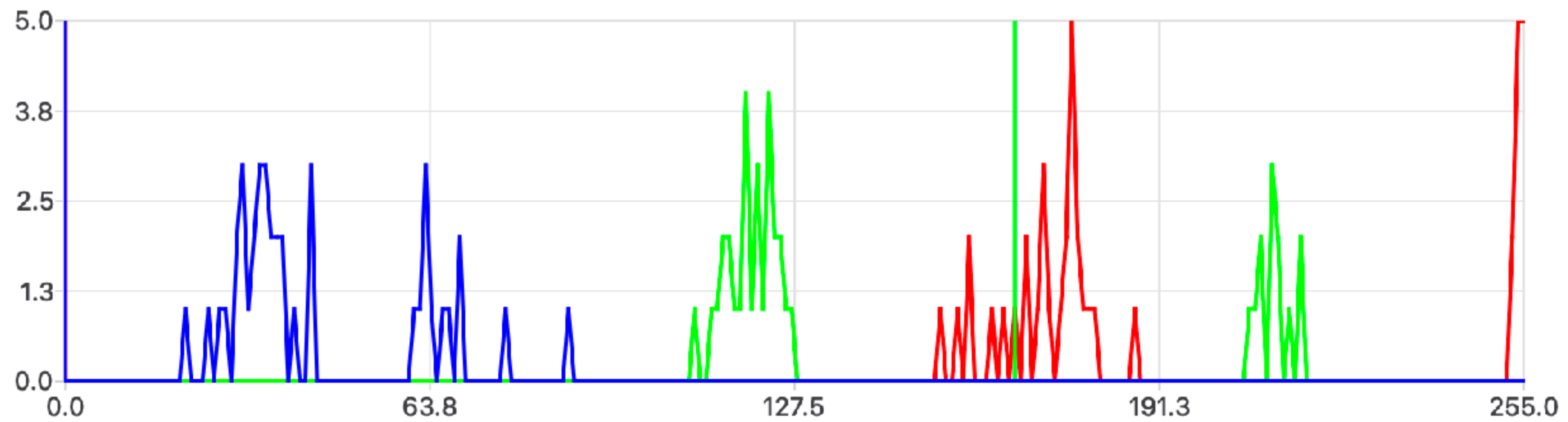
Average

Sampler Default

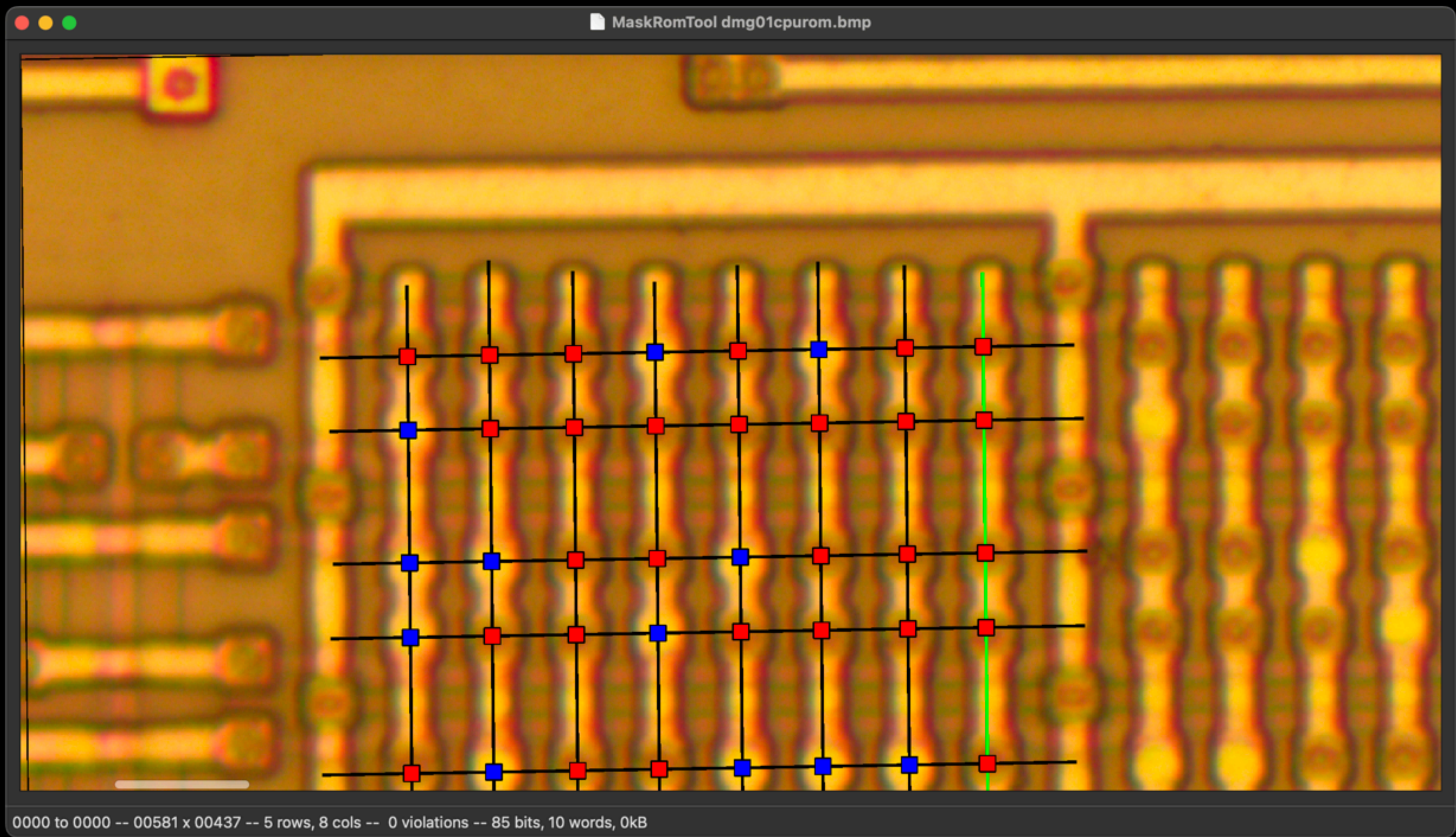
Size  2

☐ Inverted Bits

Bit Histogram











ASCII...

11101011

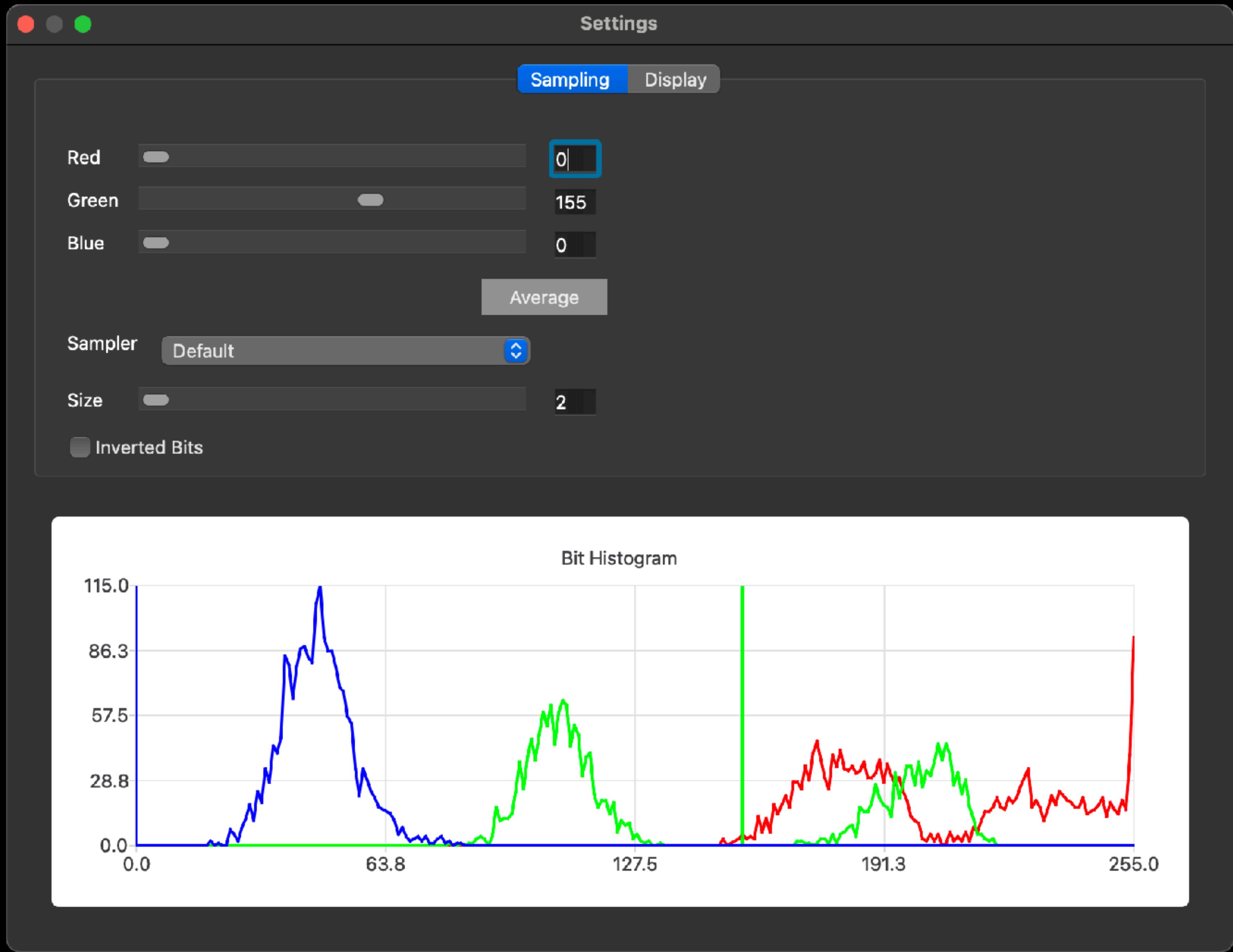
01111111

00110111

01101111

10110001







## ASCII Preview

```
11101011111100101100101100110010011000110111110000100001011100101110000000110011110001001001001011000100001011110001101100001000
01111111011100110111111101100011001010110100111100000110101010110011001111110011001010111011000000111000001011101101011011101111
001101111101101101110111110101110110011010010111011111111011010011100010111001001011000010101110111000111110111011110111011000
01101111111000111110111011110110001011100101011010100010001110000111100000111010011111000111001100101111010000110100111111101001
1011000100110000101110011011011100011001110110011110000011110011111101101011000111111100111011010111010000110100100001100010011
01101110011100110110100100110111011100110101101001001111111100110010111110100011001110011010011101111010000111100111001010110010
01011100011101111111110001110111101100000101101100111000011100010011000011110101001100001011111000110000100110010111011111010010
10110101110101111011101001011111001110101111101000010101111100011101011111000011111010111010101100001110011110011011011111000101
00011111000101000001101110011110101111001111000011111011011100000100011011010000110100111001100100110011110101000101101110110110
11011001000101100111100100011011001110010101010100001101111101110110010110000111010101101101111010101100101100101101111110010111
11111111111110011111101101010101001101111101000010100110101010011011010011111001101101001101010110101010010101011110010110100011
01011110111110100001111011010110001000011101000000111011001011100101001111101110100110110000101000101011100001000001111000100001
11011000100010111011110010010101001011000111000010011000111110001111011011000000100111001101010010001100111101100010100110111001
11111011001010011111101000111101001100101111100110110101011111011011110110000101001011001101000100110111001101011110110110001010
00111111111101100011101111110010001011001111010000011111101111010011011111110111001101110111010101011111111110110101011100111111
10101101111101111000110111110110100001010111100111001101101110100100111111000011011110101010001101011100100011111100111110011111
```



# After Bit Extraction

- Bits must be ordered into bytes.
- Errors must be corrected.
- Disassembly, reverse engineering!

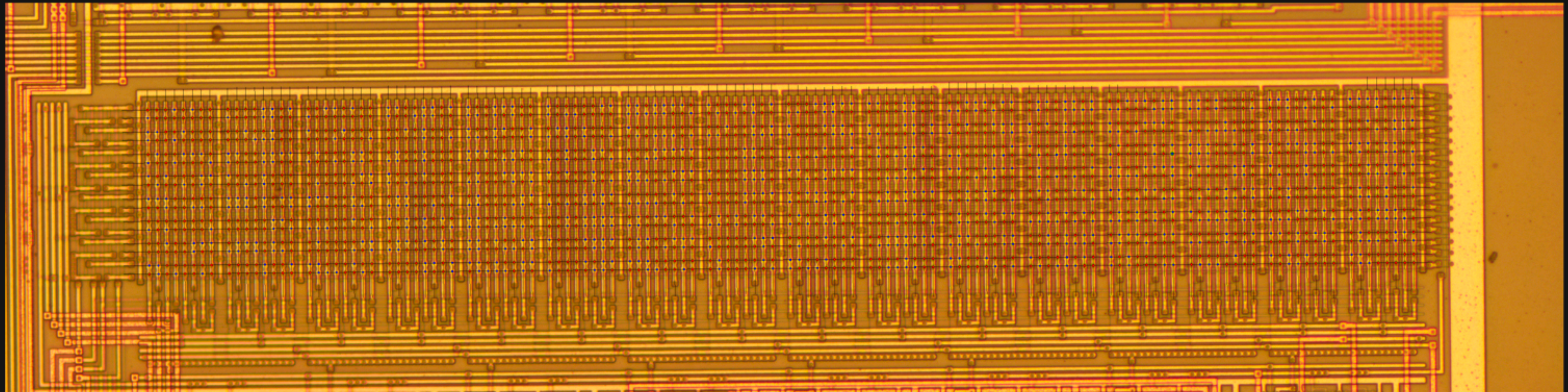


# Bits into Bytes

- Each "major column" holds one bit of significance.
  - 8-bit ROM, eight major columns.
  - 32-bit ROM, thirty-two major columns.
- Take one bit from the same position in each major column to form a word.
- Word ordering is less consistent:
  - Left to right? Top to bottom?
  - Rotations, flips.



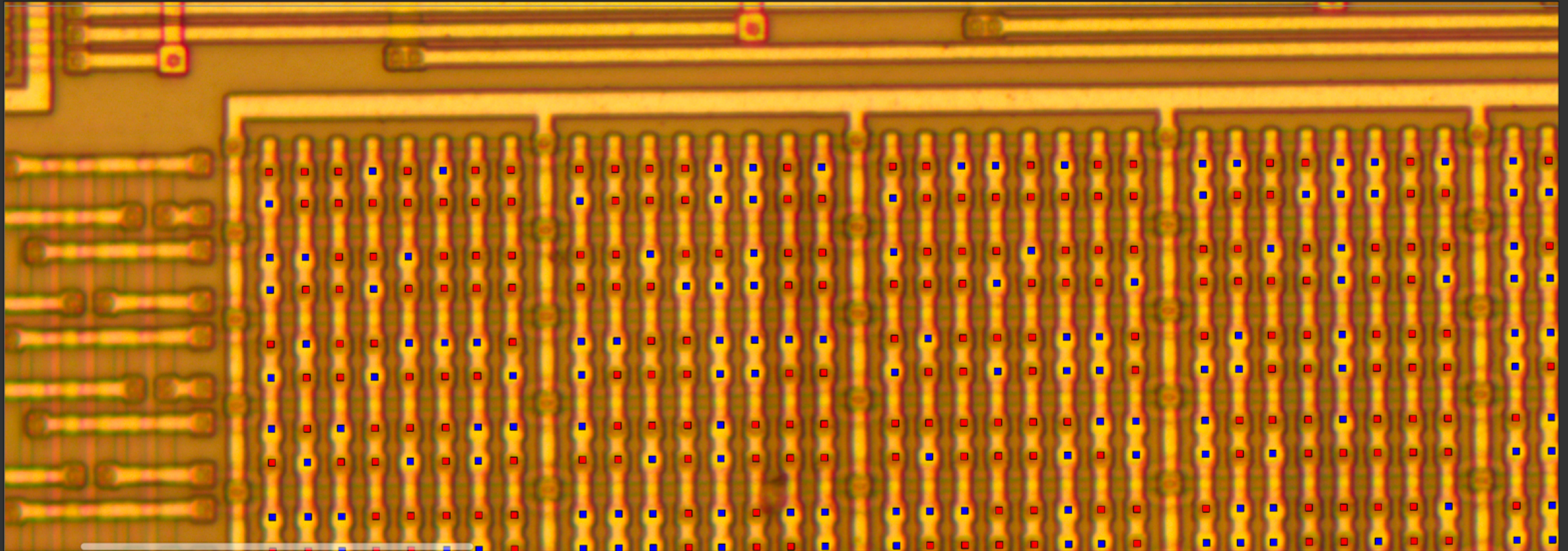
MaskRomTool dmg01cpurom.bmp



0000 to 0000 -- -0090 x -0414 -- 0 rows, 0 cols -- 0 violations -- 2048 bits, 256 words, 0kB

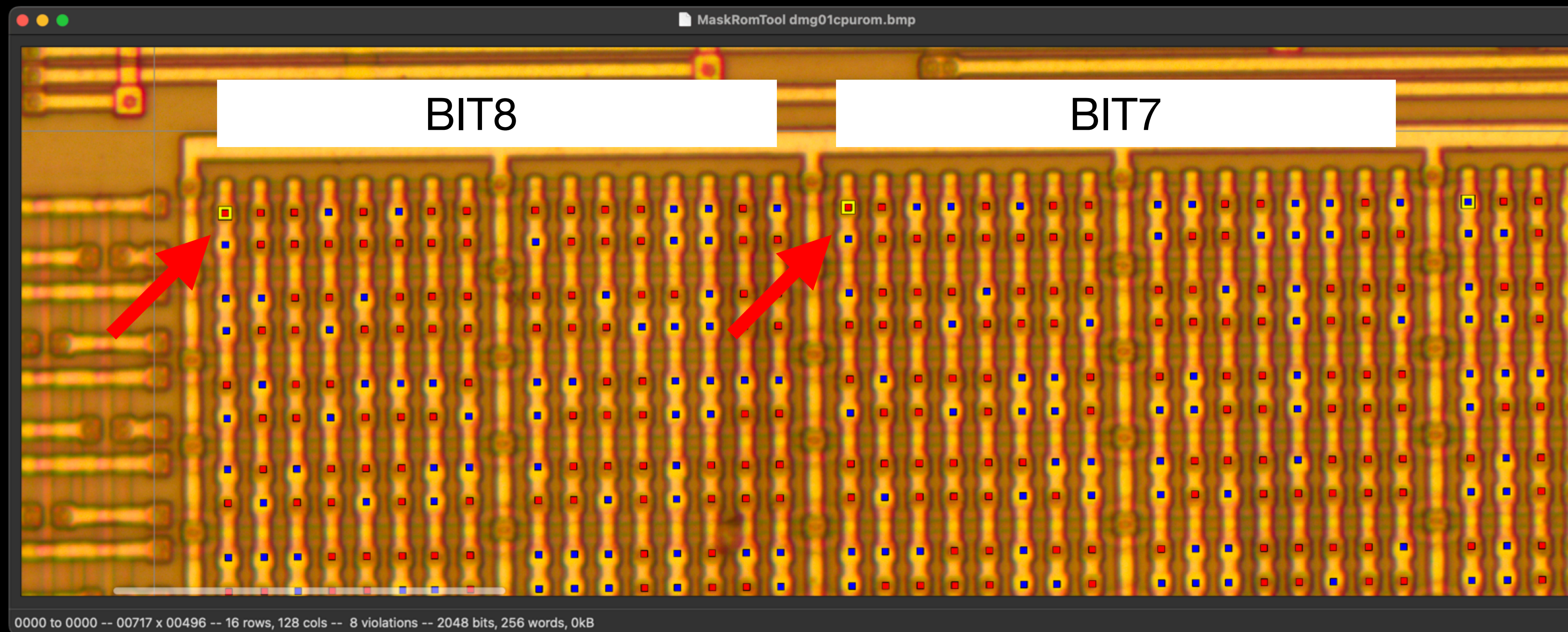


MaskRomTool dmg01cpurom.bmp

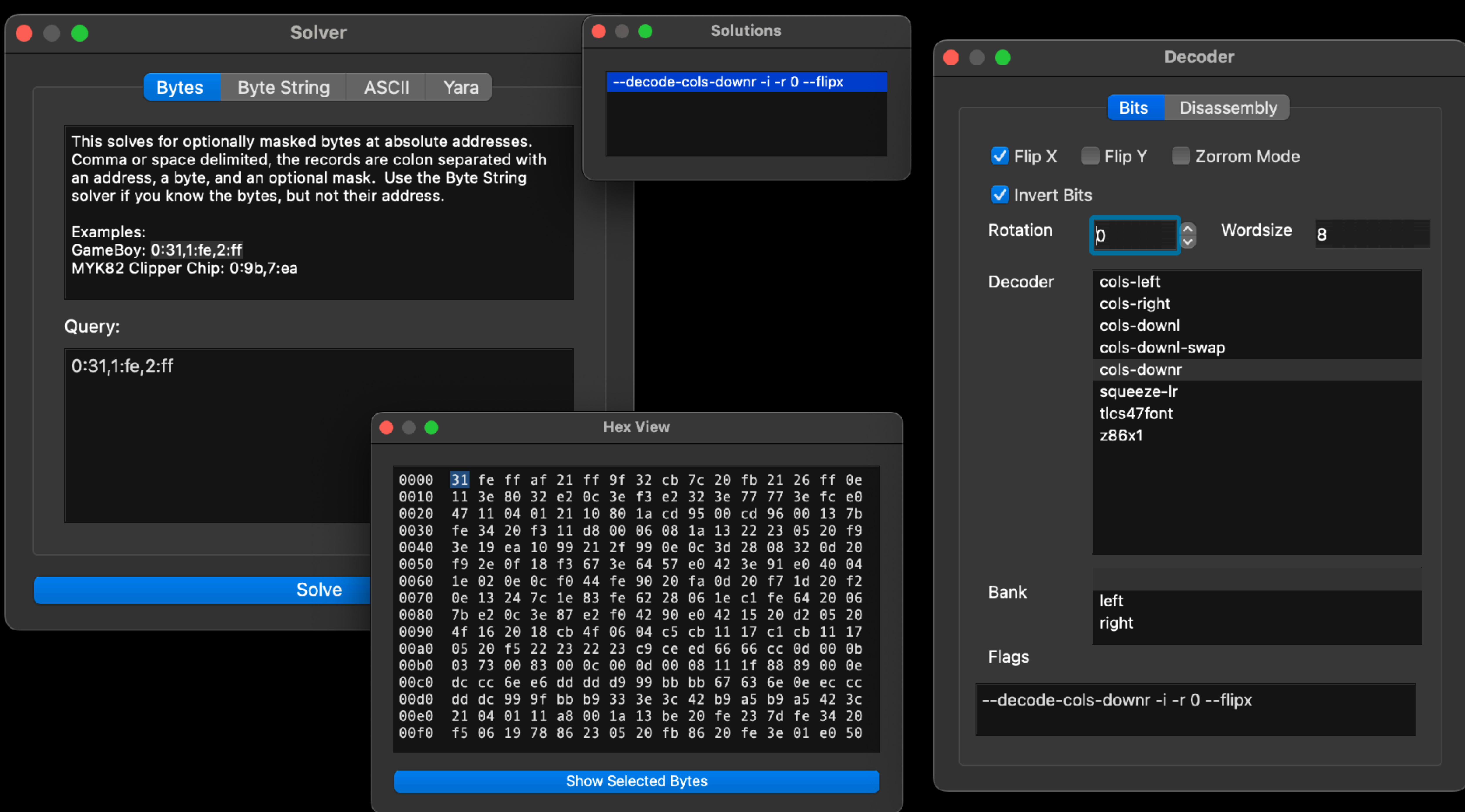


0000 to 0000 -- 00429 x 00371 -- 0 rows, 0 cols -- 0 violations -- 2048 bits, 256 words, 0kB











# Decoder

Bits

Disassembly

Architecture:

r2/bf  
r2/bpf  
r2/bpf.mr  
r2/chip8  
r2/cr16  
r2/cris  
r2/dalvik  
r2/dis  
r2/ebc  
r2/evm  
r2/fslsp  
**r2/gb**  
r2/h8300  
r2/hppa  
r2/i4004  
r2/i8080  
r2/java  
r2/jdh8  
r2/kvx  
r2/lanai  
r2/lh5801  
r2/lm32  
r2/loongarch  
r2/lua  
r2/m680x  
r2/m68k

# r2/gb Disassembly

0x00000000	3	31feff	ld sp, 0xfffe
0x00000003	1	af	xor a
0x00000004	3	21ff9f	ld hl, 0x9fff
0x00000007	1	32	ldd [hl], a
0x00000008	2	cb7c	bit 7, h
0x0000000a	2	20fb	jr nZ, 0xfb
0x0000000c	3	2126ff	ld hl, 0xff26
0x0000000f	2	0e11	ld c, 0x11
0x00000011	2	3e80	ld a, 0x80
0x00000013	1	32	ldd [hl], a
0x00000014	1	e2	ld [0xff00 + c], a
0x00000015	1	0c	inc c
0x00000016	2	3ef3	ld a, 0xf3
0x00000018	1	e2	ld [0xff00 + c], a
0x00000019	1	32	ldd [hl], a
0x0000001a	2	3e77	ld a, 0x77
0x0000001c	1	77	ld [hl], a
0x0000001d	2	3efc	ld a, 0xfc
0x0000001f	2	e047	ld [rBGP], a
0x00000021	3	110401	ld de, 0x0104
0x00000024	3	211080	ld hl, 0x8010
0x00000027	1	1a	ld a, [de]
0x00000028	3	cd9500	call 0x0095
0x0000002b	3	cd9600	call 0x0096
0x0000002e	1	13	inc de



# Software and target!

- <https://github.com/travisgoodspeed/maskromtool/>
  - Latest release for Windows or macOS.
  - Build from source with Qt6 for Linux.
- <https://github.com/travisgoodspeed/gbrom-tutorial>
  - Clone this locally, follow instructions in README.



# Cheat Sheet

- Left-click sets source position.
  - Left-drag selects lines.
- Middle-drag pans the view.
- Right-drag moves the selected lines.
- R places a Row, C places a Column, SPACE duplicates last placement.
- D deletes a selection, S sets a new end position.
- Shift+D duplicates a selected group, then right-drag to move the copy.
- V to check for errors, E for next error.