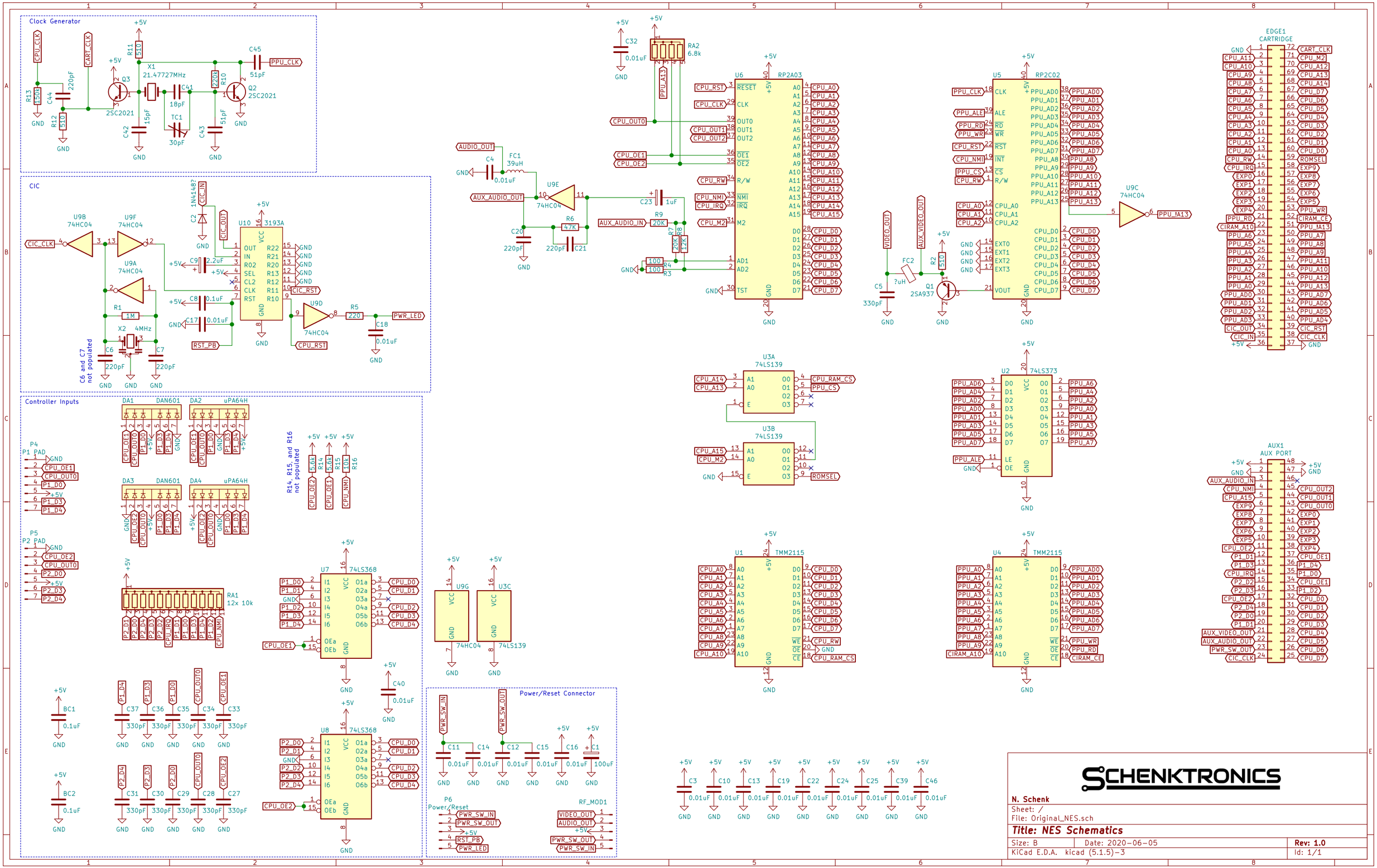
Clock Generator
CIC
Controller Inputs
Power/Reset Connector
EDGE1
CARTRIDGE
AUX1
AUX PORT
U6 RP2A03
U5 RP2C02
U1 TMM2115
U4 TMM2115
U2 74LS373
U3A 74LS139
U3B 74LS139
U7 74LS368
U8 74LS368
U10 3193A
U9B 74HC04
U9F 74HC04
U9A 74HC04
U9E 74HC04
U9D 74HC04
U9C 74HC04
U9G 74HC04
U3C 74LS139
Q1 2SA937
Q2 2SC2021
Q3 2SC2021
X1 21.47727MHz
X2 4MHz
RA1 12x 10k
RA2 6.8k
C6 and C7 not populated
R14, R15, and R16 not populated
SCHENKTRONICS
N. Schenk
Sheet: /
File: Original_NES.sch
Title: NES Schematics
Size: B
Date: 2020-06-05
Rev: 1.0
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Id: 1/1