

ESPRESSO bin

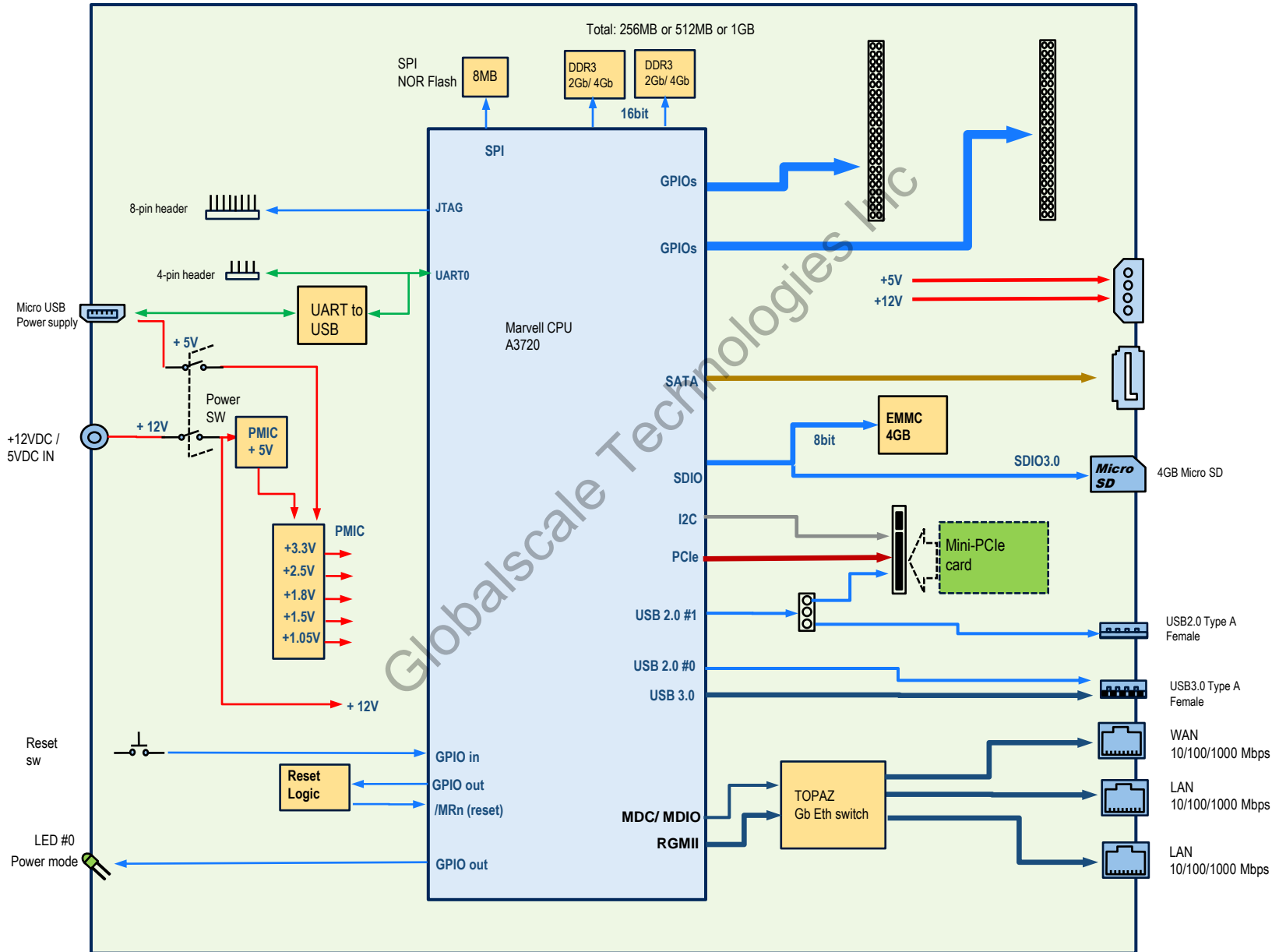
Board Block Diagram & Schematics

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ESPRESSObin- V2-Block diagram

Aug 19, 2016 - GTI



RAM

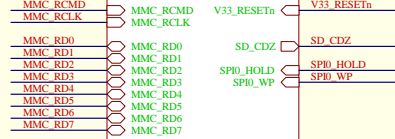
RAM.Sch



+DDR_VCC
+3.3V

Storage

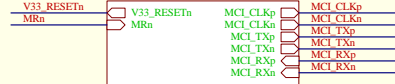
Storage.Sch



+1.8V
+3.3V

JTAG

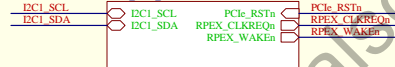
JTAG.Sch



+1.8V
+3.3V
+5V

PCIE_USB_SATA

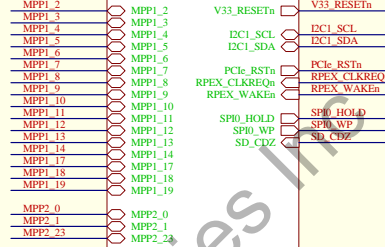
PCIE_USB_SATA.Sch



+1.5V
+3.3V
+5V

GPIO

GPIO.Sch



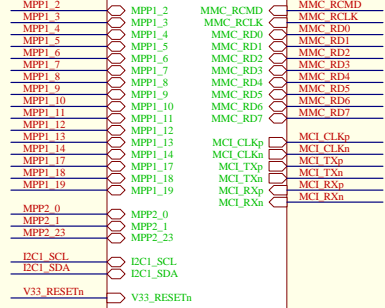
POWER

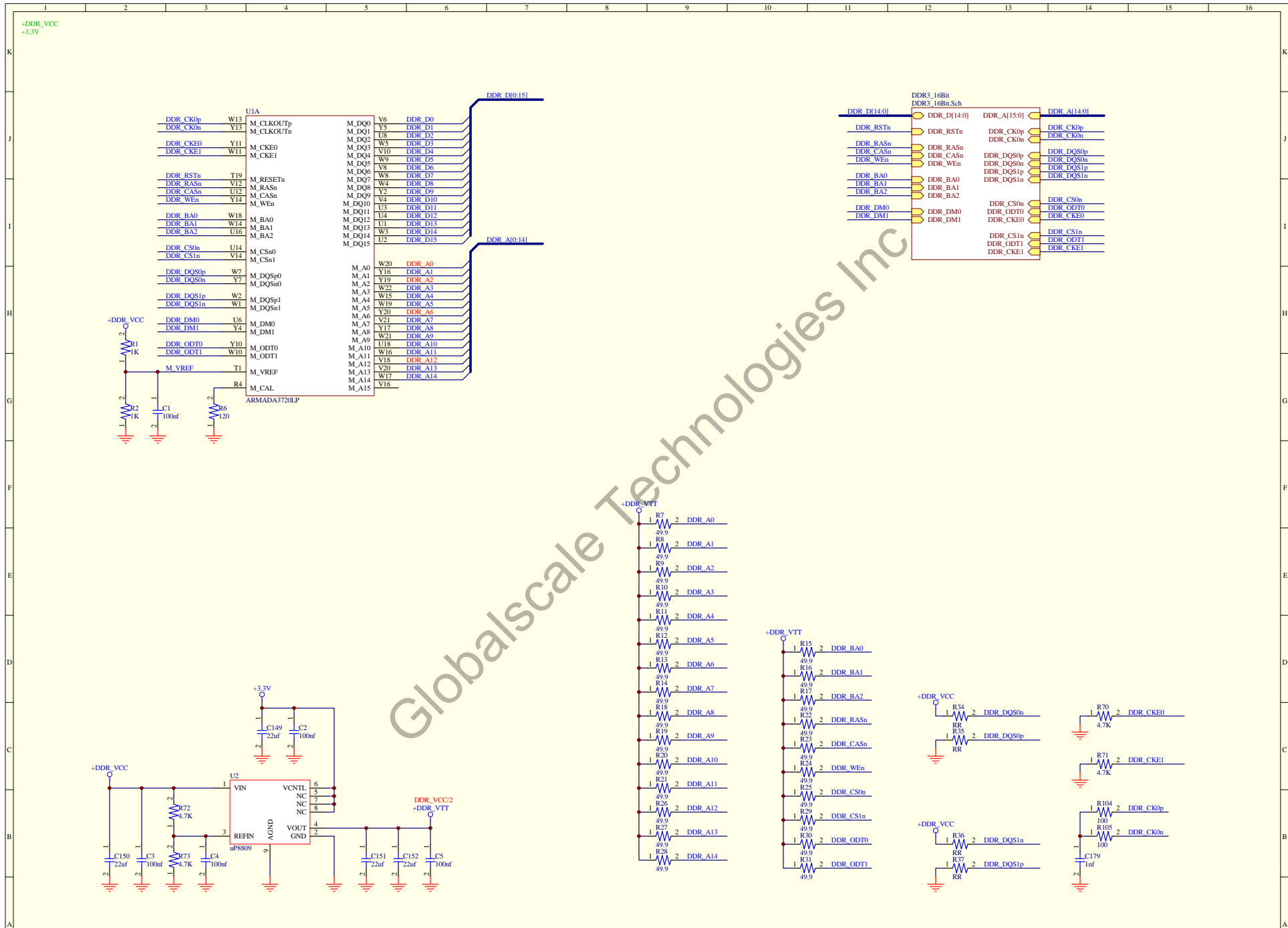
POWER.Sch



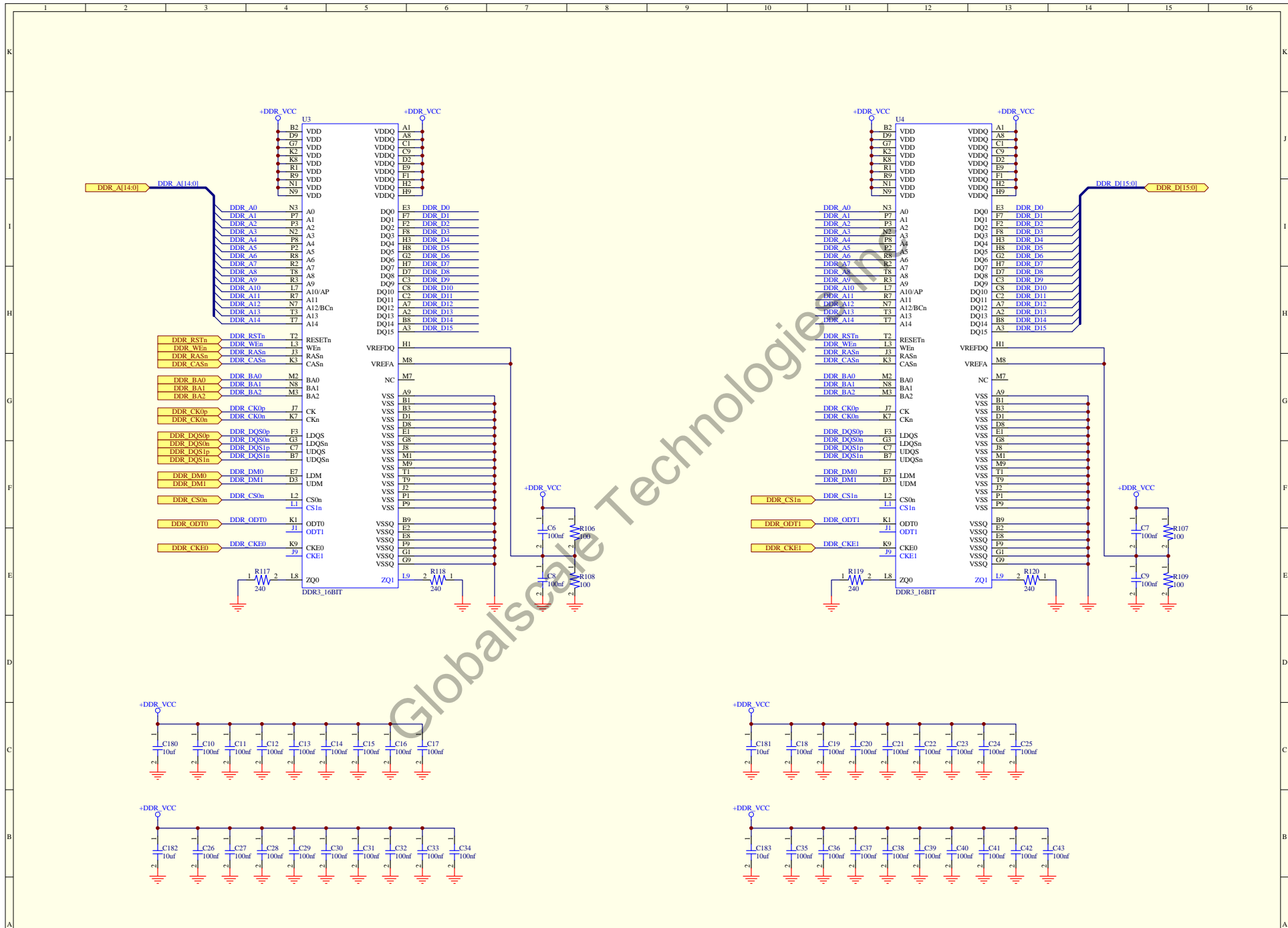
IO

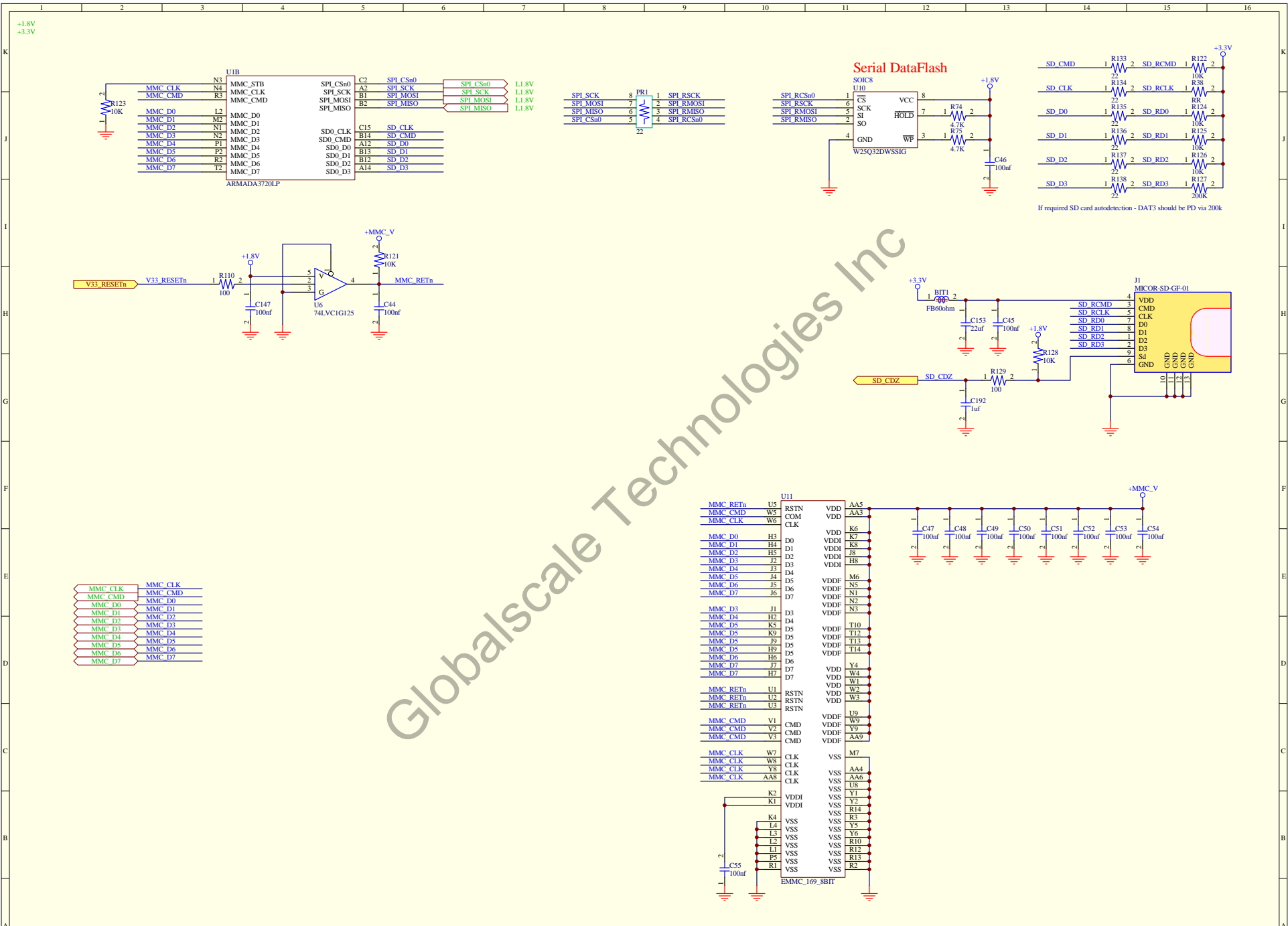
IO.Sch





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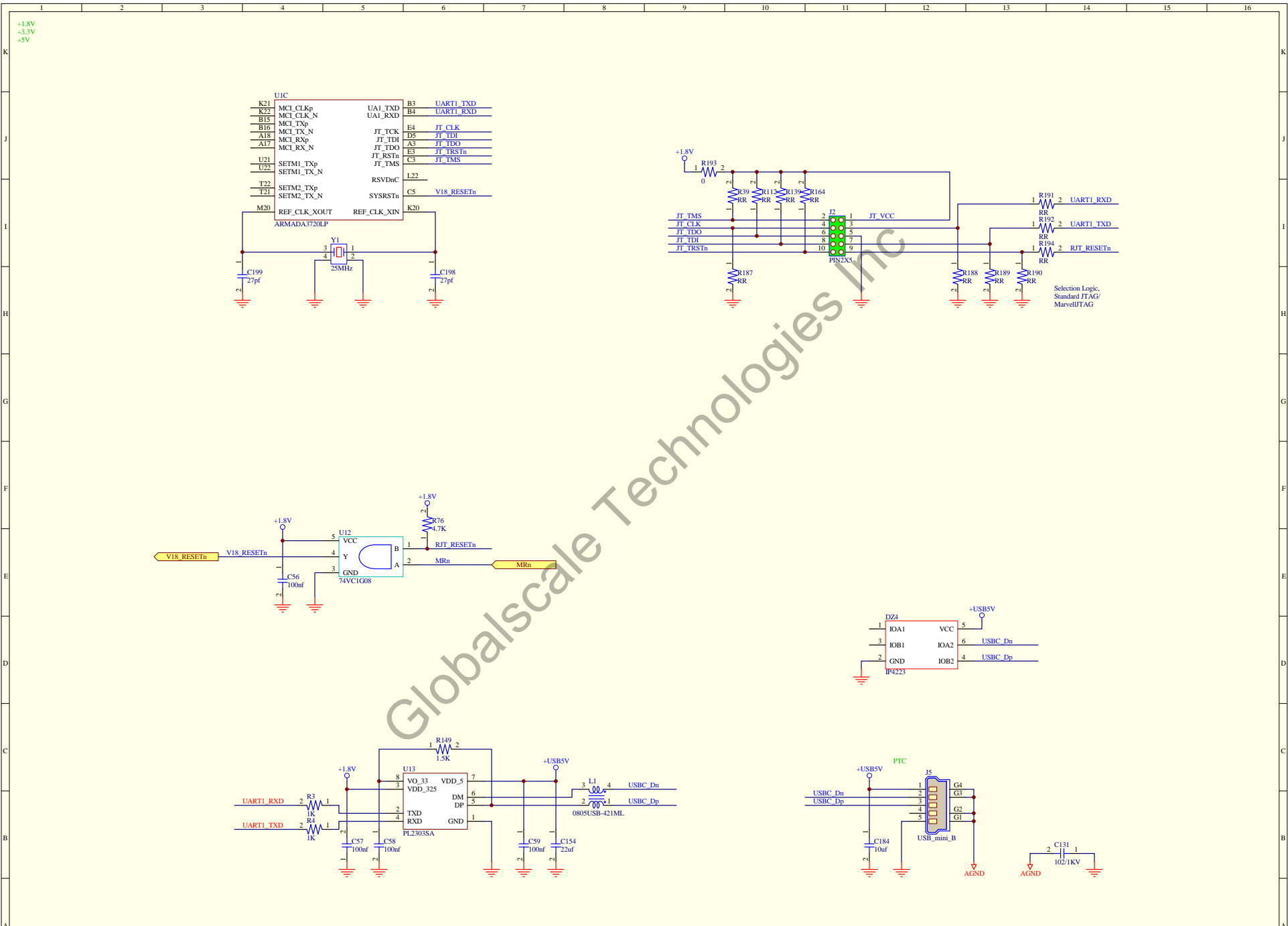


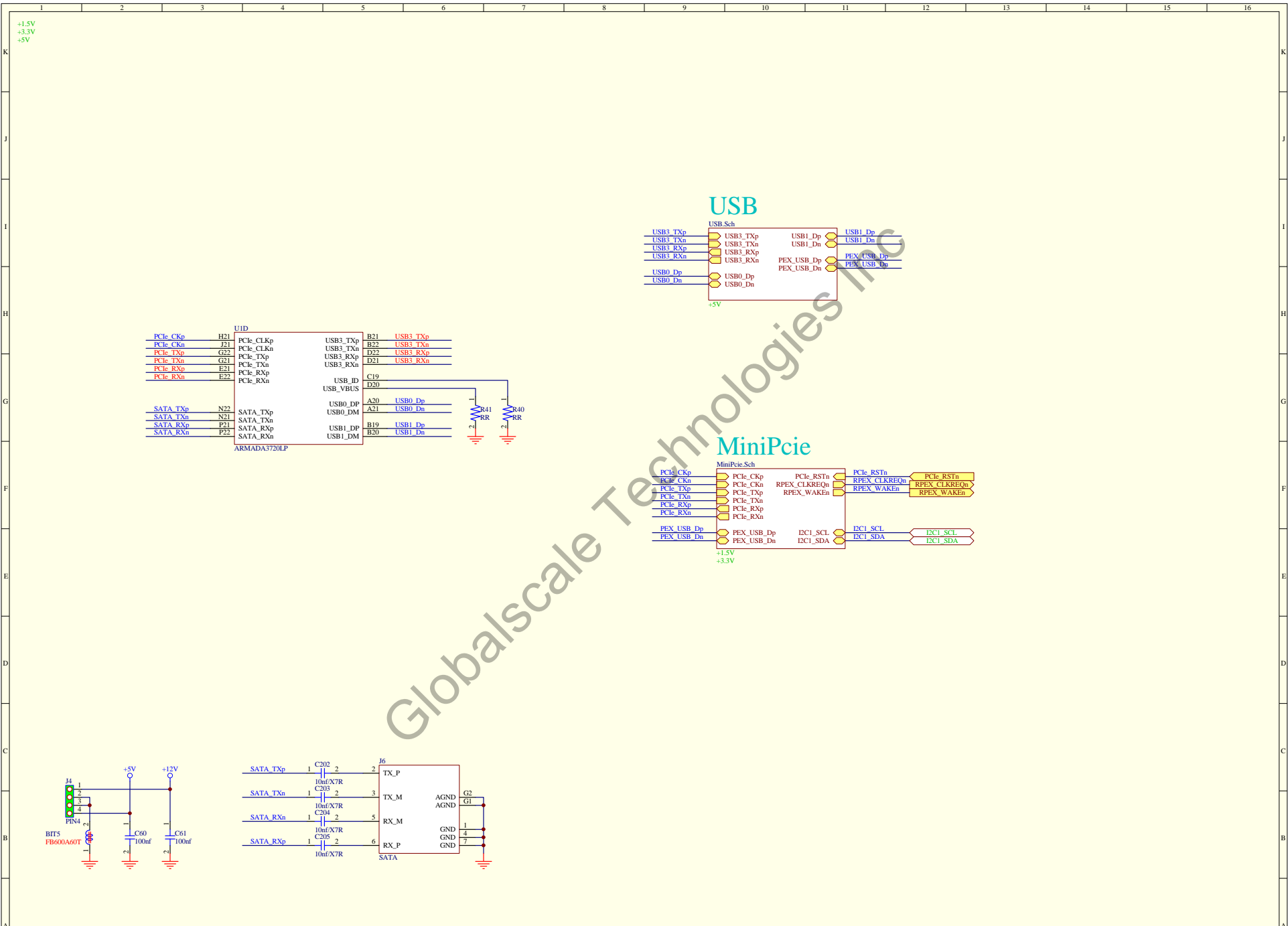


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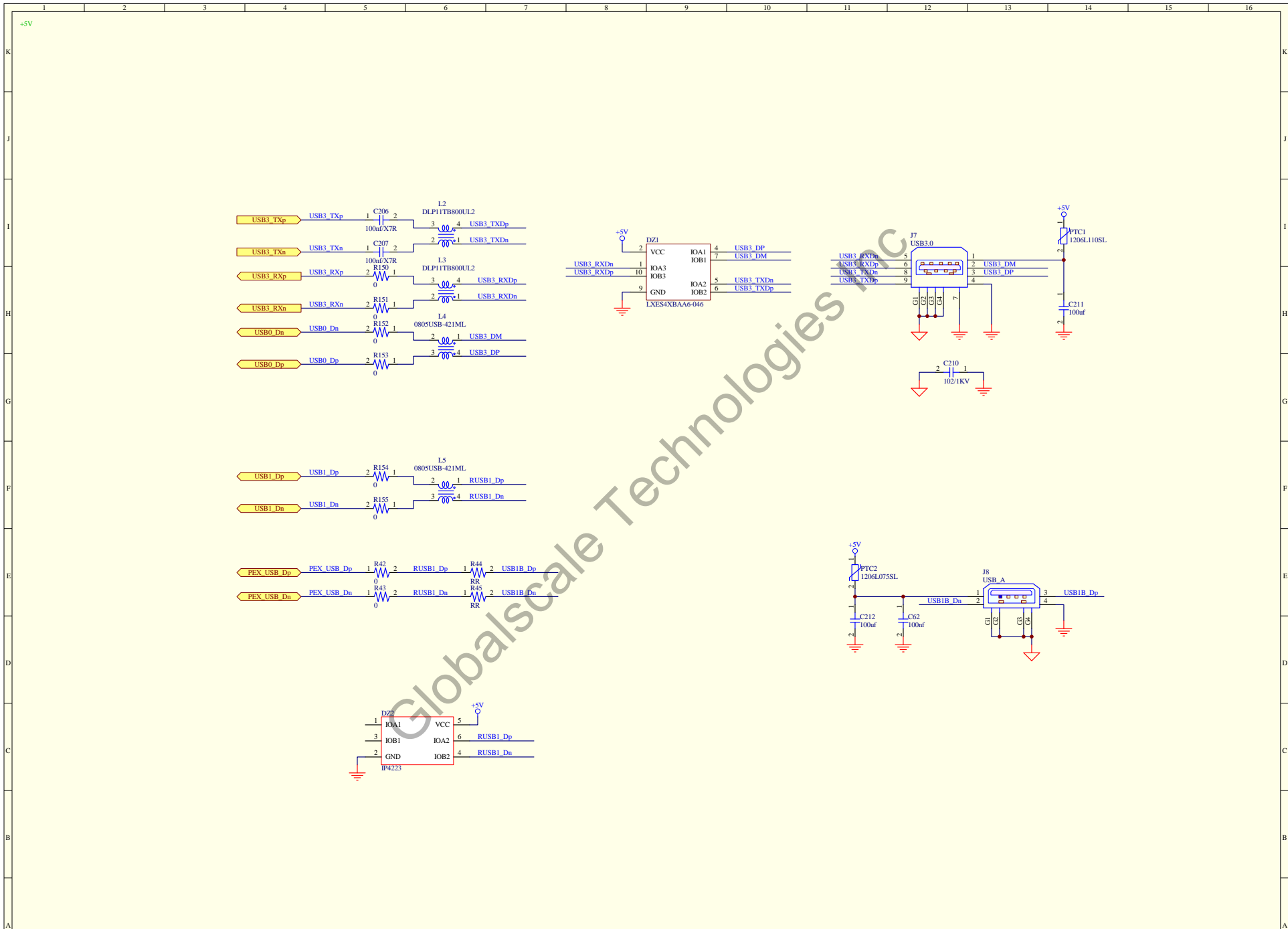
MMC_CLK	MMC_CLK
MMC_CMD	MMC_CMD
MMC_D0	MMC_D0
MMC_D1	MMC_D1
MMC_D2	MMC_D2
MMC_D3	MMC_D3
MMC_D4	MMC_D4
MMC_D5	MMC_D5
MMC_D6	MMC_D6
MMC_D7	MMC_D7

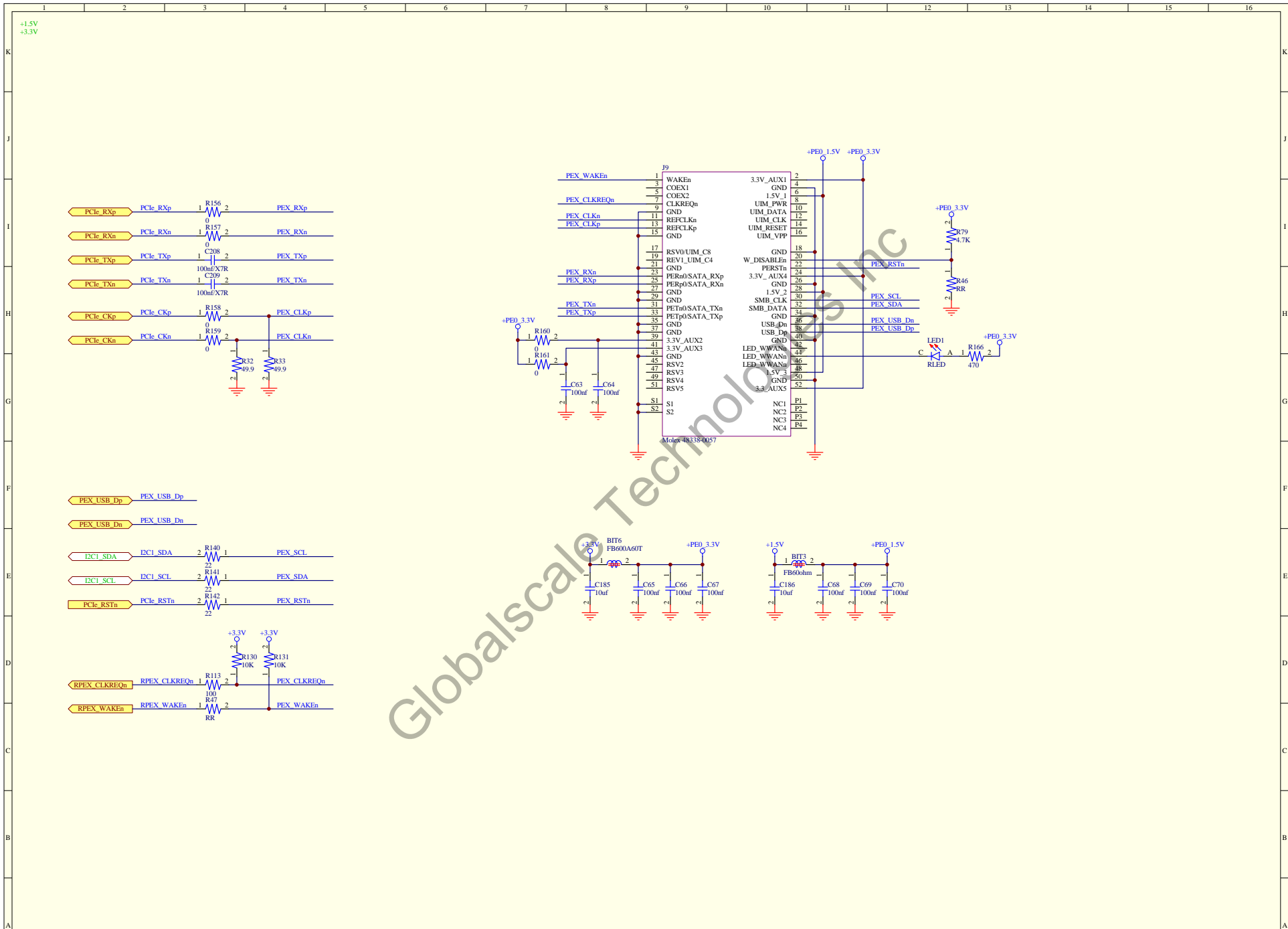
MMC_REStn	U5	RSTN	AA5
MMC_CMD	W5	COM	AA3
MMC_CLK	W6	CLK	AA3
MMC_D0	H3	VDD	K6
MMC_D1	H4	VDDI	K8
MMC_D2	H5	VDDI	J8
MMC_D3	J2	VDDI	H8
MMC_D4	J3	VDDI	H8
MMC_D5	J4	VDDF	M6
MMC_D6	J5	VDDF	N5
MMC_D7	J6	VDDF	N1
MMC_D3	J1	VDDF	N3
MMC_D4	H2	VDDF	T10
MMC_D5	K5	VDDF	T12
MMC_D5	K9	VDDF	T13
MMC_D5	J9	VDDF	T14
MMC_D6	H9	VDDF	T14
MMC_D6	H6	VDD	Y4
MMC_D7	J7	VDD	W4
MMC_D7	H7	VDD	W1
MMC_REStn	U1	VDD	W2
MMC_REStn	U2	VDD	W3
MMC_REStn	U3	VDD	W3
MMC_CMD	V1	VDDF	U9
MMC_CMD	V2	VDDF	Y5
MMC_CMD	V3	VDDF	AA9
MMC_CLK	W7	VSS	M7
MMC_CLK	W8	VSS	AA4
MMC_CLK	Y8	VSS	AA6
MMC_CLK	AA8	VSS	U8
		VSS	Y1
		VSS	Y2
		VSS	R14
		VSS	R3
		VSS	Y5
		VSS	Y6
		VSS	R10
		VSS	R12
		VSS	R13
		VSS	R2



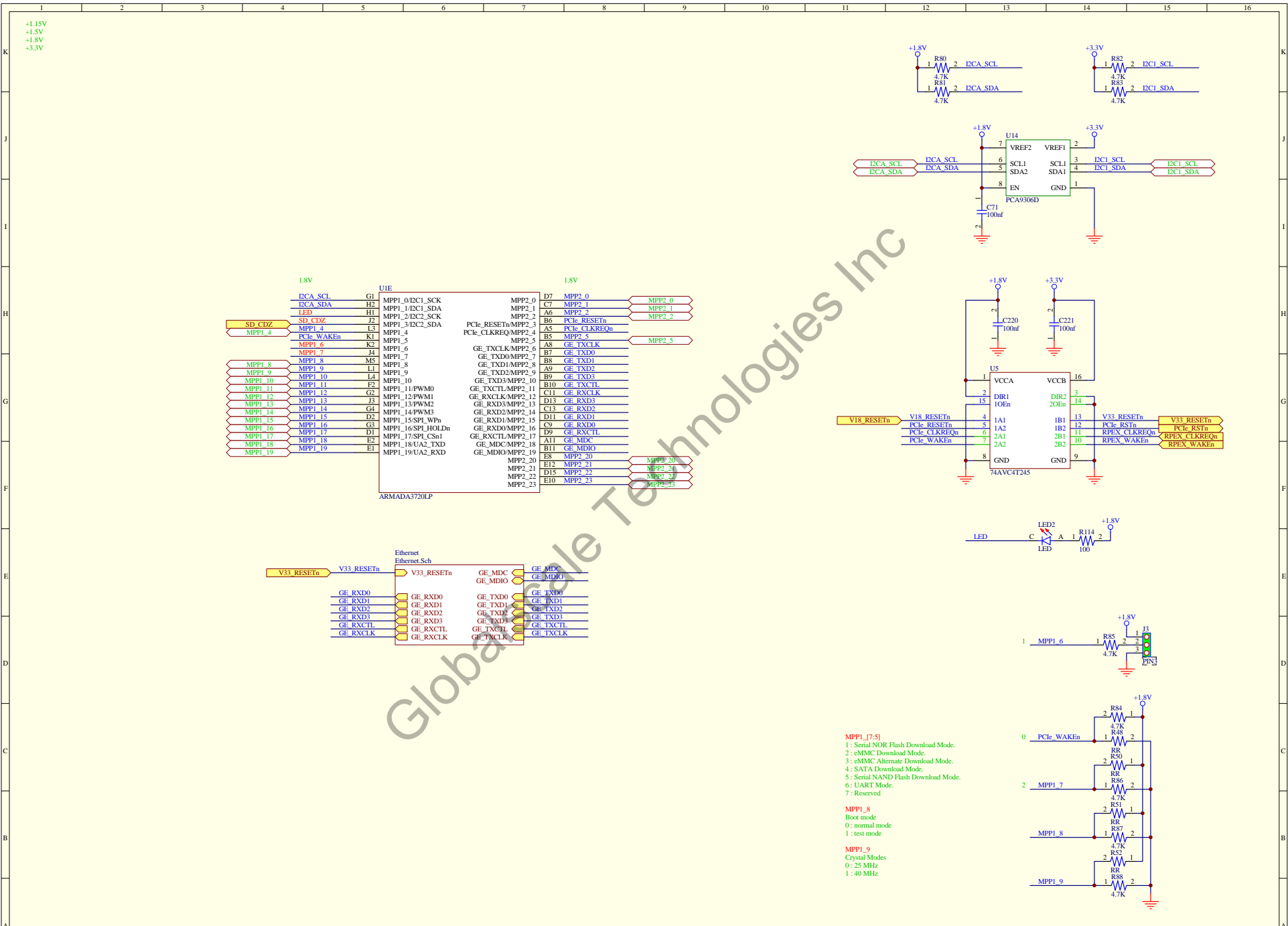


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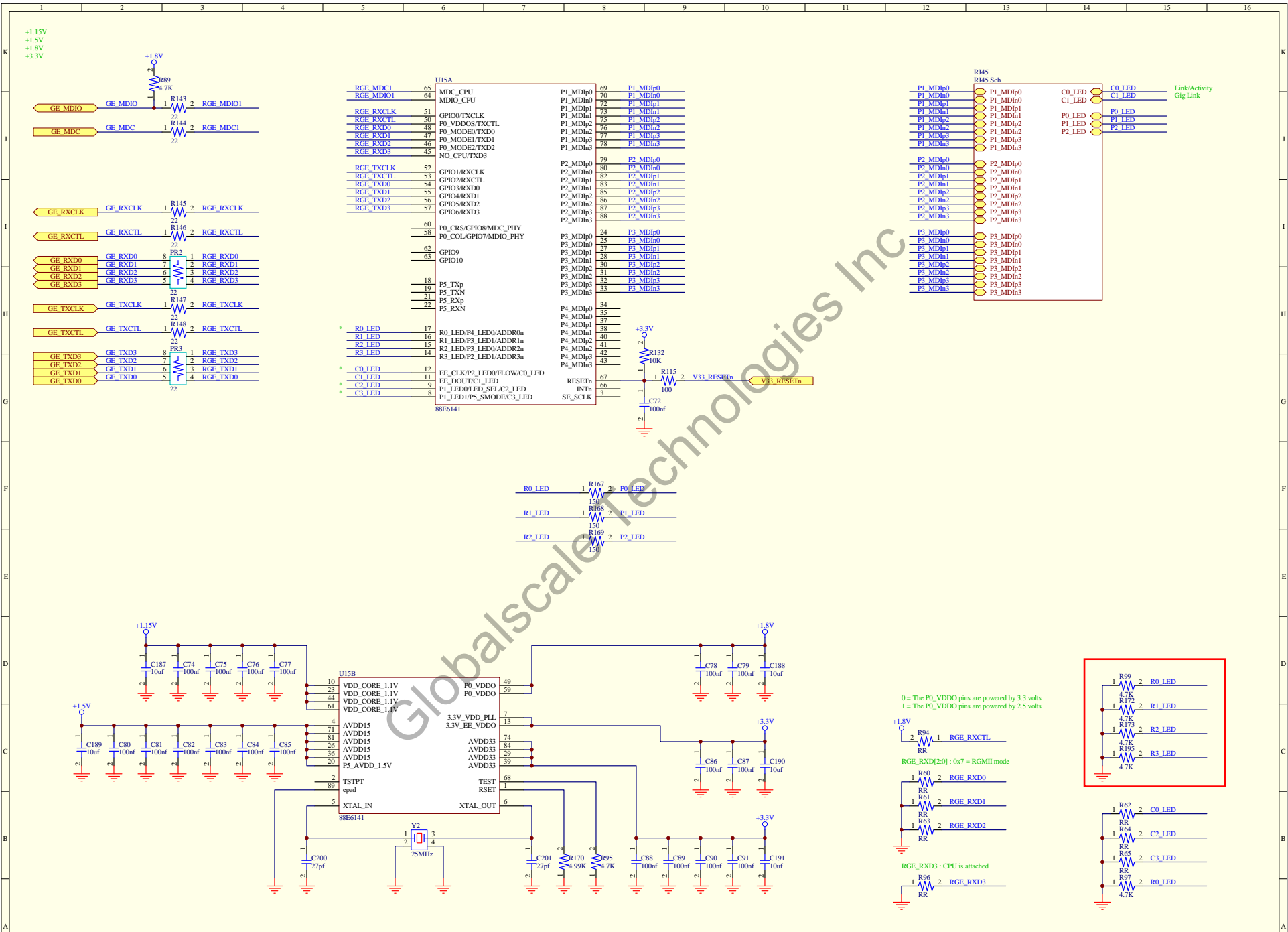
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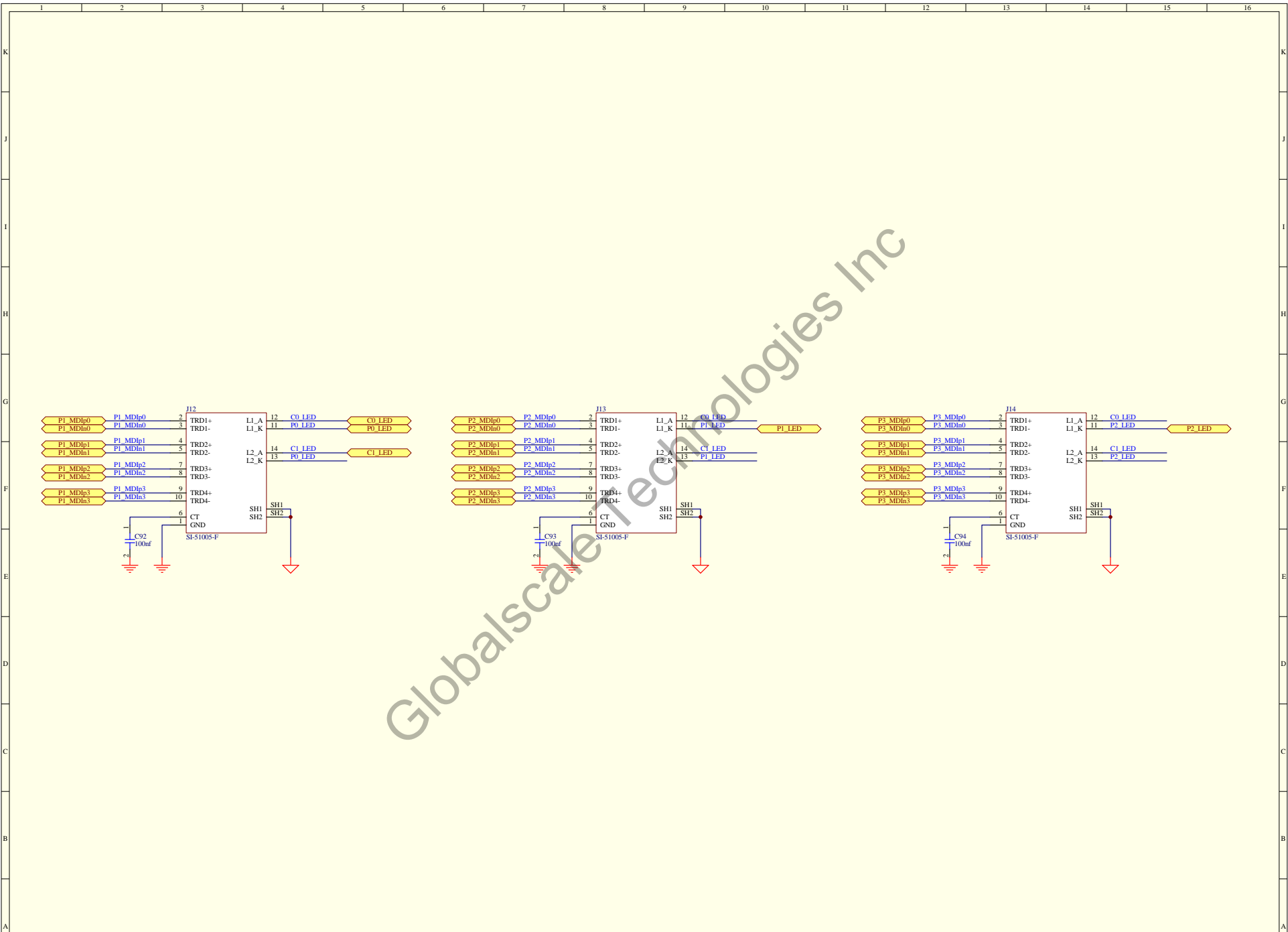


1.8V		UIE		1.8V	
ECA_SCL	G1	MPP1_0/IC1_SCK	D7	MPP2_0	MPP2_0
ECA_SDA	H2	MPP1_1/IC1_SDA	C7	MPP2_1	MPP2_1
LED	H1	MPP1_2/IC2_SCK	A6	MPP2_2	MPP2_2
SD_CDZ	J2	MPP1_3/IC2_SDA	B6	PCIE_RESETn	MPP2_2
MPP1_4	L3	MPP1_4	A5	PCIE_CLKREQn	MPP2_5
PCIE_WAKEEn	K1	MPP1_5	B5	MPP2_5	MPP2_5
MPP1_6	K2	MPP1_6	A8	GE_TXCLK	MPP2_6
MPP1_7	J4	MPP1_7	B7	GE_TXD0	MPP2_7
MPP1_8	M5	MPP1_8	B8	GE_TXD1	MPP2_8
MPP1_9	L1	MPP1_9	A9	GE_TXD2	MPP2_9
MPP1_10	L4	MPP1_10	B9	GE_TXD3	MPP2_10
MPP1_11	F2	MPP1_11	B10	GE_TXCTL	MPP2_11
MPP1_12	G2	MPP1_12/PWM0	C11	GE_RXCLK	MPP2_12
MPP1_13	J3	MPP1_13/PWM1	D13	GE_RXD3	MPP2_13
MPP1_14	G4	MPP1_14/PWM2	C13	GE_RXD2	MPP2_14
MPP1_15	D2	MPP1_15/PWM3	D11	GE_RXD1	MPP2_15
MPP1_16	G3	MPP1_16/SPI_WpA	C9	GE_RXD0	MPP2_16
MPP1_17	D1	MPP1_17/SPI_CSn1	D9	GE_RXCTL	MPP2_17
MPP1_18	E2	MPP1_18/UA2_TXD	A11	GE_MDC	MPP2_19
MPP1_19	E1	MPP1_19/UA2_RXD	B11	GE_MDIO	MPP2_20
			E8	MPP2_20	MPP2_20
			E12	MPP2_21	MPP2_21
			D15	MPP2_22	MPP2_22
			E10	MPP2_23	MPP2_23

Ethernet Sch		Ethernet Sch	
V33_RESETn	V33_RESETn	GE_MDC	GE_MDC
		GE_MDIO	GE_MDIO
GE_RXD0	GE_RXD0	GE_TXD0	GE_TXD0
GE_RXD1	GE_RXD1	GE_TXD1	GE_TXD1
GE_RXD2	GE_RXD2	GE_TXD2	GE_TXD2
GE_RXD3	GE_RXD3	GE_TXD3	GE_TXD3
GE_RXCTL	GE_RXCTL	GE_TXCTL	GE_TXCTL
GE_RXCLK	GE_RXCLK	GE_TXCLK	GE_TXCLK

- MPP1_7[5]**
- Serial NOR Flash Download Mode.
 - eMMC Download Mode.
 - eMMC Alternate Download Mode.
 - SATA Download Mode.
 - Serial NAND Flash Download Mode.
 - UART Mode.
 - Reserved.
- MPP1_8**
- Boot mode
- test mode
- MPP1_9**
- Crystal Modes
- 0: 25 MHz
 - 1: 40 MHz





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